

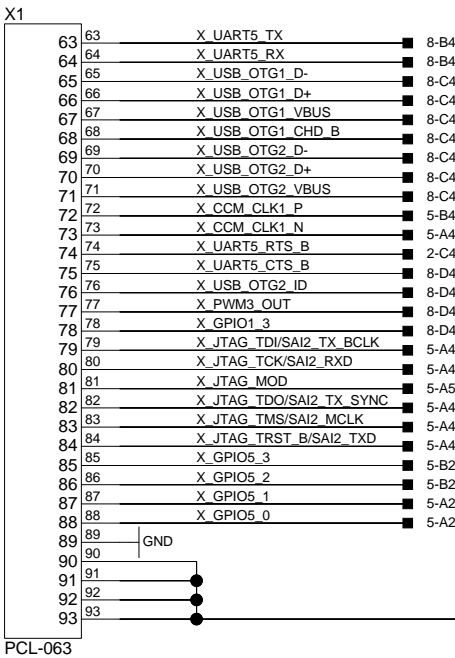
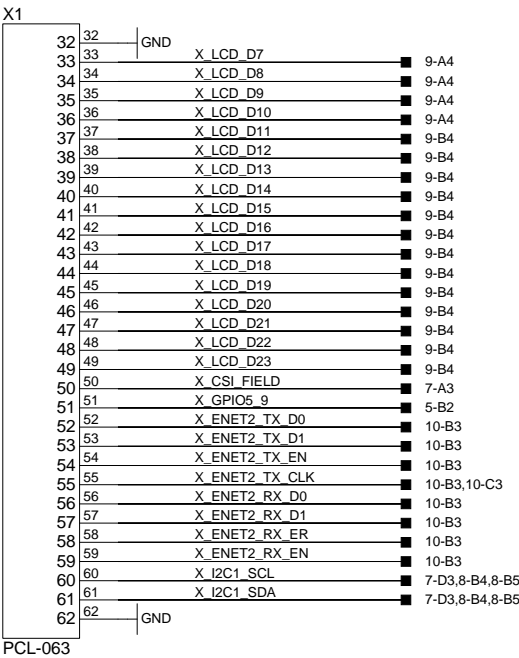
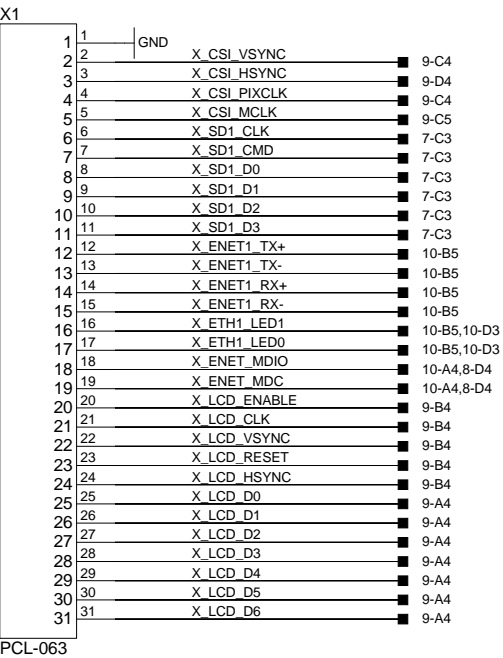
PCL-063
phyCORE-i.MX 6UL

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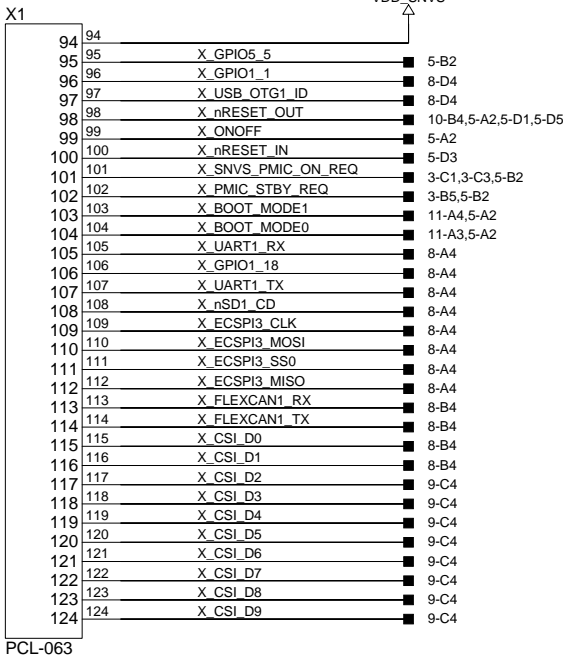
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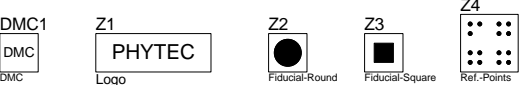
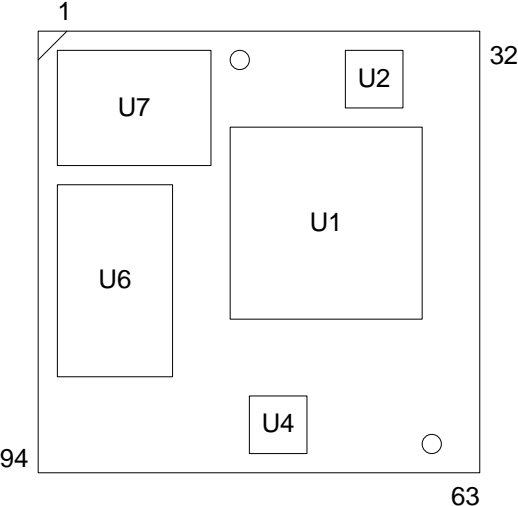
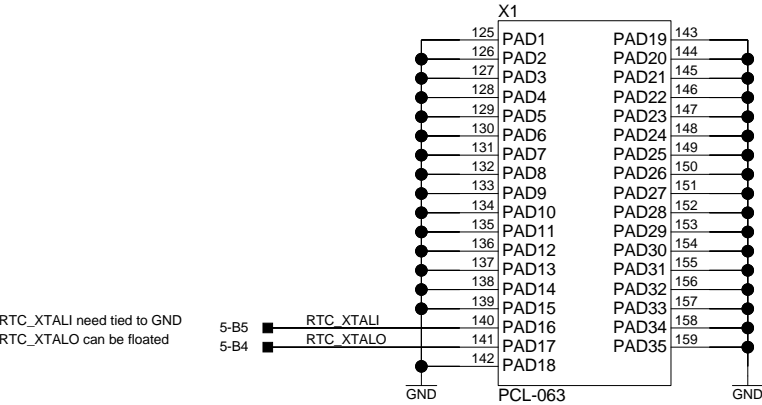
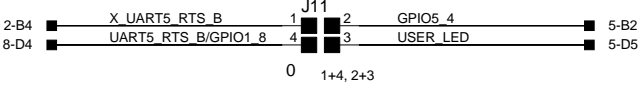
Connector



! Caution !
RTS is an input, usually used as CTS
CTS is an output, usually used as RTS

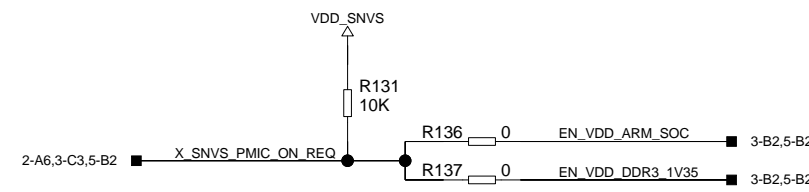
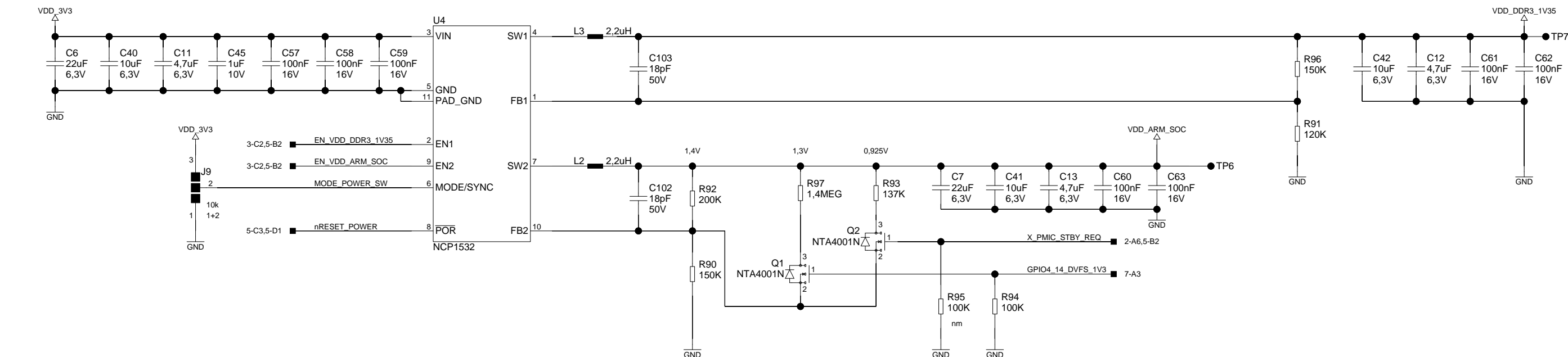


Pads underneath

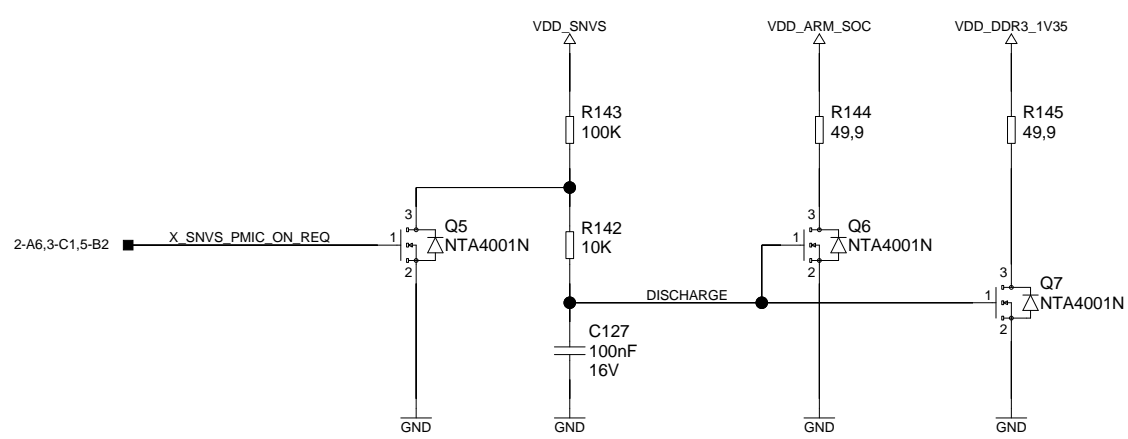
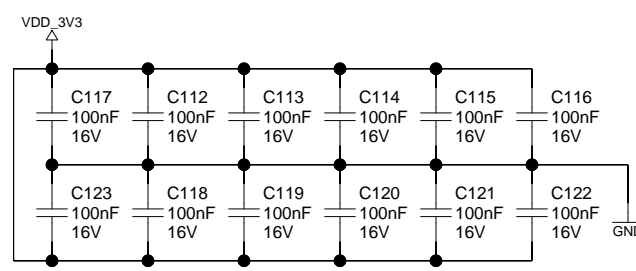


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Power Supply

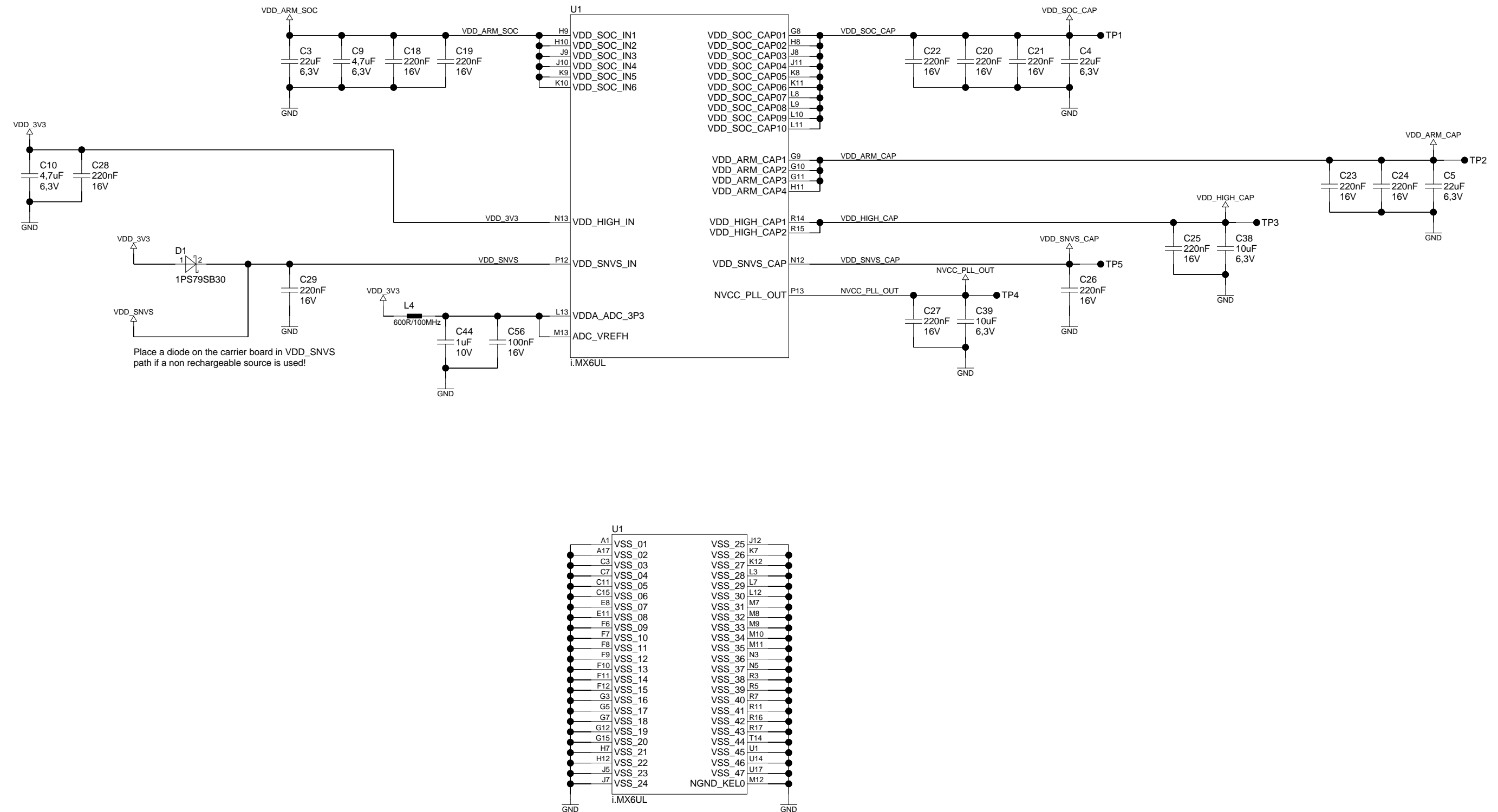


EMC



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iMX6UL - Power | GND



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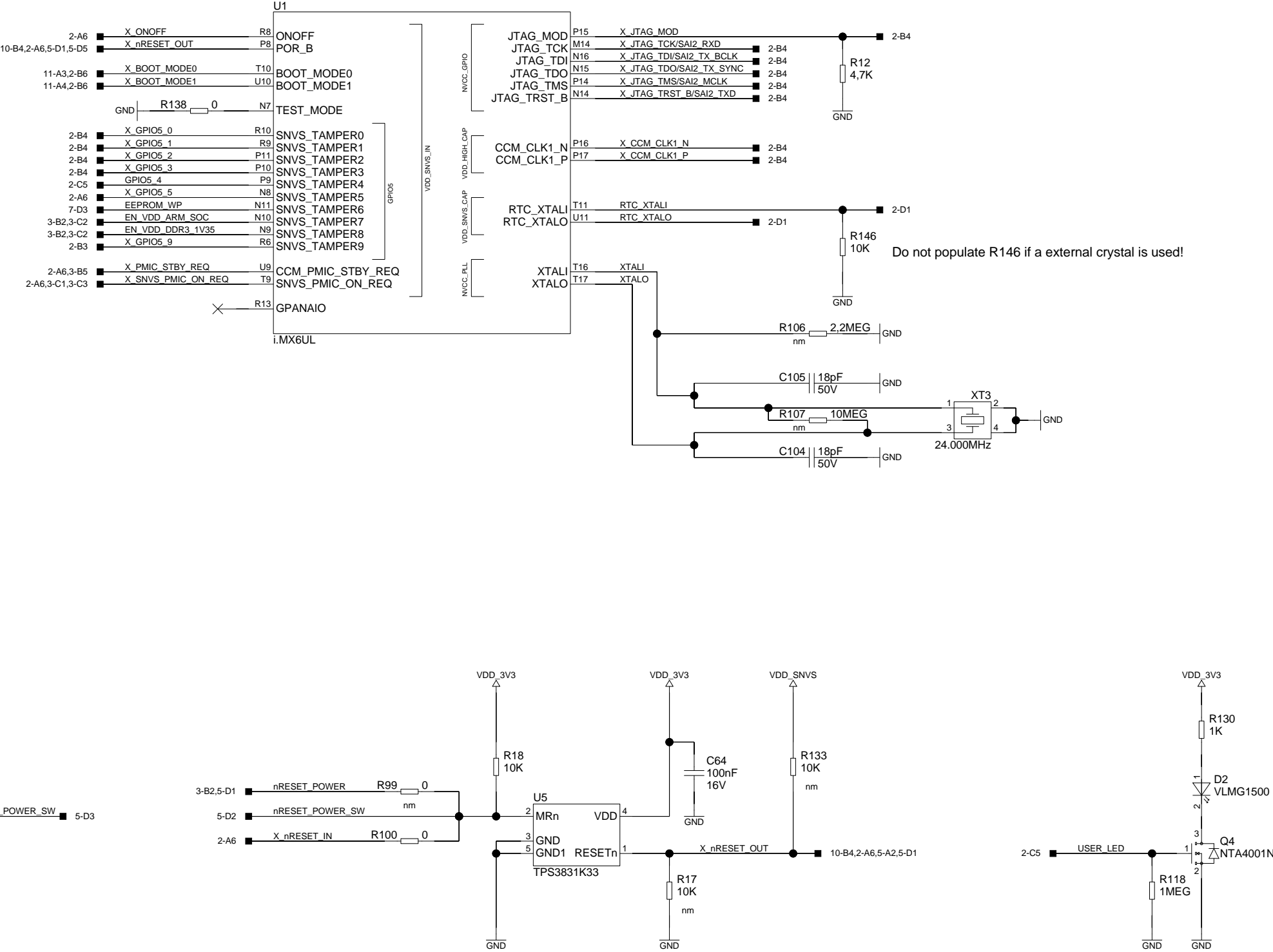
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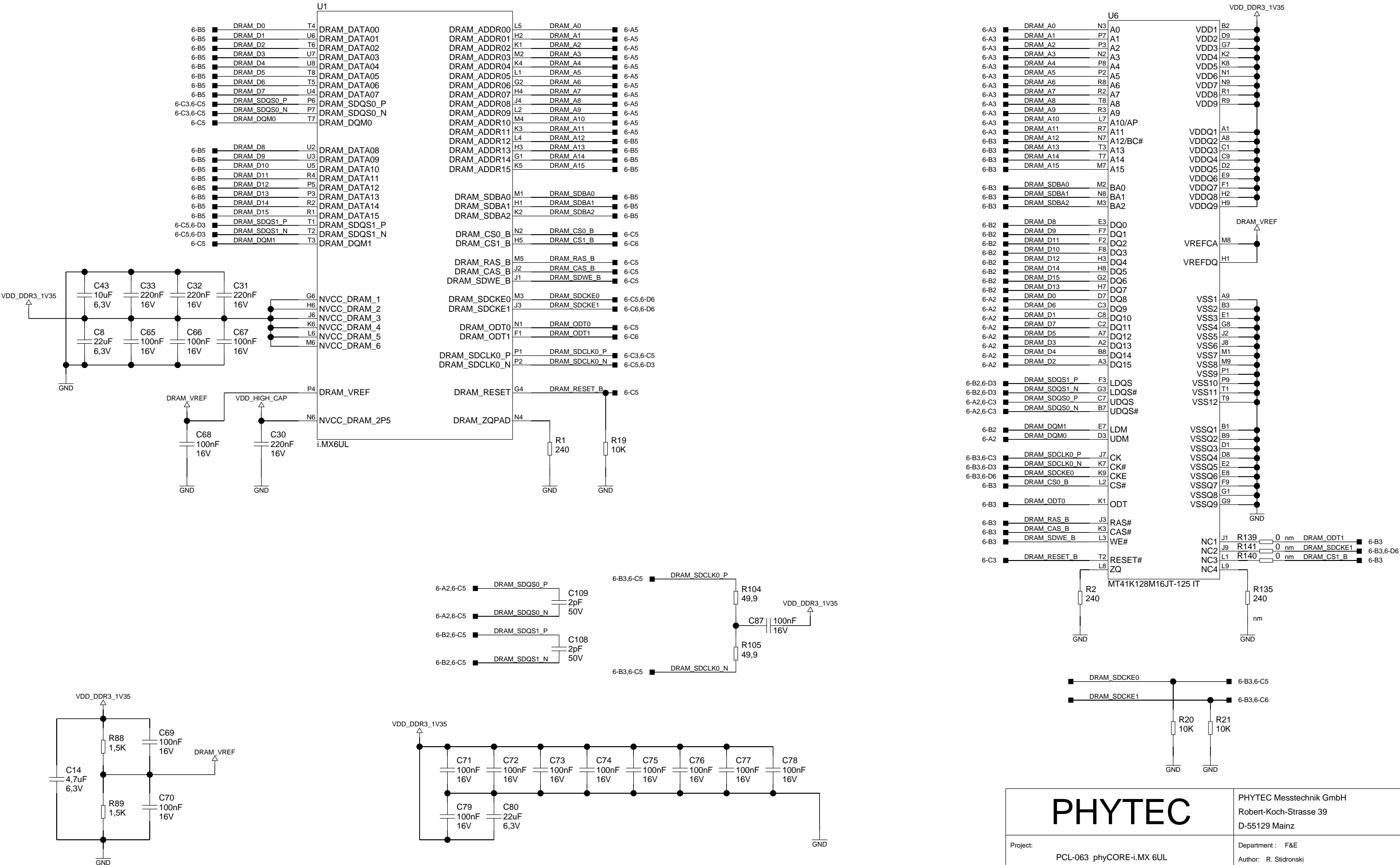
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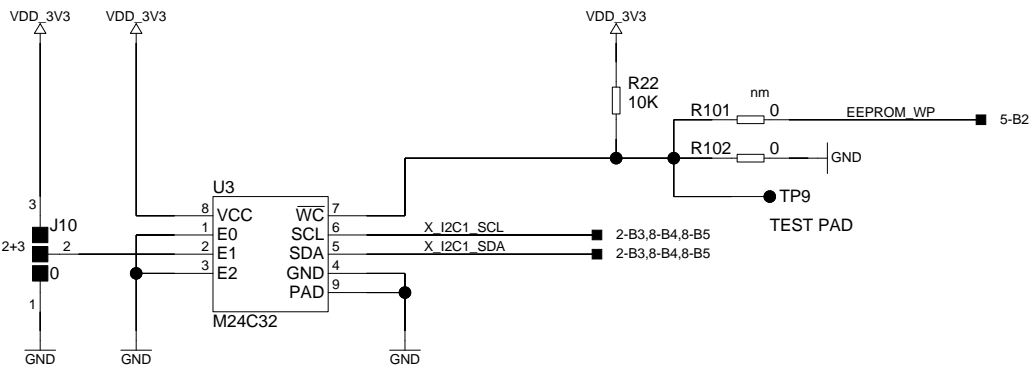
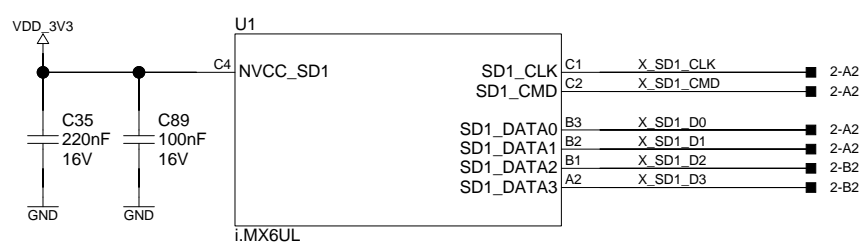
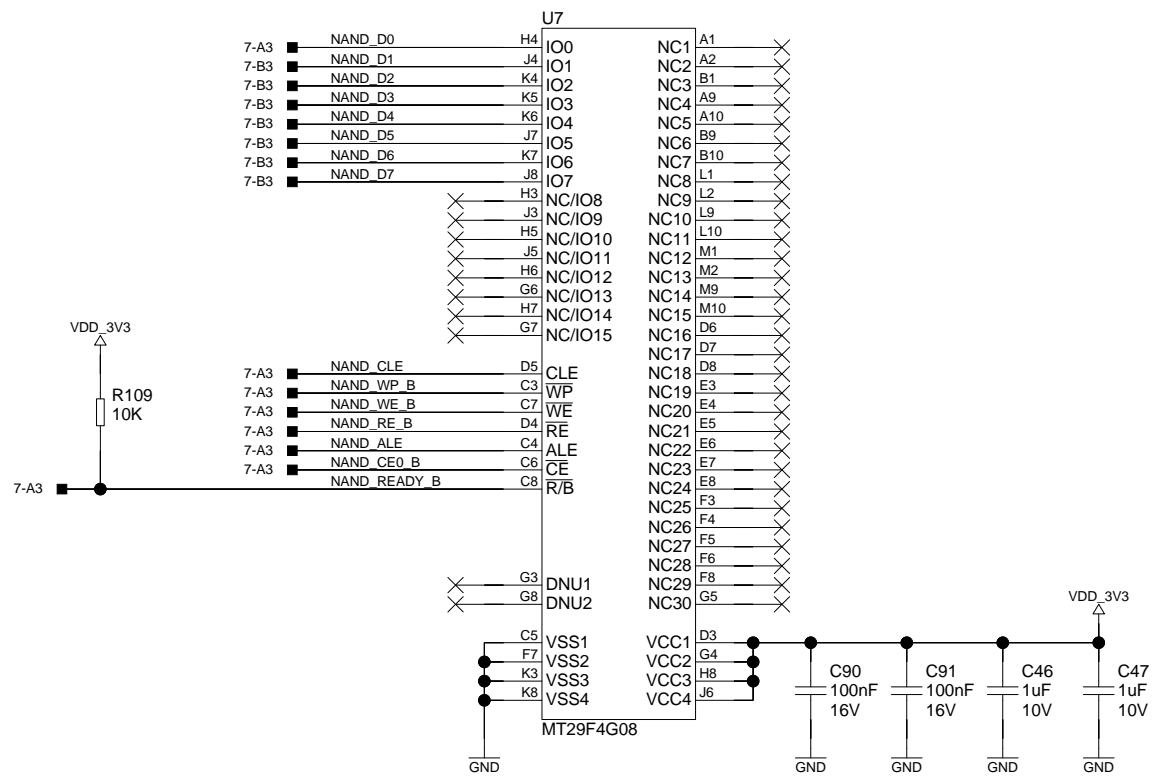
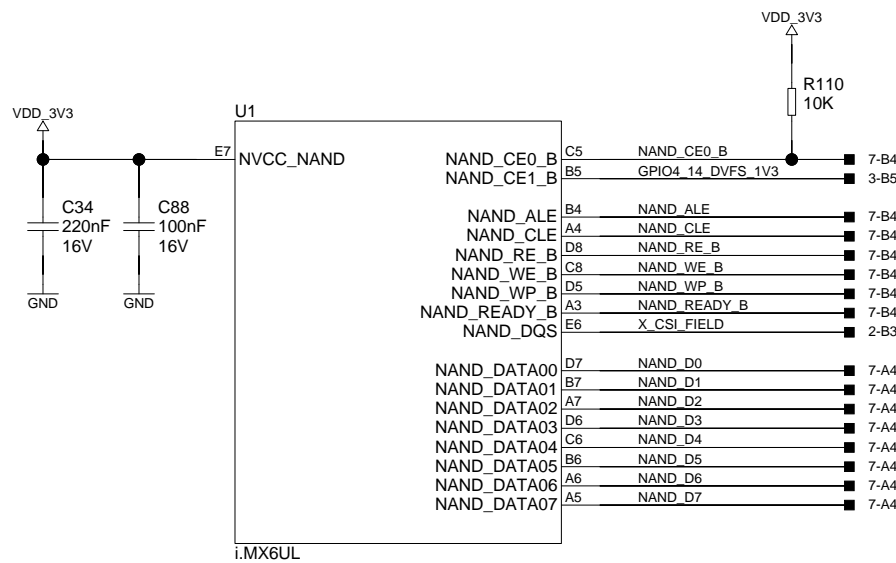
iMX6UL - Control



iMX6UL - DDR3



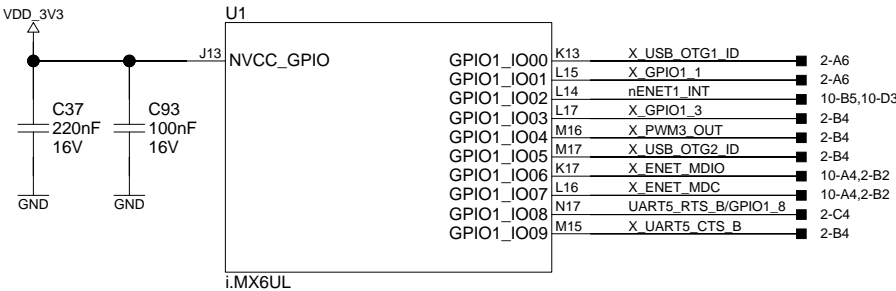
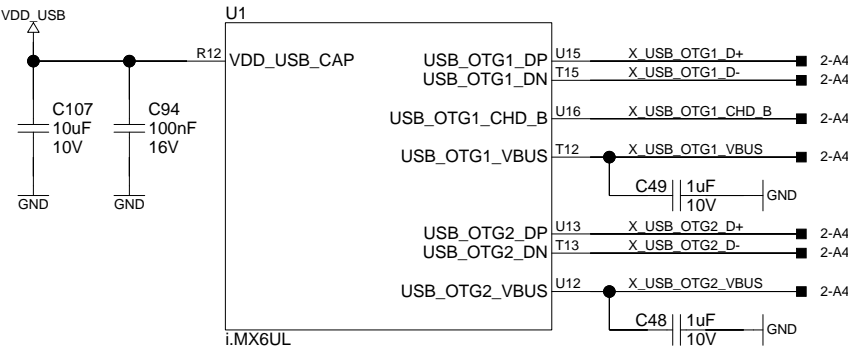
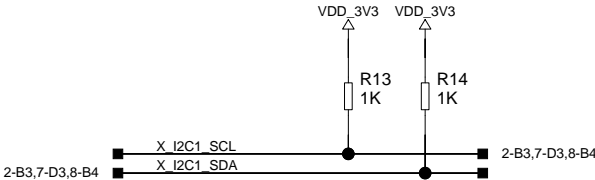
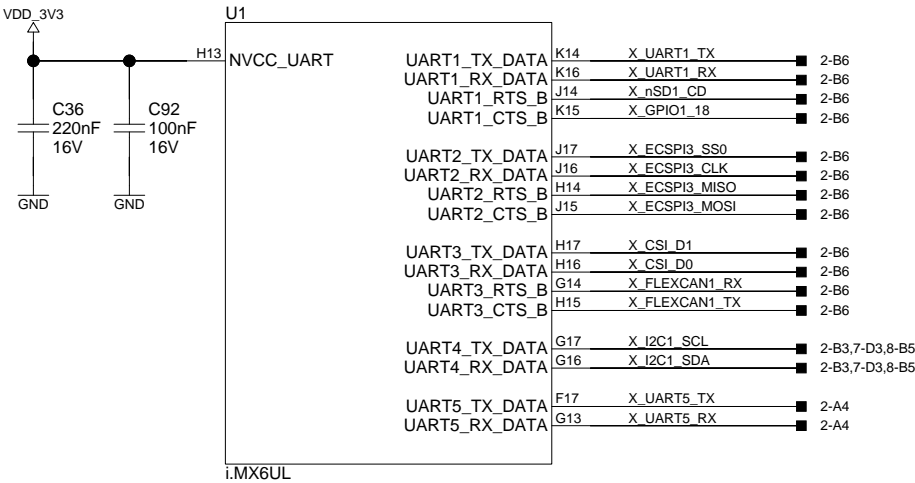
iMX6UL - NAND | SD | EEPROM



J10:	1+2	default 2+3
Adress Memory Area:	0x50	0x52
Adress ID page:	0x58	0x5A

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iMX6UL - UART | USB | GPIO



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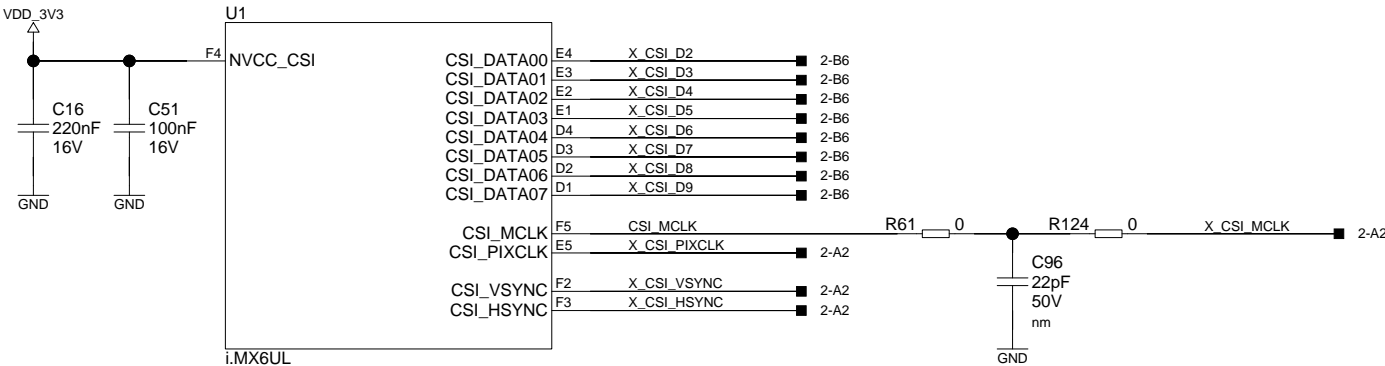
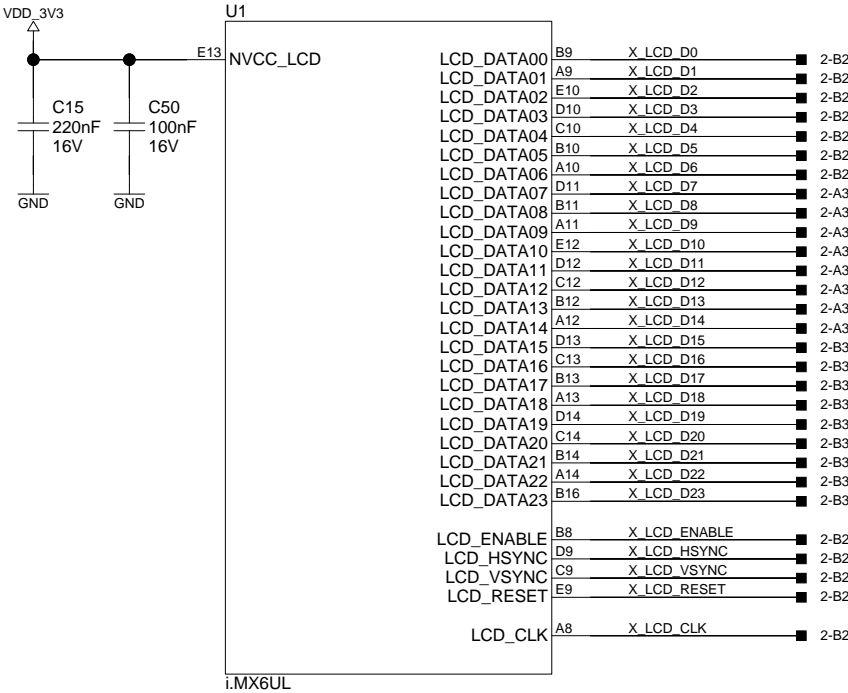
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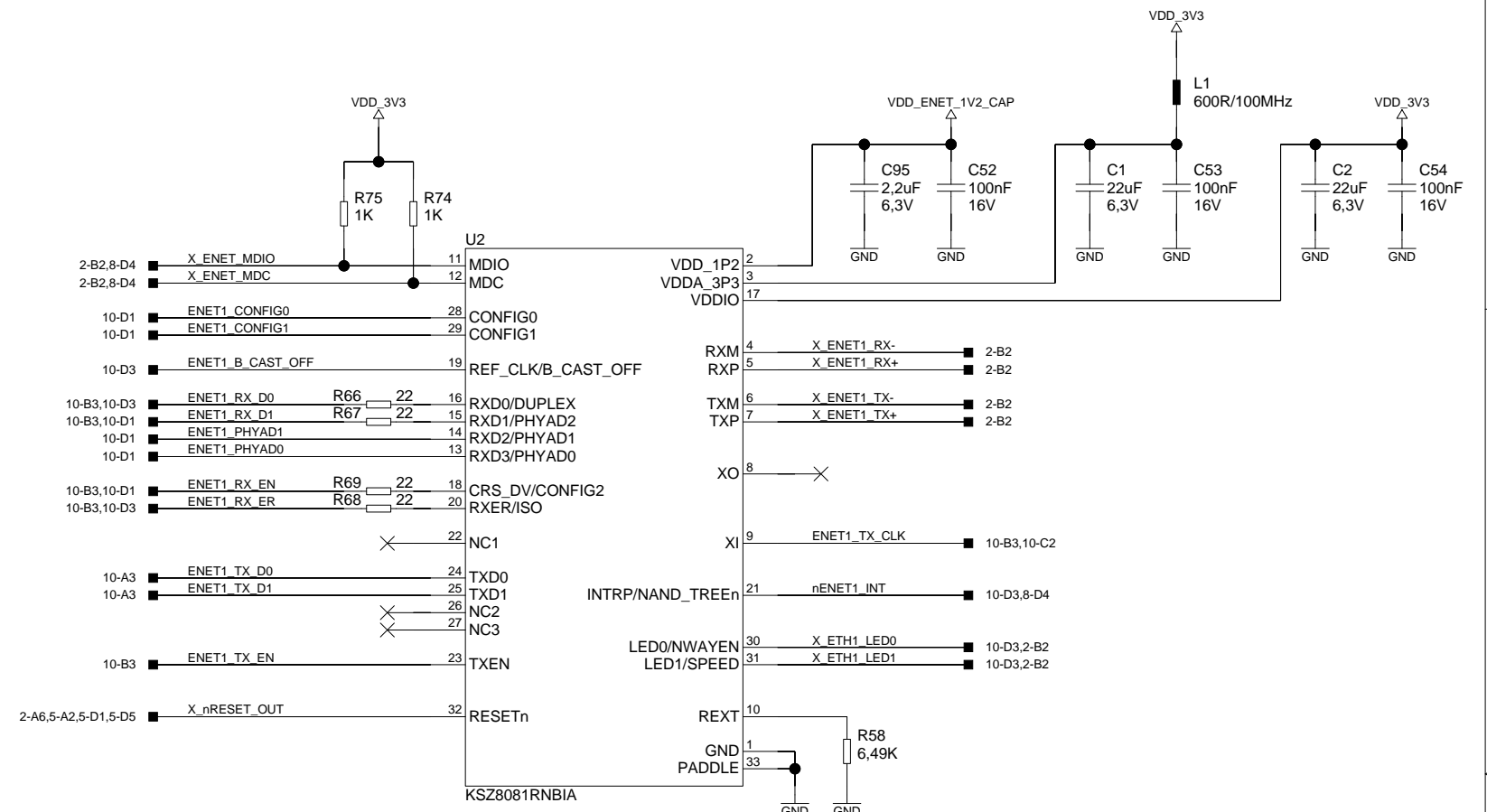
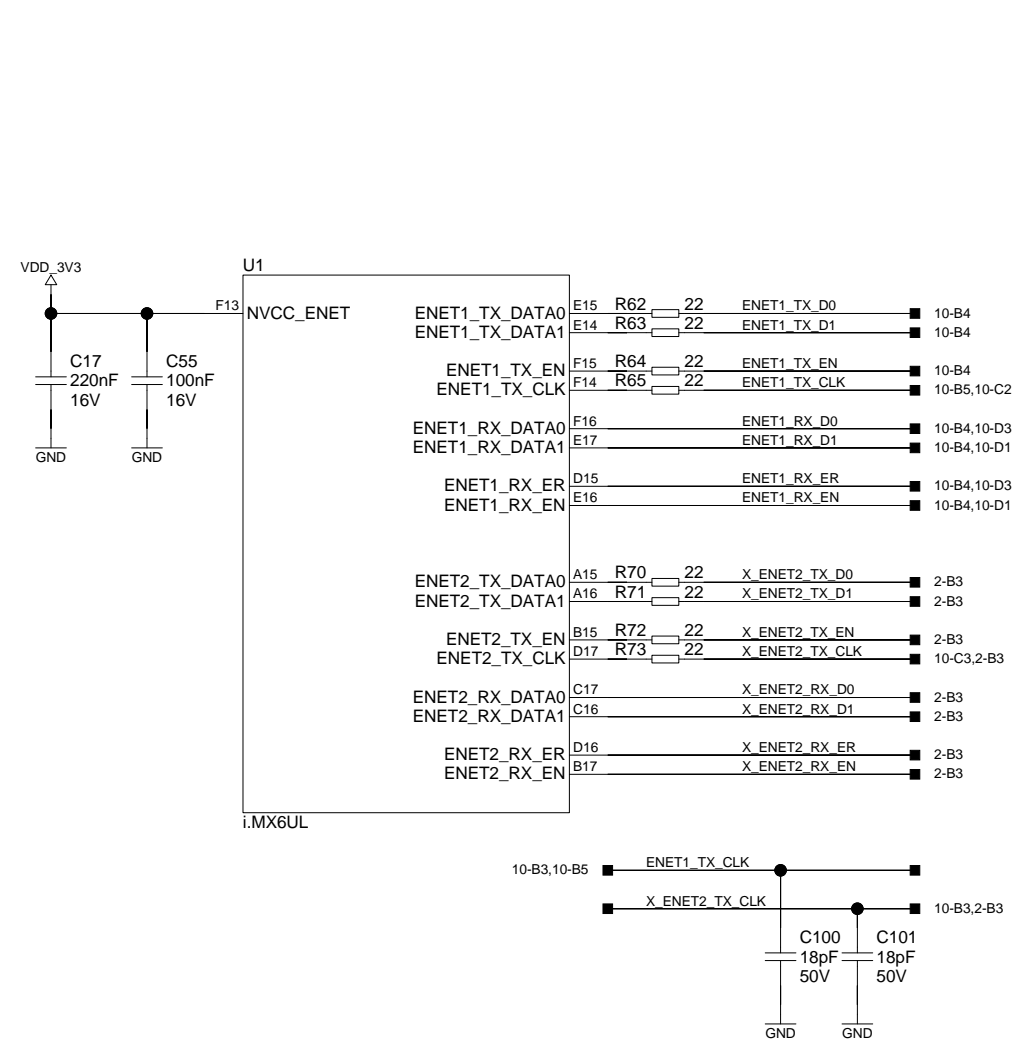
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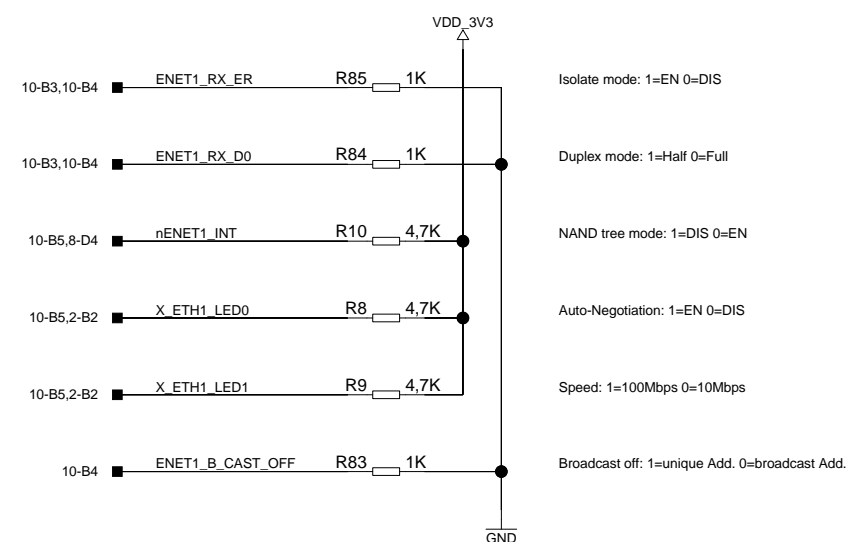
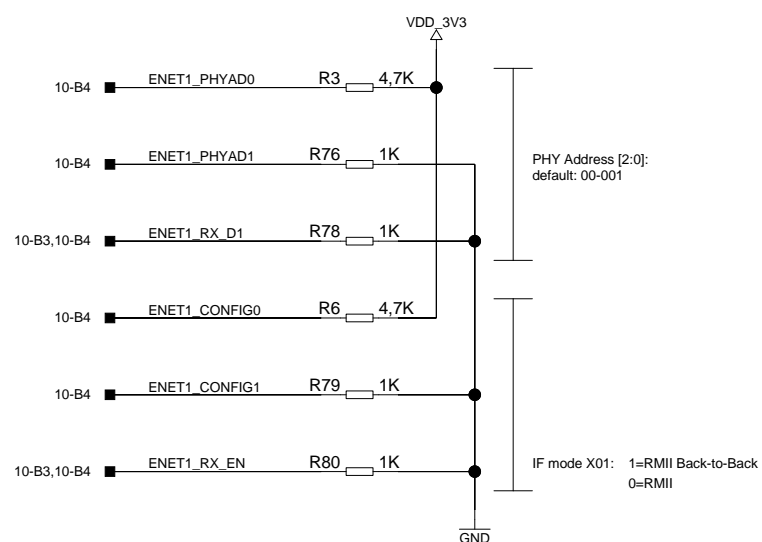
iMX6UL - LCD | CSI



iMX6UL - Ethernet



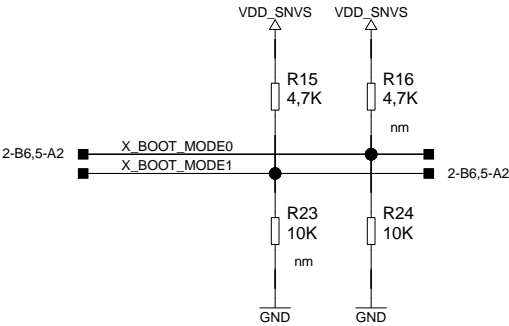
ENET1 Configuration



<h1>PHYTEC</h1>		PHYTEC Messtechnik GmbH Robert-Koch-Strasse 39 D-55129 Mainz	
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Boot Configuration

BOOT_MODE[1:0]	Boot Type
00	Boot From Fuses
01	Serial Downloader
10 (Default)	Internal Boot
11	Reserved



Boot configuration jumper needs to be placed on the carrier board

	BOOTCFG1[7:0] XXXXXXXX	BOOTCFG2[7:0] 00X000X0	BOOTCFG4[7:0] 00000000
NAND 1Gb (64 pages per block, 4 address bytes)	10010010	00000000	00000000
NAND 4Gb (64 pages per block, 5 address bytes)	10010011	00000000	00000000
SD-Card	01000010	00100000	00000000