Tyler Elvis, 4/2/24, ENRG 108

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Final Project

```
clc, clear
format short, format compact
```

Objectives

```
% Find the voltage and current through each component in the system.
% Use the current loop directions shown in the schematic.
% Check to determine if the resistor R5 will fail.
% Display the results using appropriate Matlab display commands.
```

Code

```
% all values of the resistors
R1 = 100;
R2 = 500;
R3 = 500;
R4 = 750;
R5 = 500;
R6 = 200;
R7 = 1000;
R8 = 250;
R9 = 1000;
R10 = 750;
R11 = 500;
R12 = 500;
R13 = 500;
R14 = 500;
% all values of the ERG source in volts
V1 = 9;
V2 = 5;
V3 = 15;
% Values of all ending terms of the equations
    V = [0; V1; V1; 0; 0; V2; V3];
    % All Ressiors * current loop matrices
    Rcoef = [(R1+R3+R2), -R3, -R2, 0, 0, 0;
```

```
-R2,
                 (R2+R4+R9),-R4, -R9, 0, 0, 0;
              -R3, -R5, (R3+R5+R7), -R7, 0, 0, 0;
         -R7, -R8, -R6, (R7+R8+R6), 0, 0, 0;
                -R9, -R10, -R6, 0, (R9+R10+R6), 0, 0;
              -R4, -R10, 0, 0, (R1+R4+R10+R13), 0;
                -R8, -R5, 0, 0, 0, (R12+R14+R8+R5)];
% left div to find all I values of each loop
I = Rcoef \V;
% Takes each of the answers from I and turns into own variable
    I1 = I(1);
    I2 = I(2);
    I3 = I(3);
   I4 = I(4);
   I5 = I(5);
    I6 = I(6);
    I7 = I(7);
% outside loop check
% the equatiosn of R * I values
Check = (R11+R13), (R12+R14), R1, 0, 0, 0;
 % outside loop check end terms of equations
 V Check = [V2+V3];
 % uese left div to find the total value
   Check = round(Check \V Check);
   % if else statment for the value above, so if its below or equal to
    % zero is very good but if its above zero its bad, if its above but
    % cloes to zero it still checks out.
            if Check <=0</pre>
                fprintf('The Voltage going through the outside loop is %4.2f
and checks out \n', Check);
            else
                fprintf('The Votage going through the outside loop is %4.2f
and does not check out', Check);
            end
format bank
% Currents going through ressistor
   C R1 = I1;
   C R2 = I1 - I2;
   C R3 = I1 - I3;
    C R4 = I2 - I4;
    C R5 = I3 - I5;
   C R6 = I4 - I5 - I6;
   C R7 = I3 - I4 - I7;
   C R8 = I4 - I7;
   C R9 = I2 - I5;
    C R10 = I5 - I6;
```

```
C R11 = I6;
    C R12 = I7;
    C R13 = I6;
    C R14 = I7;
       % Voltage running through ressistor
       V R1 = R1 * C R1;
       V R2 = R2 * C R2;
       V R3 = R3 * C R3;
       V R4 = R4 * C R4;
       V R5 = R5 * C R5;
       V R6 = R6 * C R6;
       V R7 = R7 * C R7;
       V R8 = R8 * C R8;
       V R9 = R9 * C R9;
       V R10 = R10 * C R10;
       V R11 = R11 * C R11;
       V R12 = R12 * C R12;
       V R13 = R13 * C R13;
       V R14 = R14 * C R14;
              % Ressistor Check
              if C R5 <10.0
                  fprintf("\n \n R5 meets the requirment \n \n")
                  fprintf("\n \n R5 does not meet requirement \n \n")
              end
    % getting all of the data nice for table
    Ressistor = ["R1"; "R2"; "R3"; "R4"; "R5"; "R6"; "R7"; "R8"; "R9";
"R10"; "R11"; "R12"; "R13"; "R14";];
    Ohms = [R1; R2; R3; R4; R5; R6; R7; R8; R9; R10; R11; R12; R13; R14;];
    Votlage = [C R1; C R2; C R3; C R4; C R5; C R6; C R7; C R8; C R9; C R10;
C R11; C R12; C R13; C R14;];
    Current = [ V R1; V R2; V R3; V R4; V R5; V R6; V R7; V R8;
V R9; V R10; V R11; V R12; V R13; V R14;];
circuit analysis = table(Ressistor, Ohms, Votlage, Current)
Check =
        1000
ans =
        1000
R1 =
   100
ans =
ans =
     0
ans =
The Voltage going through the outside loop is 0.00 and checks out
```

R5 meets the requirment

circuit_analysis =
 14×4 table

Ressistor	Ohms	Votlage	Current
	100.00	0.08	7.57
"R2"	500.00	-0.01	-3.78
"R3"	500.00	-0.01	-3.78
"R4"	750.00	0.01	3.91
"R5"	500.00	0.00	1.94
"R6"	200.00	-0.06	-12.09
"R7"	1000.00	-0.04	-37.94
"R8"	250.00	0.03	8.71
"R9"	1000.00	0.00	3.88
"R10"	750.00	0.02	15.16
"R11"	500.00	0.06	29.56
"R12"	500.00	0.04	21.58
"R13"	500.00	0.06	29.56
"R14"	500.00	0.04	21.58

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