

**Electrical Engineering & Computing Sciences
Computing Security**

CSEC 202 Reverse Engineering Fundamentals

Spring 2024 | Sections: 600, 601, and 602

Final Exam

Student Name		Duration	2 hours
Student ID		Section	Exam Date
			11/05/2024

Questions	CLO	Marks	Score
Q1	CLO 1, 2	5	
Q2	CLO 1, 2	5	
Q3	CLO 1, 2	5	
Q4	CLO 1, 2	6	
Q5	CLO 3	3	
Total		24	

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Instructions

1. Answer all questions.
2. No notes or textbooks are permitted in the examination hall.
3. Use your time carefully. If you feel you are stuck, move on to the next question.
4. Answer questions in the space provided. You may use the back of a page if the space for a question is insufficient.
5. Please hand in the complete questionnaire at the end of the examination.
6. **IMPORTANT:** All cell phones, electronic devices, smartwatches, and earbuds are prohibited. They must be switched off and stored away.
7. Any cheating will result in an F and may lead to disciplinary action.

All the best!

Instructor: Emad AbuKhoua (eakcad@rit.edu | emadak@gatech.edu)

Q1 [5 points]: Select the correct Answer

- 1. Which technique is NOT commonly used in basic dynamic analysis of malware?**
 - A. Monitoring API calls.
 - B. Analyzing changes in file systems.
 - C. Disassembling executable binaries without execution.
 - D. Observing network traffic patterns.
- 2. Which of the following is NOT a characteristic of kernel-level debuggers?**
 - A. They can debug at the operating system kernel level.
 - B. They are less likely to affect the whole system during debugging.
 - C. They often require two systems: one for running the code and another for debugging.
 - D. They provide deep system-level analysis.
- 3. What is the primary goal of system baselining in dynamic malware analysis?**
 - A. To update system software
 - B. To record the initial state of a system for future comparison
 - C. To repair any compromised system files
 - D. To enhance the system's security features
- 4. Host Integrity Monitoring is crucial for dynamic malware analysis. Which of the following is NOT typically monitored?**
 - A. CPU temperature
 - B. Windows Services
 - C. Registry changes
 - D. Network traffic
- 5. In assembly, how is the instruction to access a global variable different from a local variable?**
 - A. Global variables are accessed via direct memory locations; local variables are accessed via stack pointers.
 - B. There is no difference in assembly.
 - C. Local variables use CPU registers; global variables use stack memory.
 - D. Global variables are always encrypted.
- 6. What is the primary advantage of disabling Address Space Layout Randomization during reverse engineering efforts?**
 - A. To facilitate the encryption of data for secure analysis.
 - B. To maintain stable memory address references for more accurate debugging.
 - C. To enhance the performance of debugging tools by reducing memory overhead.
 - D. To enable more effective injection of test code into predictable locations.

7. Which of the following best describes the function of the xor eax, eax command in an assembly program?
- A. It multiplies the value in EAX by itself.
 - B. It sets flags based on the logical AND operation of EAX with itself.
 - C. It subtracts EAX from itself.
 - D. The zero flag is set.
8. In an assembly language program, which instruction would be used to continue a loop based on the condition that a specific register holds a value less than another?
- A. jl
 - B. jle
 - C. jg
 - D. jnz
9. In reverse engineering a binary using IDA, which section is the most likely place to find hardcoded strings such as URLs for a command and control server (e.g., "brbbot.com")?
- A. .text
 - B. .rdata
 - C. .bss
 - D. .heap
10. Given the following assembly code sequence, what will be the hexadecimal values of the DX and AX registers after execution?

```
mov eax, 0
mov edx, 0
mov eax, 11052024h
mov cx, 100h
div cx
```

- E. AX = 0x2024, DX = 0x1105
- F. AX = 0x1105, DX = 0x2024
- G. AX = 0x0024, DX = 0x0020
- H. AX = 0x0020, DX = 0x0024

Q2 [5 points]: Assembly and X86 Architecture

2.1 [1.5 point] Match the x86 architecture registers with their primary uses:

Registers:	Uses:
[1] EAX	A. Used for addition, multiplication, and return values.
[2] ECX	B. Points to the next instruction to be executed.
[3] EBP	C. Used as a counter.
[4] ESP	D. Points to the last item on a stack.
[5] ESI/EDI	E. Used to reference arguments and local variables.
[6] EIP	F. Used by memory transfer instructions.

Provide the correct letter matching the primary use of each register from the options given.

Answers:

[1] _____

[2] _____

[3] _____

[4] _____

[5] _____

[6] _____

2.2 [1 point] Analyze the representation of values within the EAX register as shown in the accompanying diagram. Given that the EAX register holds the value **0110 1000 0111 0111 0110 0100 0101 0011**, determine the **hexadecimal** values for EAX, AX, AH, and AL. Provide your answers starting with '0x' to indicate hexadecimal notation. [1 point]

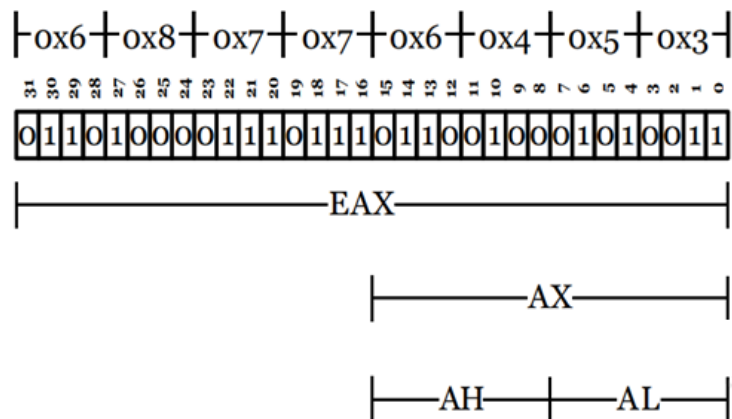
Provide your answers in the following format:

[1] EAX = 0x.....

[2] AX = 0x.....

[3] AH = 0x.....

[4] AL = 0x.....



2.3 [1 point] Analyze the CPU flags after executing the following assembly instructions. Indicate which flags are set as a result of the final instruction. [1 point]

```
mov eax, 20h
mov ebx, 40h
cmp eax, ebx
```

<input type="checkbox"/> Carry	<input type="checkbox"/> Parity
<input type="checkbox"/> Overflow	<input type="checkbox"/> Auxiliary
<input type="checkbox"/> Sign	<input type="checkbox"/> Trap
<input type="checkbox"/> Zero	<input type="checkbox"/> Direction

2.4 [1.5 points] The partial contents of a stack are shown below before the execution of the program segment listed. Determine the contents of the stack after the program has been executed and indicate the new top of the stack

Stack Before		
	Low Byte	High Byte
ESP →	A7	10
	7E	0

Low Addr

High Addr

Stack After		
	Low Byte	High Byte

Low Addr

High Addr

Assembly Instructions:

```
MOV AX, 11E4h
PUSH AX
POP BX
MOV AH, BH
ADD AH, BL
MOV BH, AH
PUSH BX
```

Tasks:

- Fill in the provided "Stack After" template to illustrate the stack contents after the execution of the program. **[1 point]**
- Indicate the position of ESP after the program segment has been executed. (Hint: Use an arrow to point to the correct row of the "Stack After" diagram)**[0.5 point]**

Q3 [5 points] Training Center and Dr.Evil Malware Bomb

Q3.1 [2 points]:

- 1- Analyze the assembly code snippet below for a function called MyFunction(). There is an error in one of the instructions that could potentially lead to incorrect program behavior. Identify which instruction is incorrect and explain why the error affects the function's execution. **[0.5 point]**

1	MyFunction():
2	` push ebp
3	mov ebp, esp
4	push ecx
5	mov dword [ebp-4], 0
6	mov dword [ebp-8], 1
7	mov eax, [ebp-4]
8	add eax, [ebp-8]
9	mov [ebp-4], eax
10	push eax
11	push dword format
12	call printf
13	add esp, 8
14	mov eax, 0
15	leave
16	ret

2. Propose the correct assembly instruction? [0.5 point].

3. Based on the assembly code, write the equivalent C code of **MyFunction()** that performs the same operations as described in the assembly [1 points].

Your C pseudocode:

Q3.2 [3 points] Dr.Evil is back with a 64-bits E-Bomb: You are a cybersecurity analyst working to defuse a potentially dangerous e-bomb malware. Part of your task involves understanding and neutralizing specific phases of the malware's operation. The provided assembly code represents one such critical phase where the malware checks certain conditions to decide whether to detonate.

Your job is to reverse engineer the provided assembly segment into a C function named **phase_1**.

Assembly code of **phase_1** as shown when you debug the program:

pwndbg> disass phase_1	Your C code:
0x400f43 <+0>: sub rsp,0x18	void explode_bomb() {
0x400f47 <+4>: lea rcx,[rsp+0xc]	printf("Bomb exploded!\n");
0x400f4c <+9>: lea rdx,[rsp+0x8]	exit(1);}
0x400f51 <+14>: mov esi,0x4025cf	void phase_1() {
0x400f56 <+19>: mov eax,0x0	// Your implementation goes here
0x400f5b <+24>: call 0x400bf0 <__isoc99_sscanf@plt>
0x400f60 <+29>: cmp eax,0x1
0x400f63 <+32>: jg 0x400f6a <phase_1+39>
0x400f65 <+34>: call 0x40143a <explode_bomb>
0x400f6a <+39>: cmp DWORD PTR [rsp+0x8],0x7
0x400f6f <+44>: ja 0x400fad <phase_1+106>
0x400f71 <+46>: mov eax,DWORD PTR [rsp+0x8]
0x400f75 <+50>: jmp QWORD PTR [rax*8+0x402470]
0x400f7c <+57>: mov eax,0xcf
0x400f81 <+62>: jmp 0x400fbe <phase_1+123>
0x400f83 <+64>: mov eax,0x2c3
0x400f88 <+69>: jmp 0x400fbe <phase_1+123>
0x400f8a <+71>: mov eax,0x100
0x400f8f <+76>: jmp 0x400fbe <phase_1+123>
0x400f91 <+78>: mov eax,0x185
0x400f96 <+83>: jmp 0x400fbe <phase_1+123>
0x400f98 <+85>: mov eax,0xce
0x400f9d <+90>: jmp 0x400fbe <phase_1+123>
0x400f9f <+92>: mov eax,0x2aa
0x400fa4 <+97>: jmp 0x400fbe <phase_1+123>
0x400fa6 <+99>: mov eax,0x147
0x400fab <+104>: jmp 0x400fbe <phase_1+123>
0x400fad <+106>: call 0x40143a <explode_bomb>
0x400fb2 <+111>: mov eax,0x0
0x400fb7 <+116>: jmp 0x400fbe <phase_1+123>
0x400fb9 <+118>: mov eax,0x137
0x400fbe <+123>: cmp eax,DWORD PTR [rsp+0xc]
0x400fc2 <+127>: je 0x400fc9 <phase_1+134>
0x400fc4 <+129>: call 0x40143a <explode_bomb>
0x400fc9 <+134>: add rsp,0x18	}
0x400fcd <+138>: ret	int main() {
	phase_1(); return 0;
	}

Q4 [6 points]: Assembly Reverse Engineering

Note: You may need to scroll to view the full blocks of code.

Consider the assembly representations of two functions, **func1** and **func2**, which are integral parts of a fully functioning program. These functions have been compiled using the **-m32** flag on a 64-bit machine, resulting in specific stack alignments visible in the debugger. Examine these assembly codes and use them to answer the following questions:

pwndbg> disass func1

Dump of assembler code for function **func1**:

0x565561c4	<+0>:	push	ebp
0x565561c5	<+1>:	mov	ebp,esp
0x565561c7	<+3>:	push	ebx
0x565561c8	<+4>:	sub	esp,0x14
0x565561cb	<+7>:	call	0x56556090 <__x86.get_pc_thunk.bx>
0x565561d0	<+12>:	add	ebx,0x2e24
0x565561d6	<+18>:	mov	DWORD PTR [ebp-0xc],0x4
0x565561dd	<+25>:	mov	DWORD PTR [ebp-0x10],0x0
0x565561e4	<+32>:	jmp	0x5655621c <func1+88>
0x565561e6	<+34>:	mov	eax,DWORD PTR [ebp-0x10]
0x565561e9	<+37>:	add	eax,0x1
0x565561ec	<+40>:	lea	edx,[eax*4+0x0]
0x565561f3	<+47>:	mov	eax,DWORD PTR [ebp+0x8]
0x565561f6	<+50>:	add	eax,edx
0x565561f8	<+52>:	mov	edx,DWORD PTR [eax]
0x565561fa	<+54>:	mov	eax,DWORD PTR [ebp-0x10]
0x565561fd	<+57>:	lea	ecx,[eax*4+0x0]
0x56556204	<+64>:	mov	eax,DWORD PTR [ebp+0x8]
0x56556207	<+67>:	add	eax,ecx
0x56556209	<+69>:	mov	eax,DWORD PTR [eax]
0x5655620b	<+71>:	push	edx
0x5655620c	<+72>:	push	eax
0x5655620d	<+73>:	call	0x5655618d <func2>
0x56556212	<+78>:	add	esp,0x8
0x56556215	<+81>:	add	DWORD PTR [ebp-0xc],eax
0x56556218	<+84>:	add	DWORD PTR [ebp-0x10],0x2
0x5655621c	<+88>:	mov	eax,DWORD PTR [ebp+0xc]
0x5655621f	<+91>:	sub	eax,0x1
0x56556222	<+94>:	cmp	DWORD PTR [ebp-0x10],eax
0x56556225	<+97>:	j1	0x565561e6 <func1+34>
0x56556227	<+99>:	sub	esp,0x8
0x5655622a	<+102>:	push	DWORD PTR [ebp-0xc]
0x5655622d	<+105>:	lea	eax,[ebx-0x1fec]
0x56556233	<+111>:	push	eax

0x56556234	<+112>:	call	0x56556040 <printf@plt>
0x56556239	<+117>:	add	esp,0x10
0x5655623c	<+120>:	nop	
0x5655623d	<+121>:	mov	ebx,DWORD PTR [ebp-0x4]
0x56556240	<+124>:	leave	
0x56556241	<+125>:	ret	

pwndbg> break func2

pwndbg> continue

pwndbg> disass func2

Dump of assembler code for function func2:

0x5655618d	<+0>:	push	ebp
0x5655618e	<+1>:	mov	ebp,esp
0x56556190	<+3>:	sub	esp,0x10
0x56556193	<+6>:	call	0x565562ad <__x86.get_pc_thunk.ax>
0x56556198	<+11>:	add	eax,0x2e5c
=> 0x5655619d	<+16>:	mov	edx,DWORD PTR [ebp+0x8]
0x565561a0	<+19>:	mov	eax,edx
0x565561a2	<+21>:	add	eax,edx
0x565561a4	<+23>:	add	eax,edx
0x565561a6	<+25>:	add	eax,edx
0x565561a8	<+27>:	lea	edx,[eax+0x3]
0x565561ab	<+30>:	nop	
0x565561ad	<+32>:	nop	
0x565561b0	<+35>:	sar	eax,0x2
0x565561b3	<+38>:	mov	DWORD PTR [ebp-0x4],eax
0x565561b6	<+41>:	mov	eax,DWORD PTR [ebp+0xc]
0x565561b9	<+44>:	mov	edx,DWORD PTR [ebp-0x4]
0x565561bc	<+47>:	mov	ecx,edx
0x565561be	<+49>:	shl	edx,cl
0x565561c0	<+51>:	mov	eax,edx
0x565561c2	<+53>:	leave	
0x565561c3	<+54>:	ret	

4.A [2.75] Fill in the C code below so that it aligns with the above x86-32 assembly. Ensure your C code fits into the provided blanks without adding additional lines or circumventing the format (this may require slight adjustments to the syntax or style of your initial decoding guess to ensure it fits properly). All constants of type signed or unsigned int must be written in decimal.

```
#include <stdio.h>

int func2(int x, int y) {
    int z = _____;
    return _____ << _____;
}

void func1(int *arr, int count) {
    int z = _____;
    for (int i = _____; i < _____; i += _____) {
        _____ += func2(_____, _____);
    }
    printf("%d\n", _____);
}

int main() {
    // Example array and count
    int array[] = {1, 2, 3, 4, 5, 6};
    int count = sizeof(array) / sizeof(array[0]); // Calculate the
number of elements in the array

    // Call func1 with the array and its size
    func1(array, count);

    return 0;
}
```

B) [1-point] Explain the purpose of the following lines of assembly code in func2?

0x5655618d <+0>:	push	ebp
0x5655618e <+1>:	mov	ebp,esp
0x56556190 <+3>:	sub	esp,0x10

C) [1.25 points] Stack Frame Content Analysis:

The following is the stack frame content for func2 at the point where execution breaks at this line:

=> 0x5655619d <+16>: mov edx,DWORD PTR [ebp+0x8]

Here is the output from pwndbg showing the stack values at that moment:

pwndbg> x/20xw \$esp				
0xffffcf40:	0xf7ffcff4	0x0000000c	0x00000000	0xffffcfb4
0xffffcf50:	0xffffcf78	0x56556212	0x00000001	0x00000002
0xffffcf60:	0x00000000	0x00000001	0x00000000	0x00000004
0xffffcf70:	0xffffffff	0xf7e1dfff	0xffffcfb8	0x5655629d
0xffffcf80:	0xffffcf94	0x00000006	0x00000000	0x56556258

Using this information, determine the values for the following registers and stack entries:

C1	What is the value of ESP?	
C2	What is the value of EIP?	
C3	What is the return address or the saved EIP?	
C4	What is the value of the current frame EBP?	
C5	What is the value of the previous frame EBP or the pushed EBP of func1?	

Q5 [3 Points]: Login to an ATM Banking System

After your notable success with the GreenCAT and your ingenious handling of the electronic bomb defusing scenario, your expertise is once again in demand. The developer of the **atm.c** program, recognizing your adept problem-solving skills, has urgently requested your assistance with their banking system.

The author of the **atm.c** program has implemented login functionality in their banking system. However, there have been cases where, even without knowing the hard secret password **MyPwd@2024**, users are able to log in. They've narrowed down the issue to a single function, **logMeIn()**, for which they have provided you with the source code. You can run the code on your local computer, but you don't have access to the `read_real_password` function, so you make that function up yourself for your testing.

Below, the source code for the **LogMeIn()** function from the **atm.c** program is presented, along with the corresponding disassembled code of the binary you created to analyze the system, enabling a thorough investigation into the suspected security flaw:

login.c	logMeIn () assembly
<pre>#include <stdio.h> #include <string.h> #include <stdbool.h> void read_real_password(char *buf) { strcpy(buf, "MyPwd@2024"); } bool logMeIn() { int allow_login = 0; char buffer[12]; char real_password[12]; read_real_password(real_password); printf("What is your password? "); gets(buffer); if (strcmp(buffer, real_password, 12) == 0) allow_login = 1; return allow_login; } int main() { bool is_logged_in = logMeIn(); if (is_logged_in) { printf("Welcome\n"); } else { printf("Rejected\n"); } return 0; }</pre>	<pre><gdb> disass logMeIn push rbp mov rbp, rsp sub rsp, 0x20 mov DWORD PTR [rbp-0x4], 0x0 lea rax, [rbp-0x1c] mov rdi, rax call 0x1169 <read_real_password> lea rax, [rip+0xe56] mov rdi, rax mov eax, 0x0 call 0x1050 <printf@plt> lea rax, [rbp-0x10] mov rdi, rax mov eax, 0x0 call 0x1060 <gets@plt> lea rcx, [rbp-0x1c] lea rax, [rbp-0x10] mov edx, 0xc mov rsi, rcx mov rdi, rax call 0x1030 <strcmp@plt> test eax, eax jne 0x11ef <login+99> mov DWORD PTR [rbp-0x4], 0x1 cmp DWORD PTR [rbp-0x4], 0x0 setne al leave ret</pre>

The first action taken in response to this incident involved conducting preliminary tests together with the bank team, which yielded some troubling findings:

```
(kali㉿kali)-[~/Desktop/Final]
└─$ ./login
What is your password? MyPwd@2024
Welcome
(kali㉿kali)-[~/Desktop/Final]
└─$ ./login
What is your password? IamAHacker
Rejected
(kali㉿kali)-[~/Desktop/Final]
└─$ ./login
What is your password? IamNotAHacker
Welcome
```

The login trials revealed a critical security flaw where incorrect passwords like "**IamNotAHacker**" were mistakenly accepted, granting unauthorized access. While valid passwords were correctly authenticated and invalid ones rejected, the system's vulnerability lies in improper handling or validation, necessitating immediate investigation and remediation.

You start investigating with **GDB** debugger, where you uncover the following information about the program state:

```
$ gdb ./login
pwndbg> break 18
Breakpoint 1 at 0x11cc: file login.c, line 18.
pwndbg> run
What is your password? RITDubai
[ STACK ]
00:0000| rsp 0x7fffffffdd90 ← 0x7750794d00000000
01:0008| -018 0x7fffffffdd98 ← 0x343230324064 /* 'd@2024' */
02:0010| rax 0x7fffffffdda0 ← 'RITDubai'
03:0018| -008 0x7fffffffdda8 ← 0x0
04:0020| rbp 0x7fffffffddb0 → 0x7fffffffddd0 ← 0x1
05:0028| +008 0x7fffffffddb8 → 0x55555555520a (main+18)← mov byte ptr [rbp-1], al
06:0030| +010 0x7fffffffddc0 ← 0x0
07:0038| +018 0x7fffffffddc8 ← 0x0

pwndbg> p &real_password
$1 = (char *) [12]) 0x7fffffffdd94
pwndbg> p &buffer
$2 = (char *) [12]) 0x7fffffffdda0
pwndbg> p &allow_login
$3 = (int *) 0x7fffffffddac
pwndbg> x/s 0x7fffffffdd94
0x7fffffffdd94: "MyPwd@2024"
```

5.A [0.5 point] Given the GDB results, is the buffer content capable of overwriting the real_password such that it matches the buffer? In your response, consider how the buffer expands, the arrangement of local variables in the stack memory, and the direction in which the stack grows.

5.C [1 point] Write a note to the developer explaining how it is possible to log into the system with the password "IamNotAHacker " while attempts with "IamAHacker" are unsuccessful. Provide a specific example of an input that could allow unauthorized access and explain why this approach would be successful.

5.D [1 point] Provide the developer with a 1-line code change to `logMein()` to fix the issue. Explain why your fix resolves the issue.

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