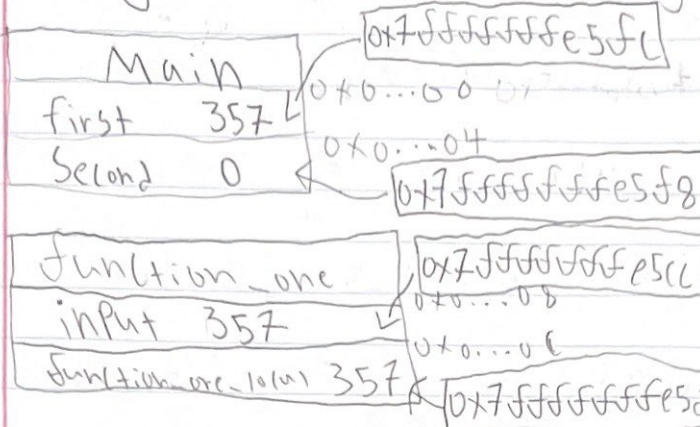
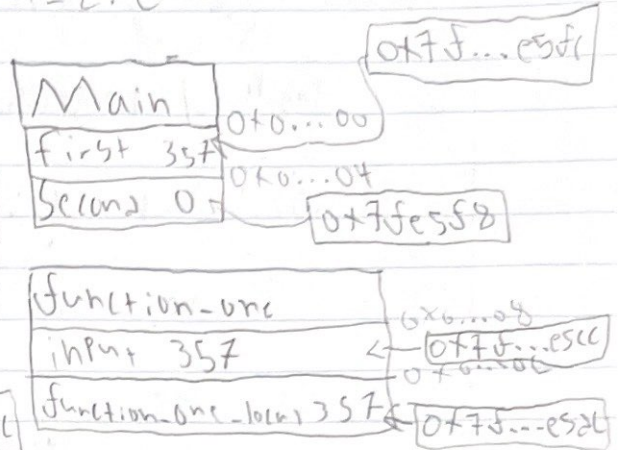


Task 2 and Task 3

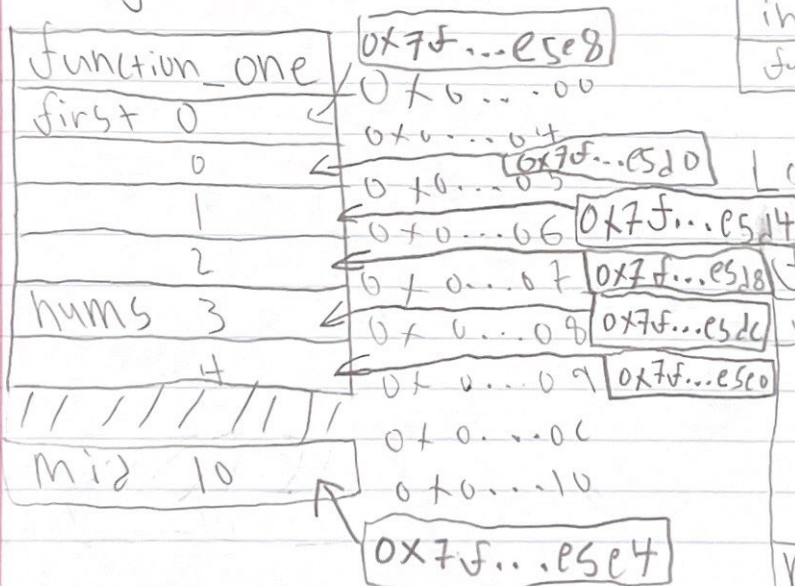
Layout-1.c



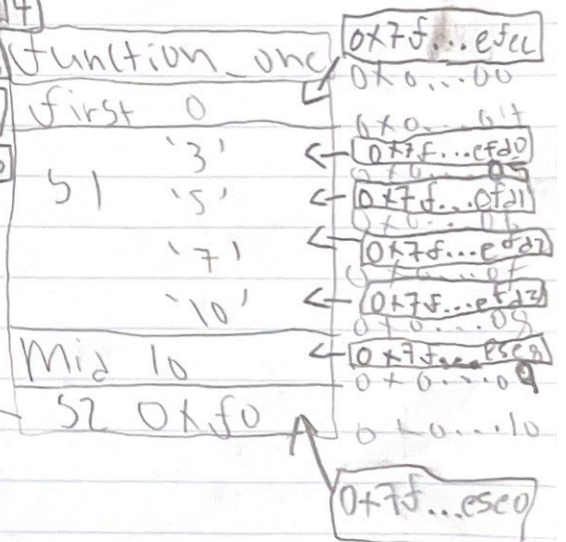
Layout-2.c



Layout-3.c



Layout-4.c



Read Only

| | |
|------|------|
| '3' | 0xF0 |
| '5' | 0xF1 |
| '7' | 0xF2 |
| '10' | 0xF3 |
| | 0xF4 |

After going through the debugger I notice the values matches the correct memory address. when you are in the array

From layout-3.c and layout-4.c, I noticed that the memory was going up by one bit while the other code was going up by 4 bits.