

Faculty of Engineering and Technology

Department of Electrical and Computer Engineering

DIGITAL ELECTRONICS AND COMPUTER ORGANIZATION LABORATORY (ENCS2110)

"Post _lab 4&6(Experiment4&6)"

Prepared by:

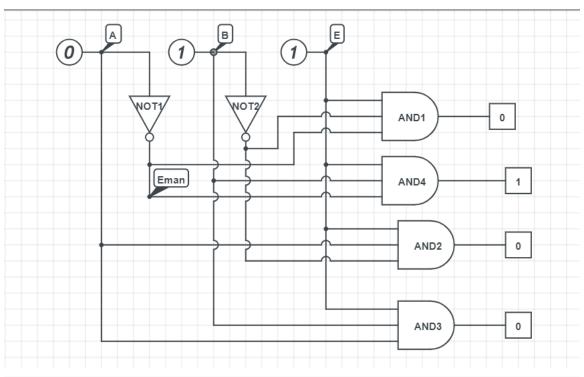
Name: Eman Asfour Number: 1200206

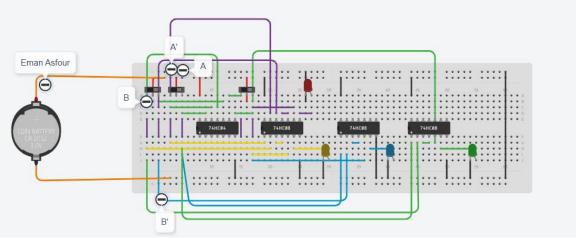
Instructor: Dr. Jamal Seyam

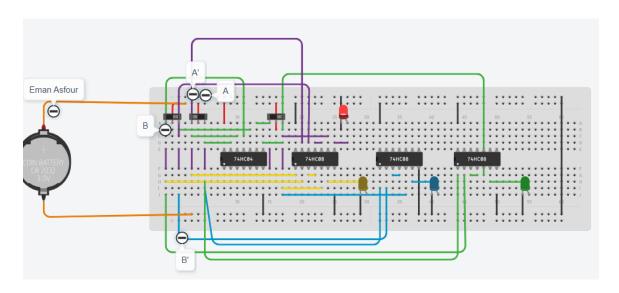
Section: 4

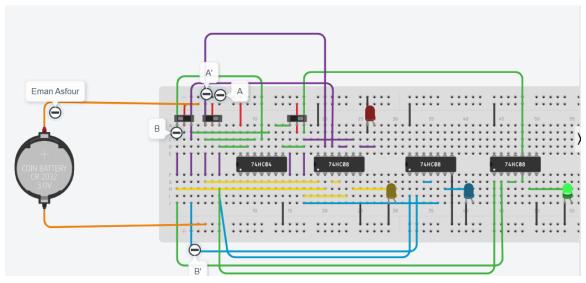
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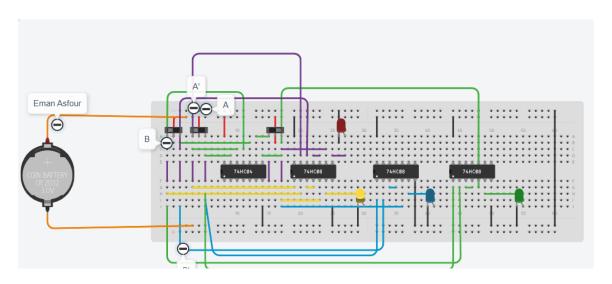
1. How do you go about adding an Enable (E) signal to the decoder in Figure 4.7? Modify the implementation to show that. (Design Only using chips in Figure 1)



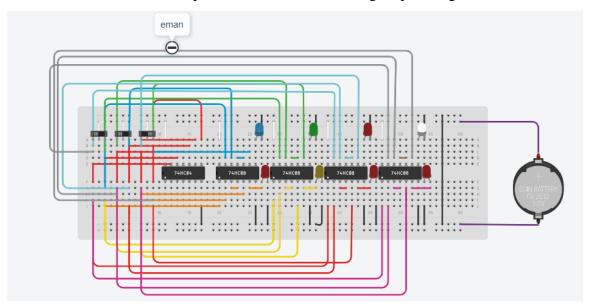


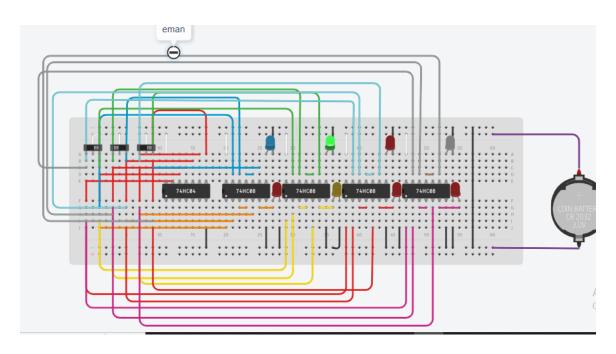






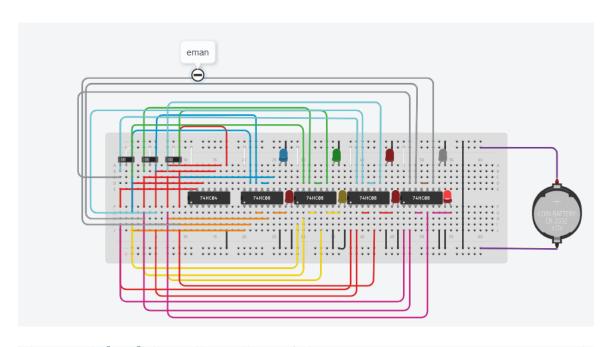
2. How to use that to implement a 3x8 decoder using chips in Figure 1

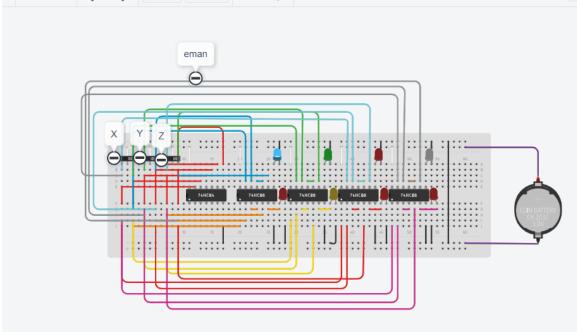




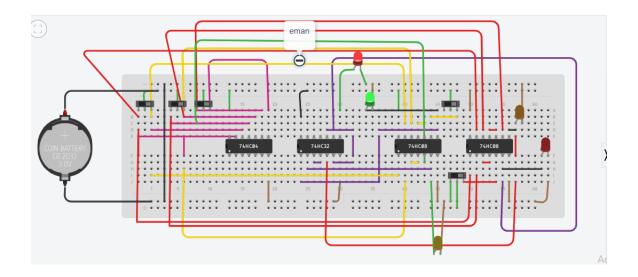
 $D0 = X'Y'Z' \qquad D1 = X'Y'Z \quad D2 = X'YZ' \quad D3 = X'YZ \quad D4 = XY'Z'$

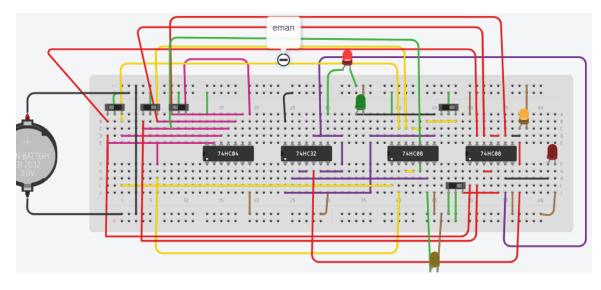
D5=XY'Z D6=XYZ' D7=XYZ



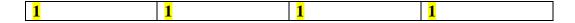


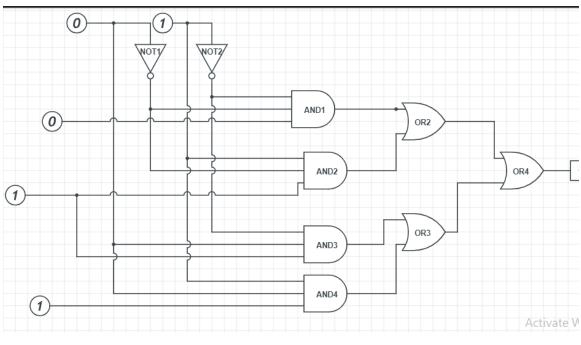
3. Use the just constructed 4x1 multiplexer to design a three inputs network that gives 1 if the majority of its inputs are 1 and outputs a zero otherwise (Design Only using chips in Figure 1)

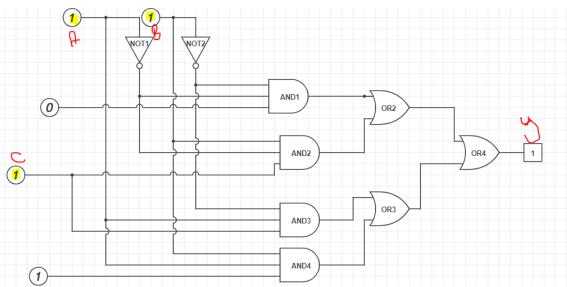


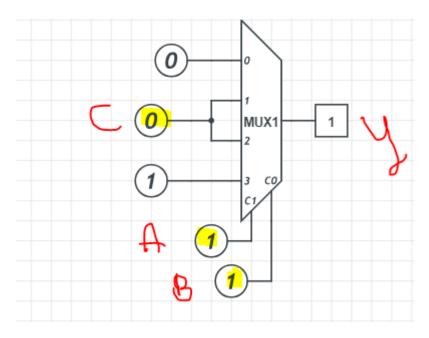


A	В	C	Y	
0	0	0	0	
0	0	1	0	
0	1	0	0	
0	<mark>1</mark>	1	<mark>1</mark>	
1	0	0	0	
1	0	1	1	
<mark>1</mark>	<mark>1</mark>	0	<mark>1</mark>	

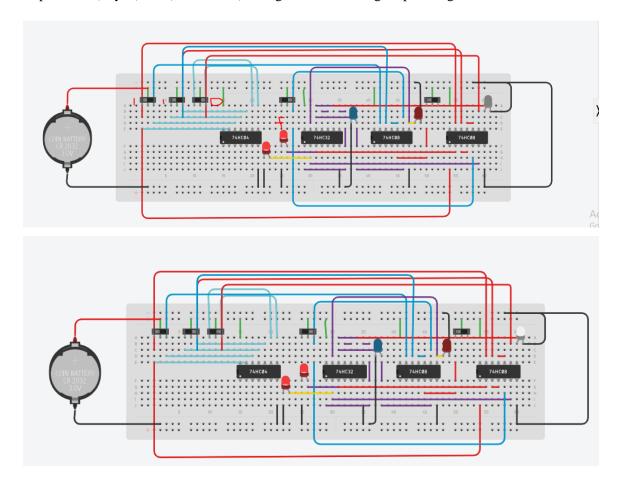


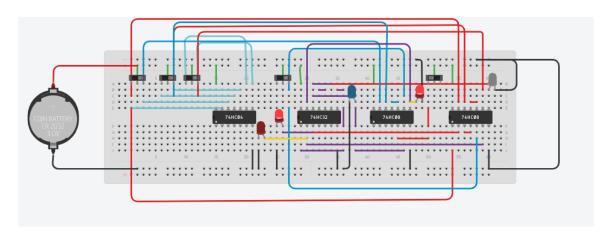




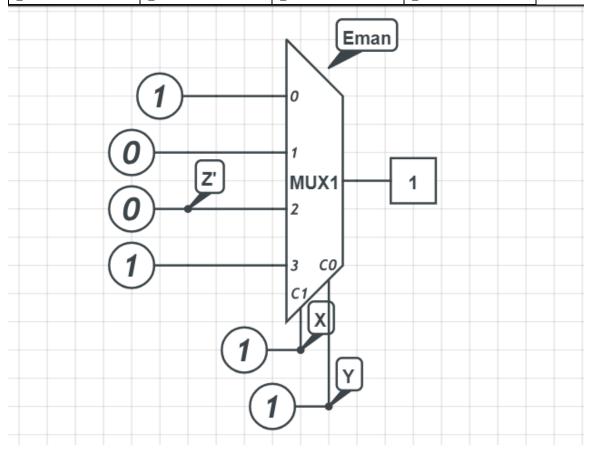


4. Implement f(x, y, z) = m(0, 1, 4, 6, 7), using 4x1 MUX using chips in Figure 1.



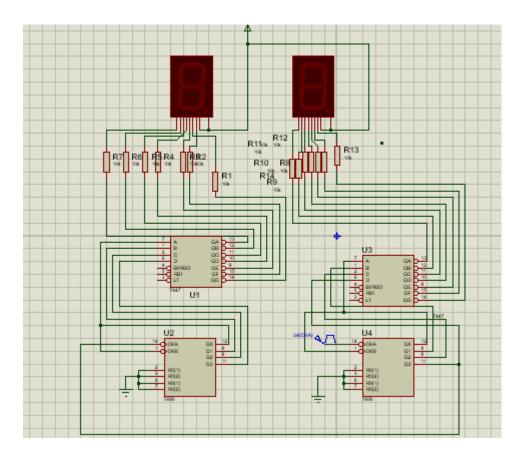


X	y	Z	f
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

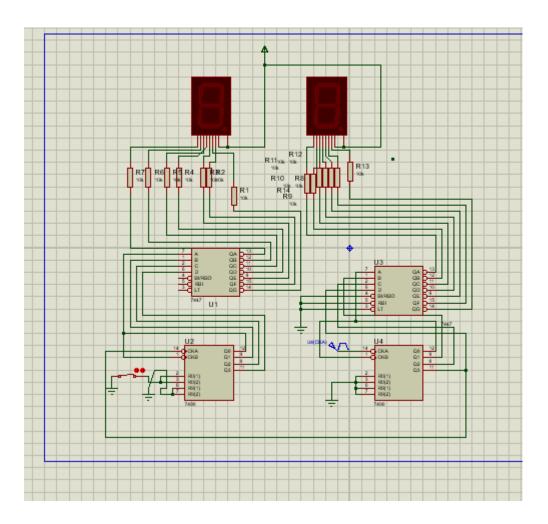


5. Post Lab6

Design a two-decade counter that counts from 00 to 99



Add additional input to your design that can be used to reset the counter



Modify the counter to count to 59 (without Reset).

