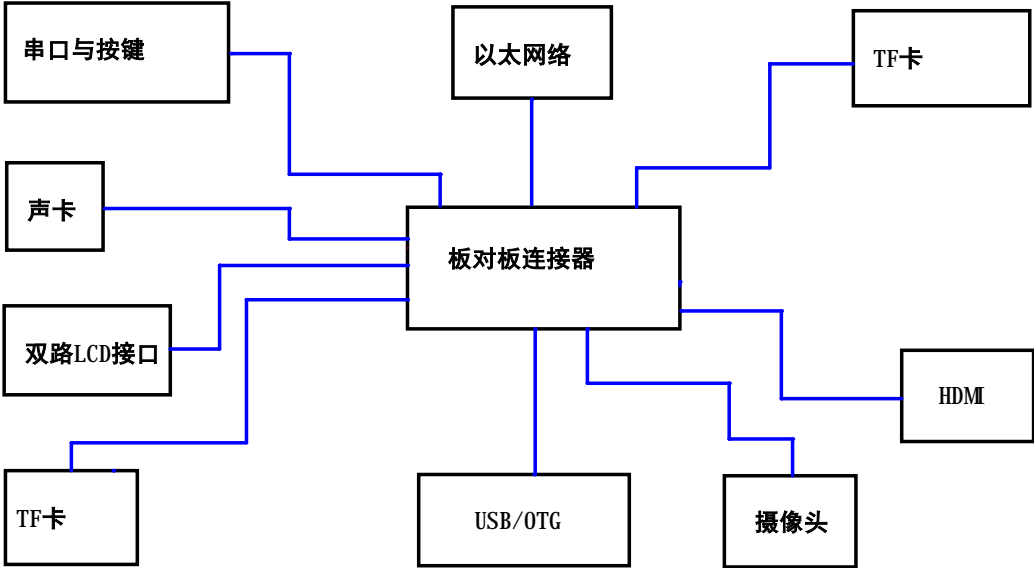
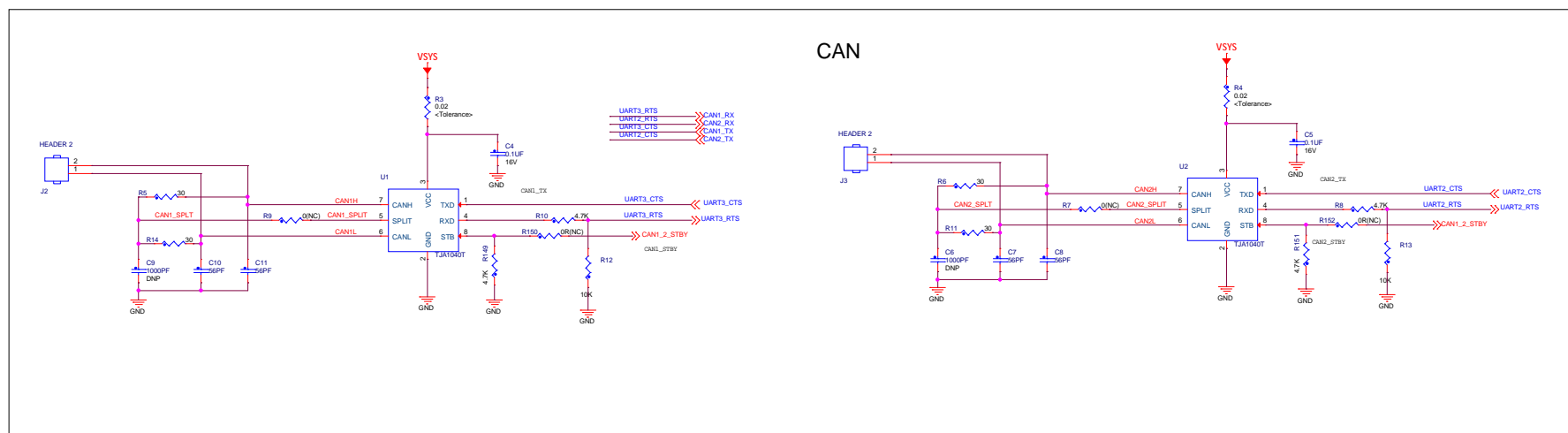
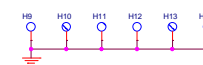
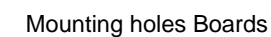
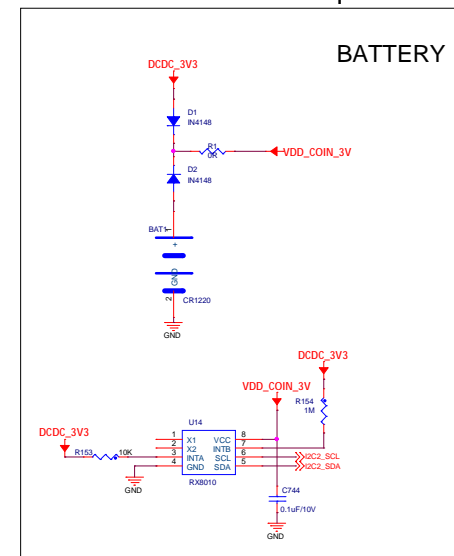
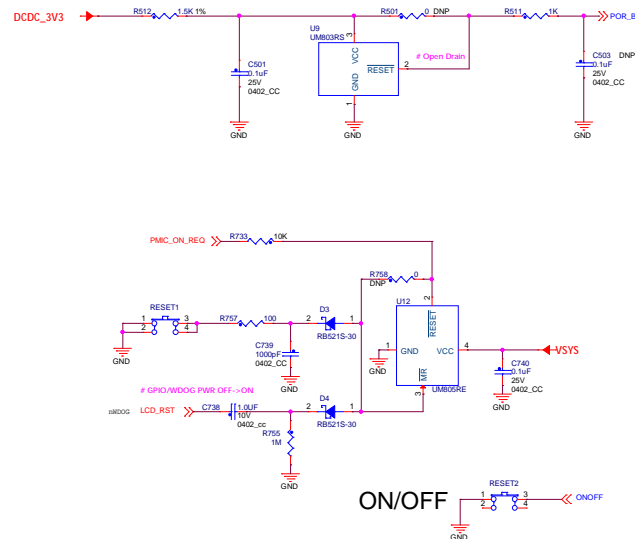
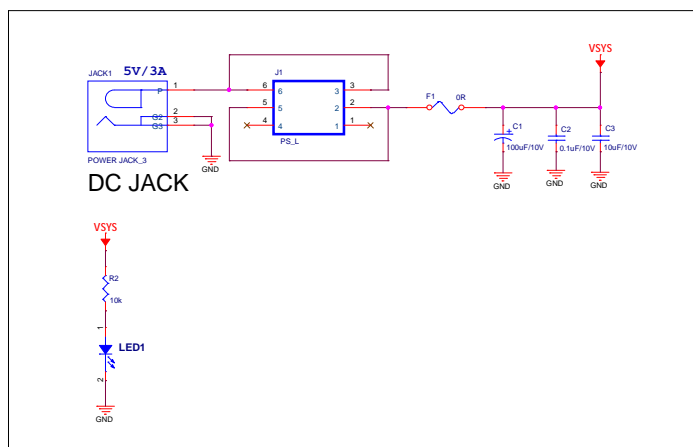


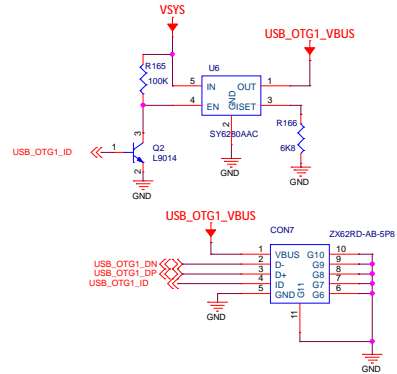
Table of Contents	Part Reference	Details of Revision



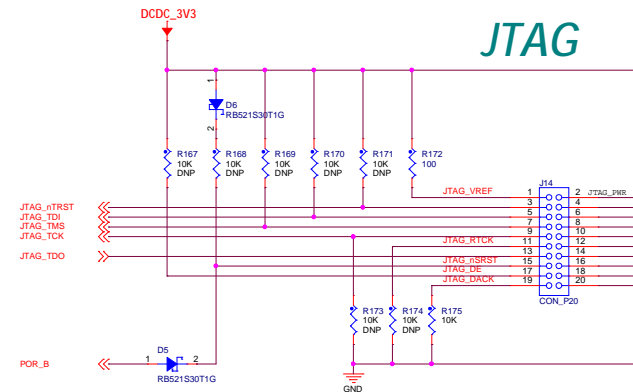


USB HOST/OTG

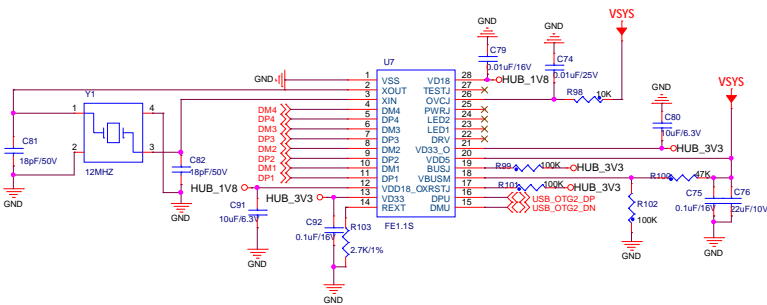
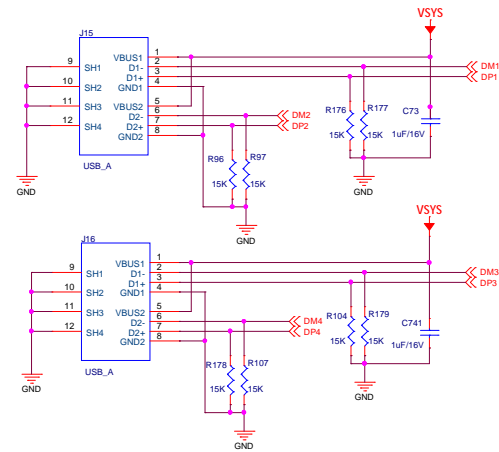
USB_OTG1_DN <> USB_OTG1_DP
 USB_OTG1_DP <> USB_OTG2_DN
 USB_OTG2_DN <> USB_OTG2_DP
 USB_OTG2_DP <> USB_OTG1_DP
 USB_OTG1_PWR <> USB_OTG2_PWR
 USB_OTG2_PWR <> USB_OTG1_ID
 USB_OTG1_ID <> USB_OTG1_OC
 USB_OTG2_OC <> USB_OTG2_OC



JTAG

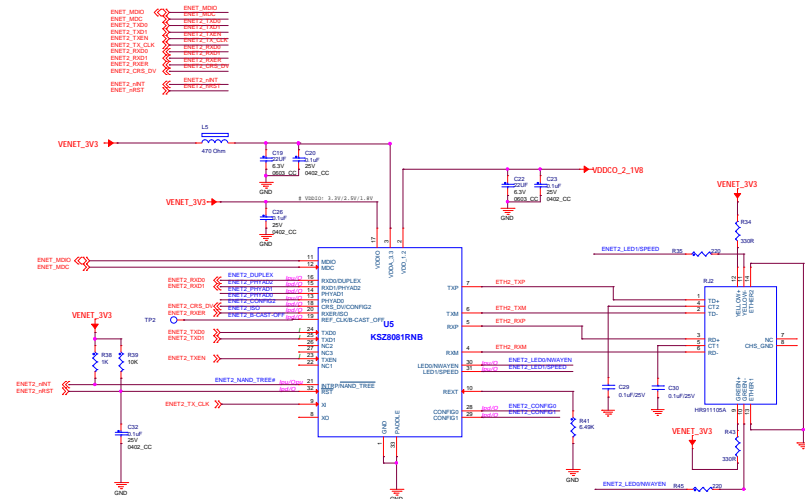
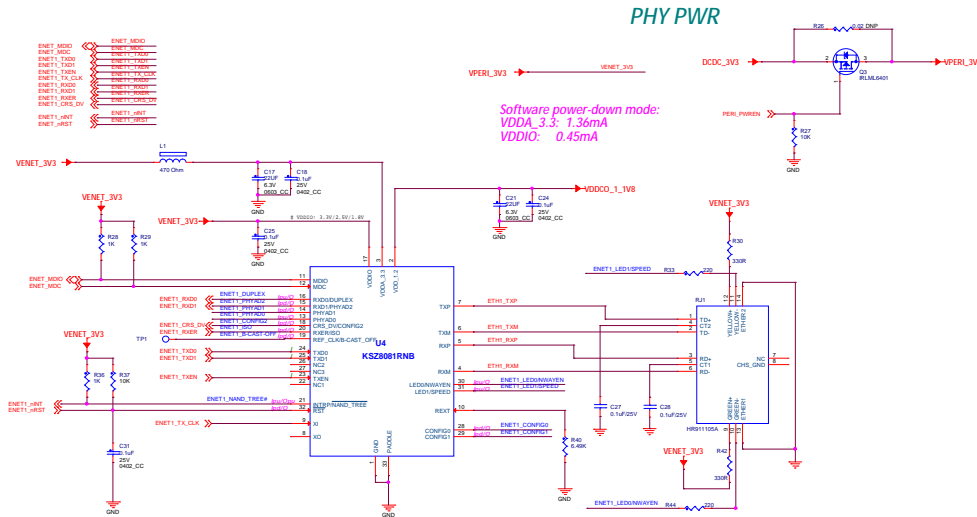


USB HOST

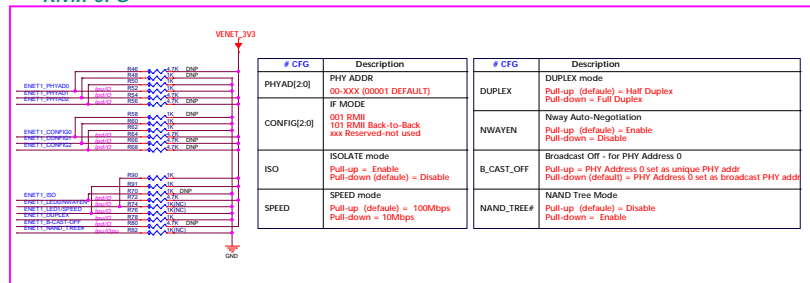


100M ETHERNET RMII PHY x1

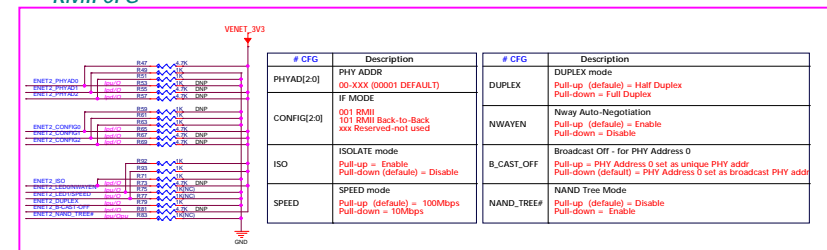
100M ETHERNET RMII PHY x2



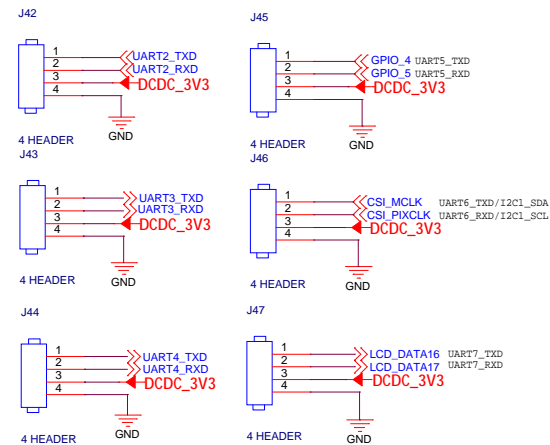
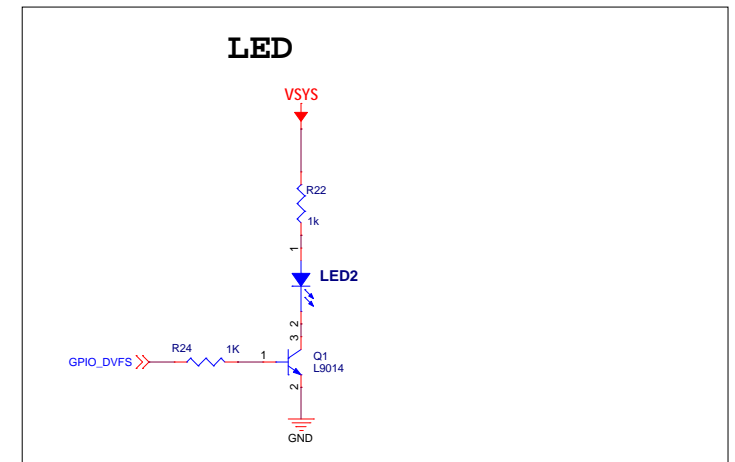
RMII CFG



RMII CFG



C



```
<Default: QSPI BOOT>
0/1      0/1      0/1      1      0      0      0      0
```

0 0 0 0 0 0 0 0

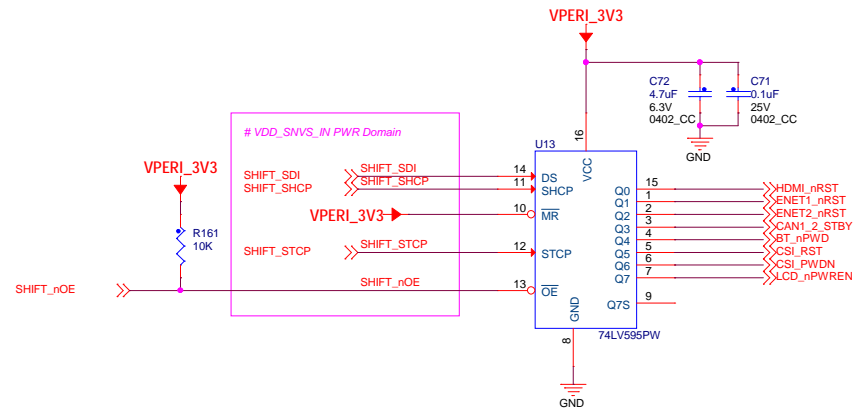
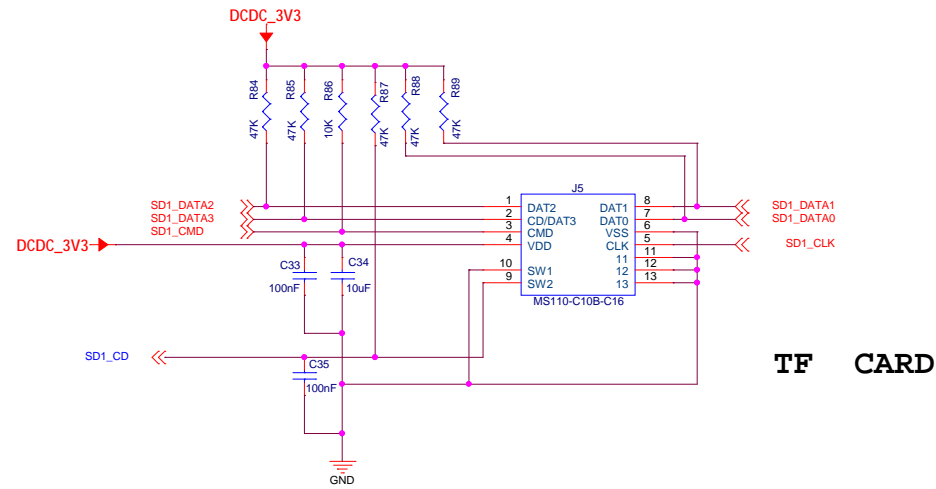
0x460	Reserved (DDR3 config options)
-------	--------------------------------

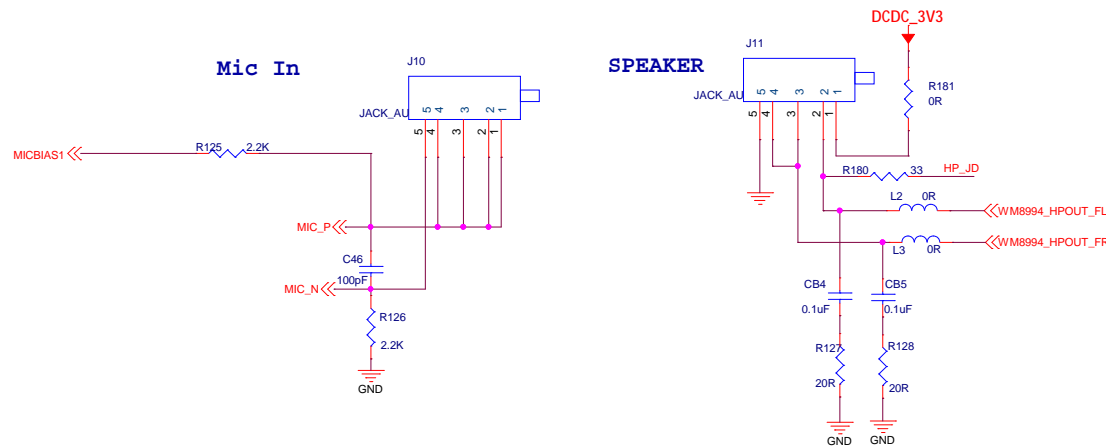
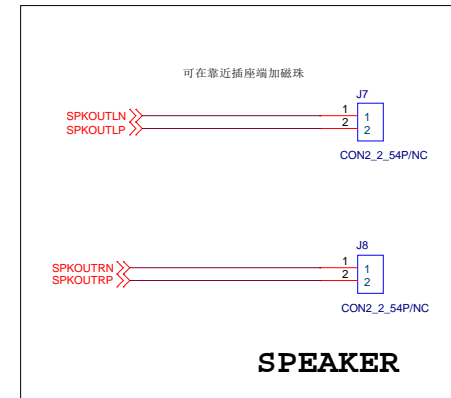
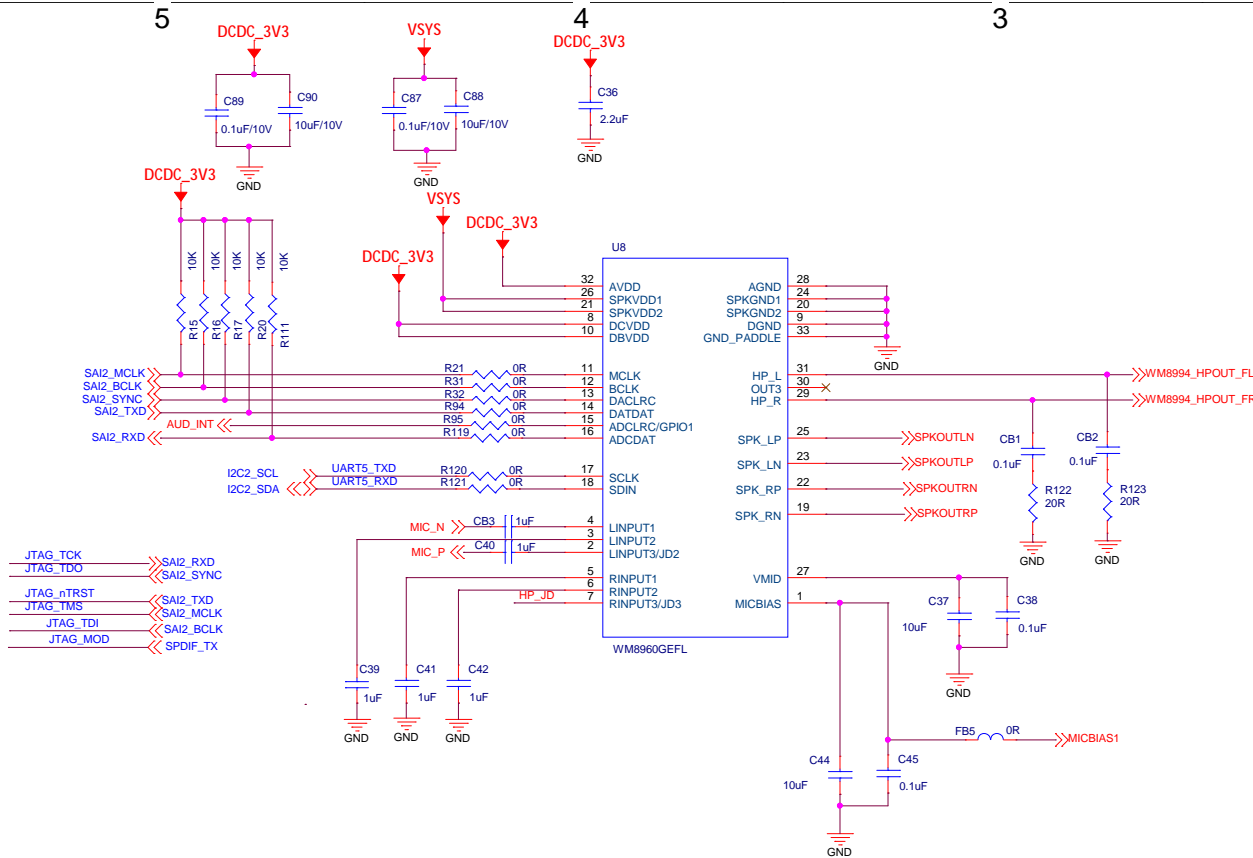
0x460	Reserved	Reserved	Reserved	TZASC_ENABLE	JTAG_HEO	KTE	Reserved	DLL_ENABLE 0 - Disable DLL for SD/emMC 1 - Enable DLL for SD/emMC
-------	----------	----------	----------	--------------	----------	-----	----------	---

0x470	Reserved for unexpected requirements	eMMC 4.4 - RESET TO PRE-IDE STATE	Override HYS bit for SD/MMC pads	USDHC_PAD_PULL_DOWN 0 - no action 1 - pull down	ENABLE_EMMC_22K_PULLUP 0 - 47K pullup 1 - 22K pullup	ADD_DS_SET_GPR1_16 0 - Set 1 - Don't set	USDHC_IOMUX_SION_BIT_ENABLE 0 - Disable 1 - Enable	LEUSDHC_IOMUX_SRE_Enable 0 - Disable 1 - Enable
-------	--------------------------------------	-----------------------------------	----------------------------------	---	--	--	--	---

0x470	Override NAND Pad Settings (using PAD_SETTINGS value)	MMC_DLL_DLY[6:0] Delay (ns) for SD/eMMC DLL. If it is applied to slave mode, target delay or override mode target delay depends on DLL Override type bit value.
-------	---	--

- 1 page = (4K + 224 bytes)
- 1 block = (4K + 224) bytes x 256 pages
= (1024K + 56K) bytes
- 1 plane = (1024K + 56K) bytes x 2048 blocks
= 17,280Mb
- 1 LUN = 17,280Mb x 2 planes
= 34,560Mb





5

4

3

2

1

E

E

D

D

C

C

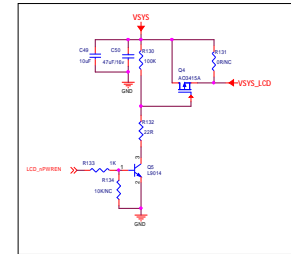
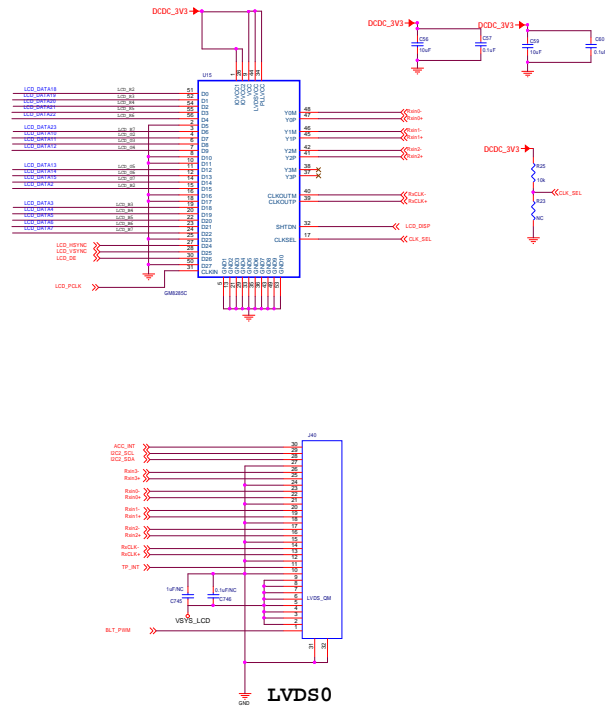
B

B

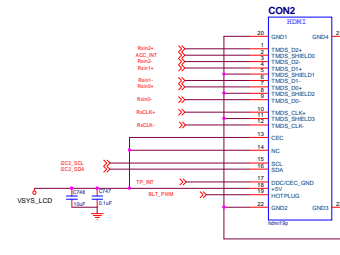
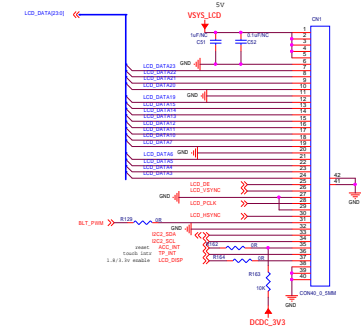
A

A

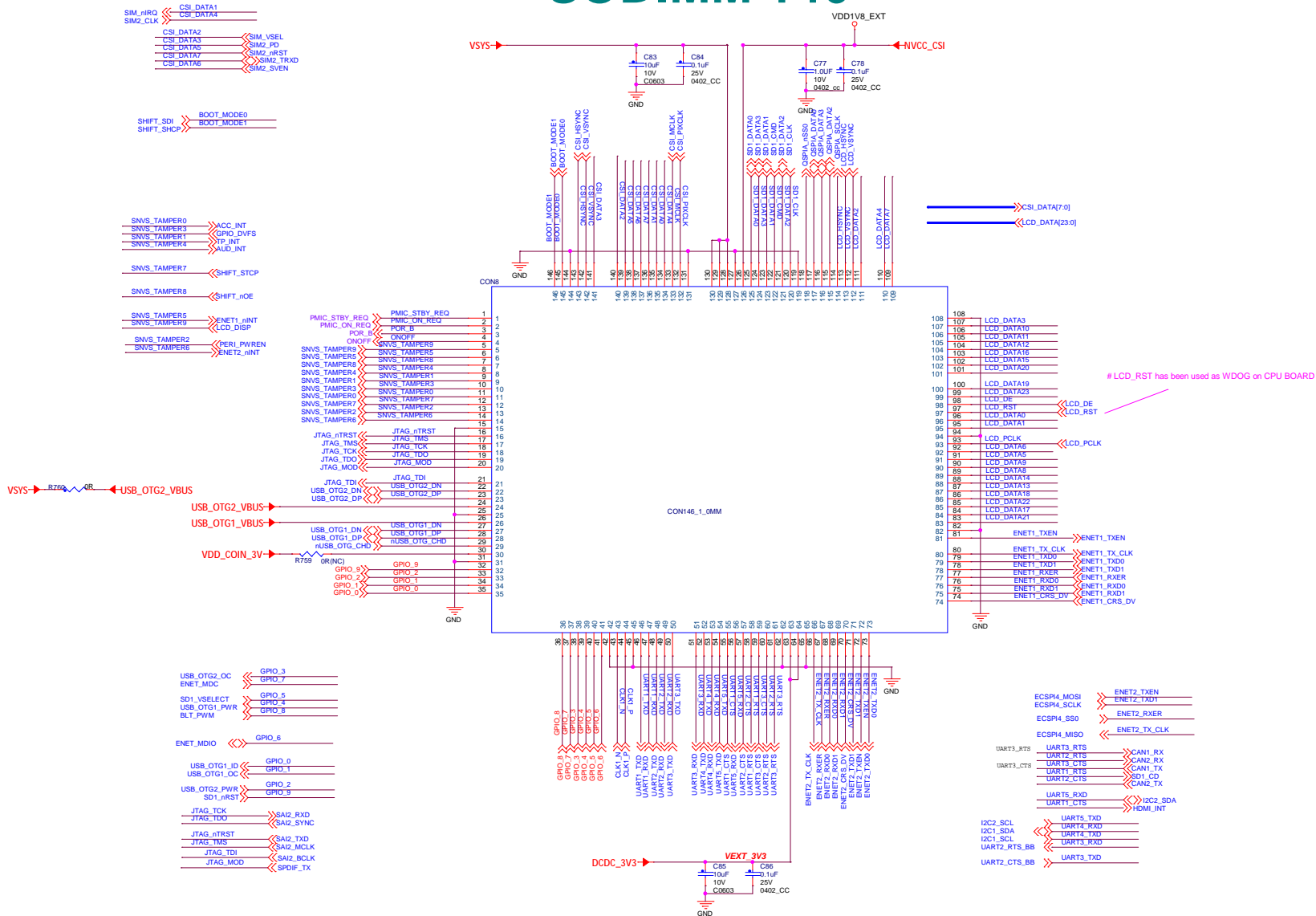
RGB TO LVDS



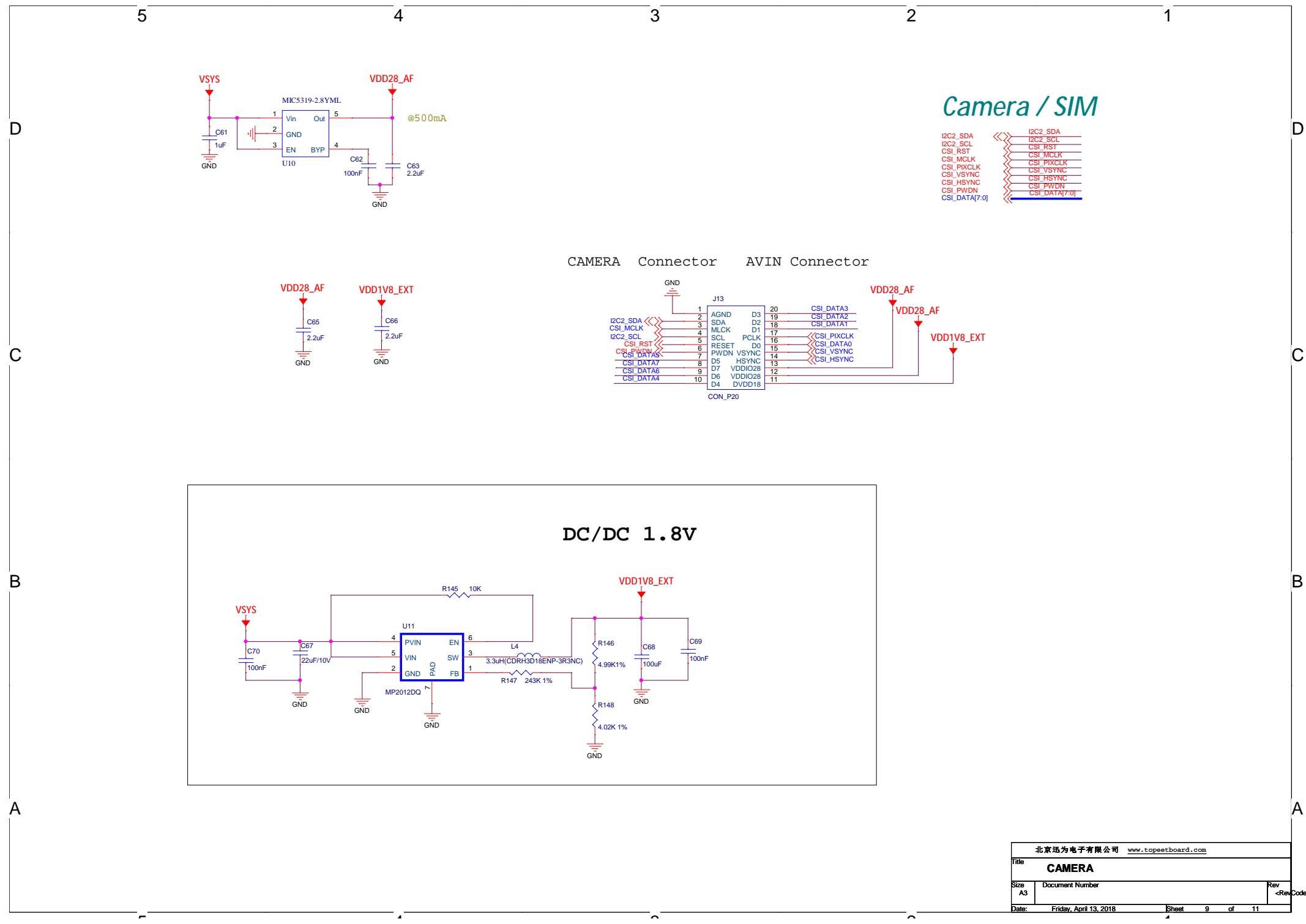
4.3 LCD RGB Interface



SODIMM 146



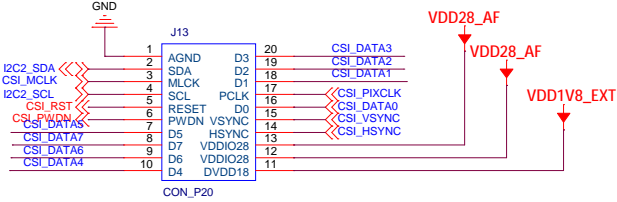
北京迅为电子有限公司 www.topeetboard.com	
Title	BOARD CONNECTOR
Size	Document Number
C	Rev
Date	First Rev. April 13, 2018
Sheet	9 of 11



Camera / SIM

I2C2_SDA	I2C2_SDA
I2C2_SCL	I2C2_SCL
CSI_RST	CSI_RST
CSI_MCLK	CSI_MCLK
CSI_PIXCLK	CSI_PIXCLK
CSI_VSYNC	CSI_VSYNC
CSI_HSYNC	CSI_HSYNC
CSI_PWDN	CSI_PWDN
CSI_DATA[7:0]	CSI_DATA[7:0]

CAMERA Connector AVIN Connector



DC/DC 1.8V

