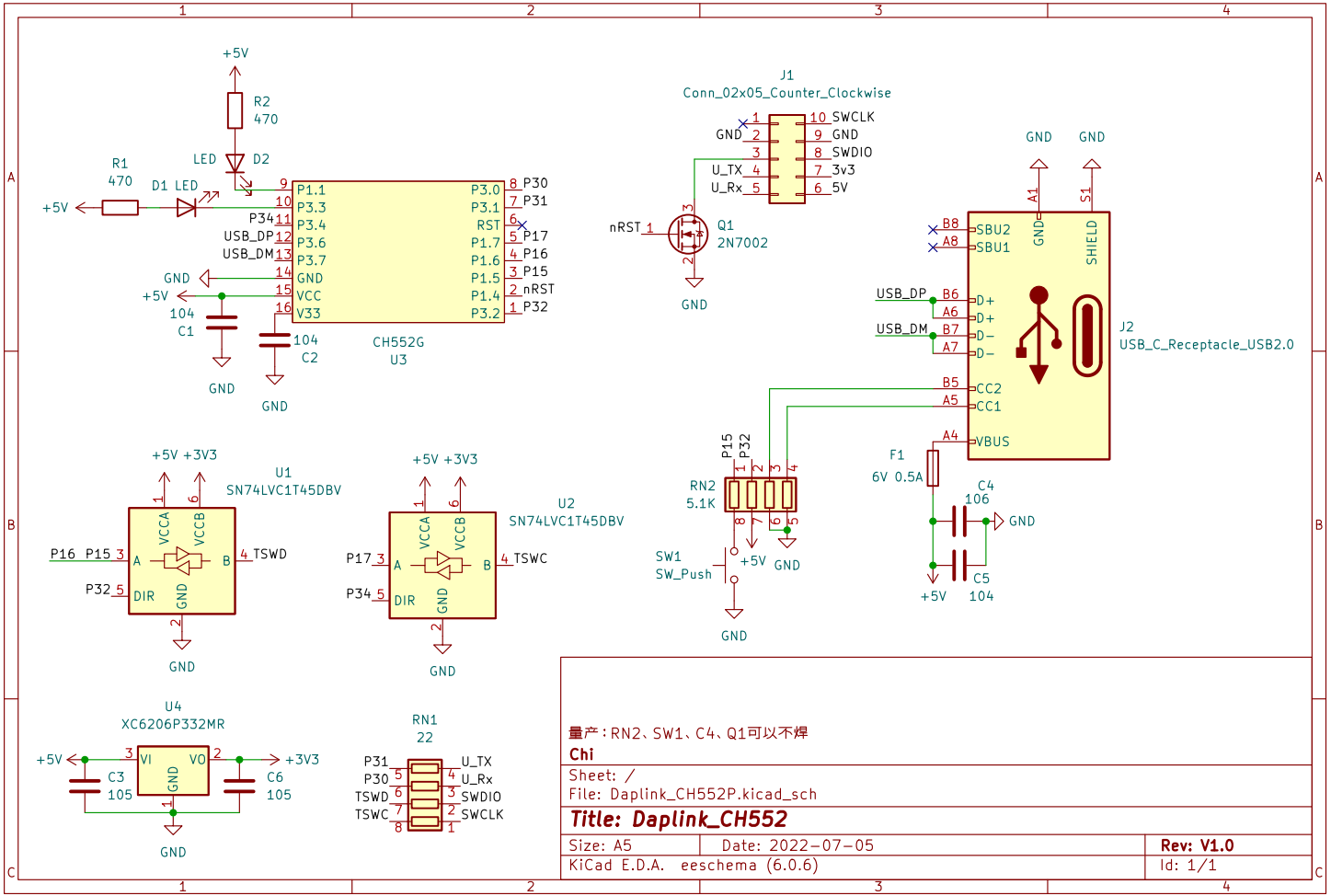
+5V
R2
470
LED
D2
R1
470
D1 LED
+5V
P34
USB_DP
USB_DM
GND
+5V
104
C1
GND
104
C2
GND
P1.1
P3.3
P3.4
P3.6
P3.7
GND
VCC
V33
P3.0
P3.1
RST
P1.7
P1.6
P1.5
P1.4
P3.2
P30
P31
P17
P16
P15
nRST
P32
CH552G
U3
J1
Conn_02x05_Counter_Clockwise
GND
U_TX
U_Rx
SWCLK
GND
SWDIO
3v3
5V
nRST
Q1
2N7002
GND
GND
GND
A1
S1
B8
A8
SBU2
SBU1
GND
SHIELD
USB_DP
B6
A6
D+
D+
USB_DM
B7
A7
D−
D−
J2
USB_C_Receptacle_USB2.0
B5
A5
CC2
CC1
A4
VBUS
F1
6V 0.5A
C4
106
GND
C5
104
+5V
P15
P32
RN2
5.1K
+5V
GND
SW1
SW_Push
GND
+5V +3V3
U1
SN74LVC1T45DBV
VCCA
VCCB
P16 P15
A
B
TSWD
P32
DIR
GND
GND
+5V +3V3
U2
SN74LVC1T45DBV
VCCA
VCCB
P17
A
B
TSWC
P34
DIR
GND
GND
U4
XC6206P332MR
+5V
VI
VO
+3V3
C3
105
GND
C6
105
GND
RN1
22
P31
P30
TSWD
TSWC
U_TX
U_Rx
SWDIO
SWCLK
量产：RN2、SW1、C4、Q1可以不焊
Chi
Sheet: /
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Title: Daplink_CH552
Size: A5
Date: 2022-07-05
Rev: V1.0
KiCad E.D.A. eeschema (6.0.6)
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