Lab 3

FPGAs as Accelerators

# Introduction

\*\*LSAL Explanation

\*\*File structure

# Software (x86 & Arm)

\*\*workflow

## Unoptimized

## Code Profiling

## Optimized

\*\*valgrind

## Comparison

# Hardware (FPGA)

\*\*workflow

## Algorithmic Optimizations

## HLS Optimizations

## Comparison

# Results

## FPGA vs CPU