Lab 3

FPGAs as Accelerators

# Εισαγωγή

\*\*Εξηγηση αλγοριθμου

# Software (x86 & Arm)

\*\*workflow

## Unoptimized

## Code Profiling

## Optimized

\*\*valgrind

## Comparison

# Hardware (FPGA)

\*\*workflow

## Code optimization

## Vitis HLS

# Results