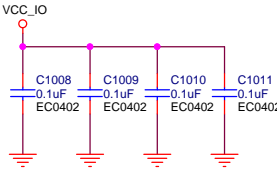
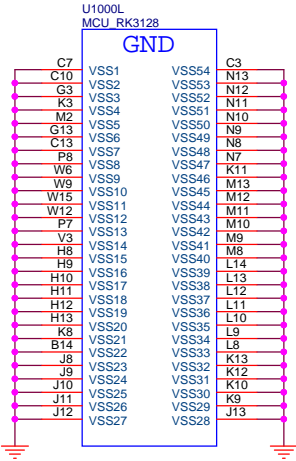
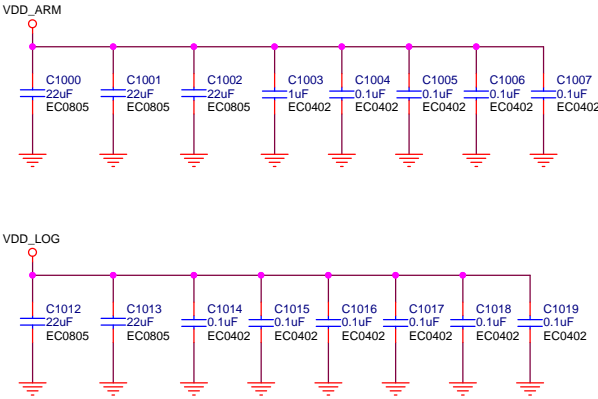
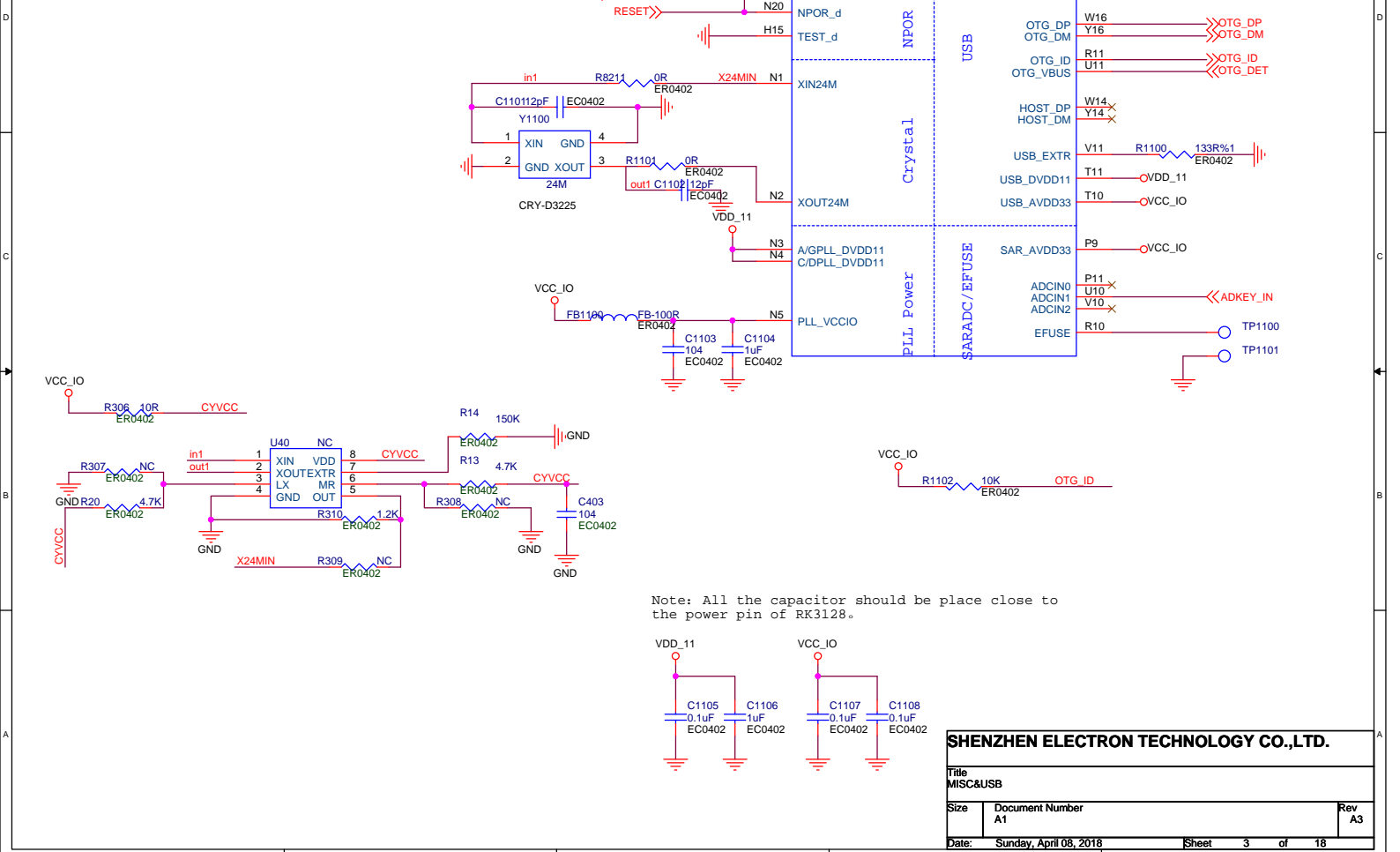


Note:  
Place these filter capacitors under CPU.

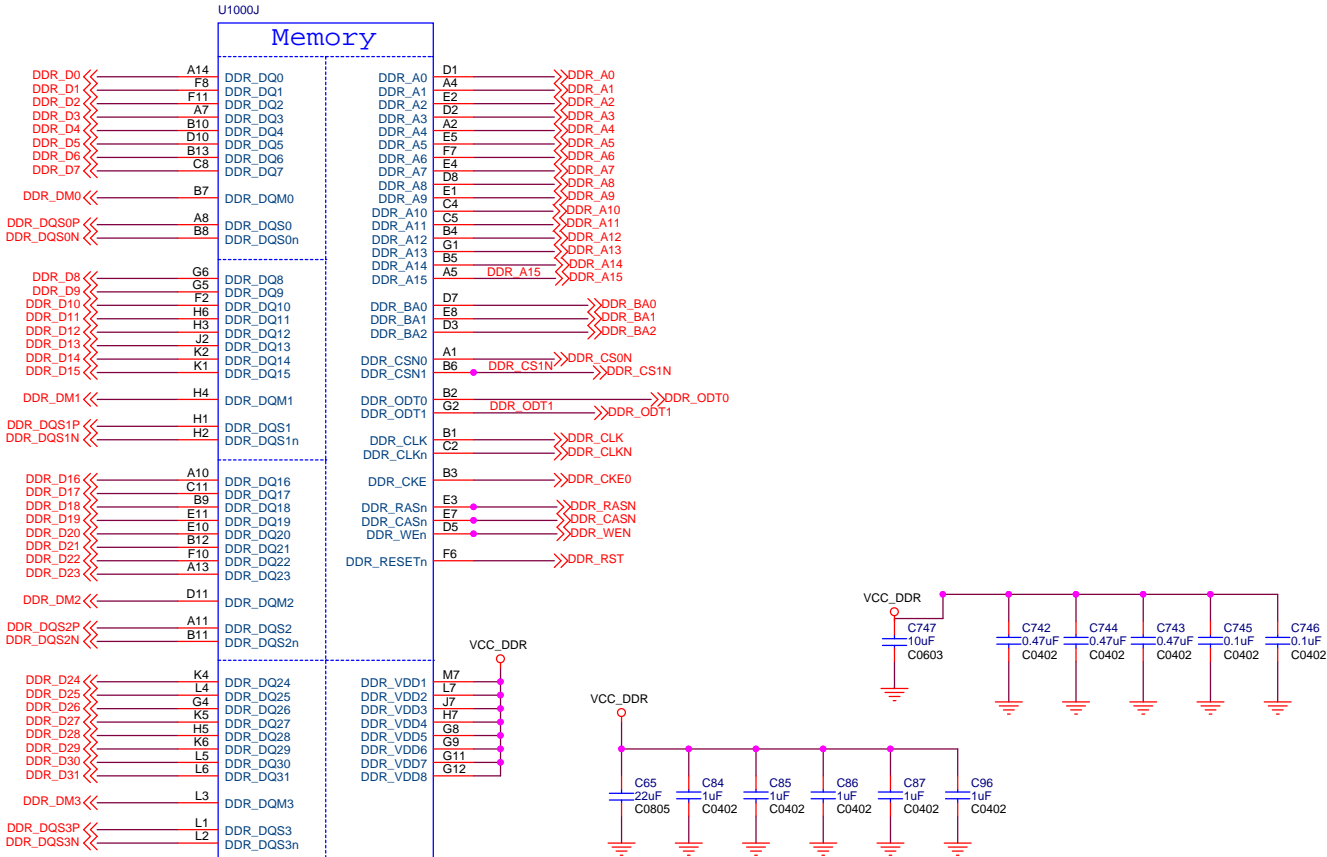


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Title RK3128 POWER		
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Date: Sunday, April 08, 2018	Sheet 2	of 18



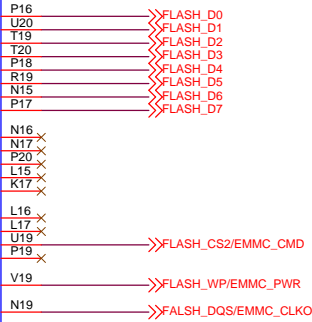
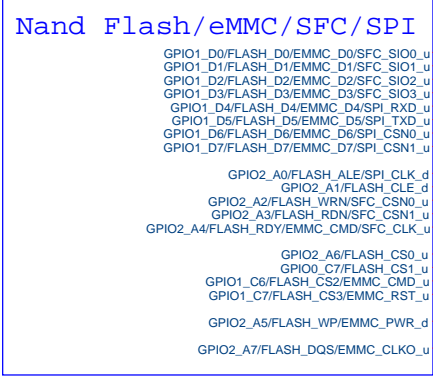
Title MISC&USB			
Size	Document Number A1		Rev A3
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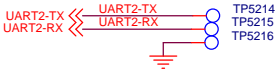
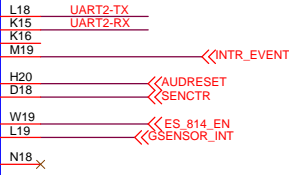
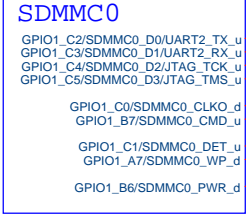
SHENZHEN ELECTRON TECHNOLOGY CO.,LTD.

Title		
RK3128 DDR		
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U1000E  
RK3128



U1000D  
RK3128

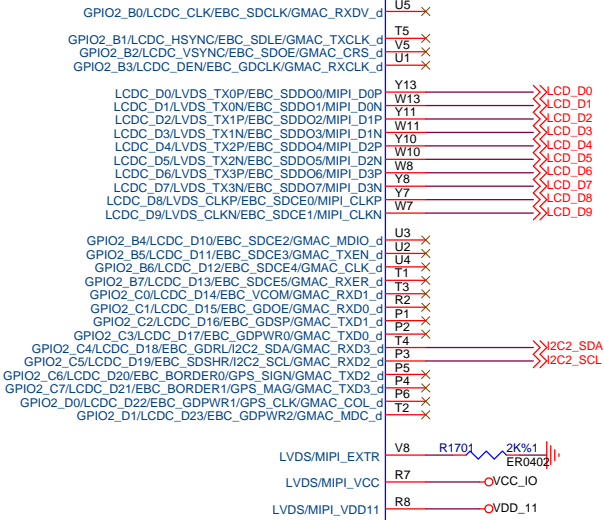


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Title RK3128 FLASH&SD			
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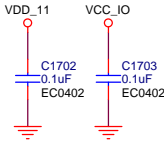
U1000A  
RK3128

LCDC / LVDS / MIPI / GMAC / EMAC



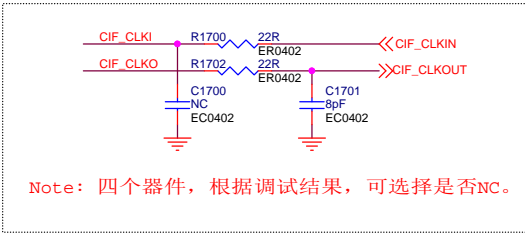
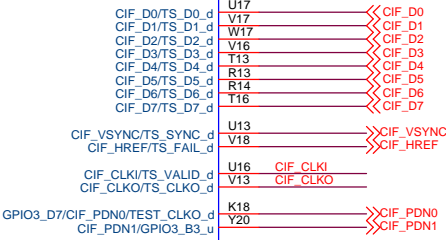
Correspondence between LCDC DATA and RGB

LCDC_D0	B2	LCDC_D9	G5
LCDC_D1	B3	LCDC_D10	G6
LCDC_D2	B4	LCDC_D11	G7
LCDC_D3	B5	LCDC_D12	R2
LCDC_D4	B6	LCDC_D13	R3
LCDC_D5	B7	LCDC_D14	R4
LCDC_D6	G2	LCDC_D15	R5
LCDC_D7	G3	LCDC_D16	R6
LCDC_D8	G4	LCDC_D17	R7



U1000B  
RK3128

CIF / TS



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U10001  
RK3128

GPIO/SmartCard/UART0

GPIO2\_D2/CARD\_RST/UART0\_TX\_d  
GPIO2\_D3/CARD\_CLK/UART0\_RX\_d  
GPIO0\_C1/CARD\_IO/UART0\_RTSN\_u  
GPIO2\_D5/CARD\_DET/UART0\_CTSN\_d

T18 >> UART0\_TXD  
V17 >> UART0\_RXD  
P15 >> UART0\_RTS  
W20 >> UART0\_CTS

GPIO/UART1/SPI

GPIO1\_B1/UART1\_TX/SPI\_TXD\_u  
GPIO1\_B2/UART1\_RX/SPI\_RXD\_u  
GPIO1\_B3/UART1\_RTSN/SPI\_CSN0\_u  
GPIO1\_B0/UART1\_CTSN/SPI\_CLK\_u  
GPIO1\_B4/SPI\_CSN1\_u

H18 >> UART1\_TX  
H19 >> UART1\_RX  
G18 >> PMIC\_INT  
G19 >> TP\_INT  
B19 >> DC\_DET

GPIO/I2S/SPI

GPIO0\_B6/I2S\_SDI/SPI\_CSN0\_u  
GPIO0\_B5/I2S\_SDO/SPI\_RXD\_u  
GPIO0\_B3/I2S\_LRCK\_RX/SPI\_TXD\_u  
GPIO0\_B4/I2S\_LRCK\_TX\_u  
GPIO0\_B1/I2S\_SCLK/SPI\_CLK\_u  
GPIO0\_B0/I2S\_MCLK\_u

K19 >> I2S0\_SDI  
L20 >> I2S0\_SDO0  
J19 >> I2S0\_LRCK\_RX  
H18 >> I2S0\_LRCK\_TX  
K20 >> I2S0\_SCLK  
H17 >> I2S0\_CLK

GPIO/I2S/SDMMC1

GPIO1\_A5/I2S\_SDI/SDMMC1\_D3\_d  
GPIO1\_A4/I2S\_SDO/SDMMC1\_D2\_d  
GPIO1\_A2/I2S\_LRCK\_RX/SDMMC1\_D1\_u  
GPIO1\_A1/I2S\_SCLK/SDMMC1\_D0/PMIC\_SLEEP\_d  
GPIO1\_A0/I2S\_MCLK/SDMMC1\_CLK0/XIN\_32K\_d  
GPIO1\_A3/I2S\_LRCK\_TX\_d

G17 >> SDMMC1\_D3  
G16 >> SDMMC1\_D2  
E19 >> SDMMC1\_D1  
E18 >> SDMMC1\_D0  
E20 >> SDMMC1\_CLK  
G15 >> WIFI\_PWR

GPIO0\_A3/I2C1\_SDA/SDMMC1\_CMD\_u  
GPIO0\_A2/I2C1\_SCL\_u

D17 >> SDMMC1\_CMD  
F19 >> PWR\_KEY

GPIO0\_D6/SDMMC1\_PWR\_d

A20 >> SPK\_CTL

GPIO/I2C0

GPIO0\_A1/I2C0\_SDA\_u  
GPIO0\_A0/I2C0\_SCL\_u

E17 >> I2C0\_SDA  
D20 >> I2C0\_SCL

GPIO/SPDIF/IR

GPIO3\_C7\_u  
GPIO3\_C6\_u  
GPIO3\_C5\_d  
GPIO3\_C4\_d

G20 >> WIFI\_HOST\_WAKE  
C18 >> BT\_HOST\_WAKE  
F13 >> BT\_RST  
D19 >> HOST\_DRV

GPIO3\_C1/DRIVE\_VBUS/PMIC\_SLEEP\_d

U14 >> PMIC\_SLEEP

GPIO0\_D0/UART2\_RTSN/PMIC\_SLEEP\_u  
GPIO0\_D1/UART2\_CTSN\_u

C17 >> PMIC\_SLEEP  
A19 >> TP\_RST >> HHREST

GPIO3\_D3/SPDIF\_u

Y19 >> WIFI\_REG\_ON

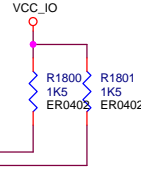
GPIO3\_D2/IR\_u

W18 >> BT\_WAKE

PWM

GPIO0\_D2/PWM0\_d  
GPIO0\_D3/PWM1\_d  
GPIO0\_D4/PWM2\_u

U18 >> LCD0\_BL  
T17 >> BL\_EN  
V14 >> BL\_EN



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Title  
RK3128 GPIO

Size Document Number  
A1

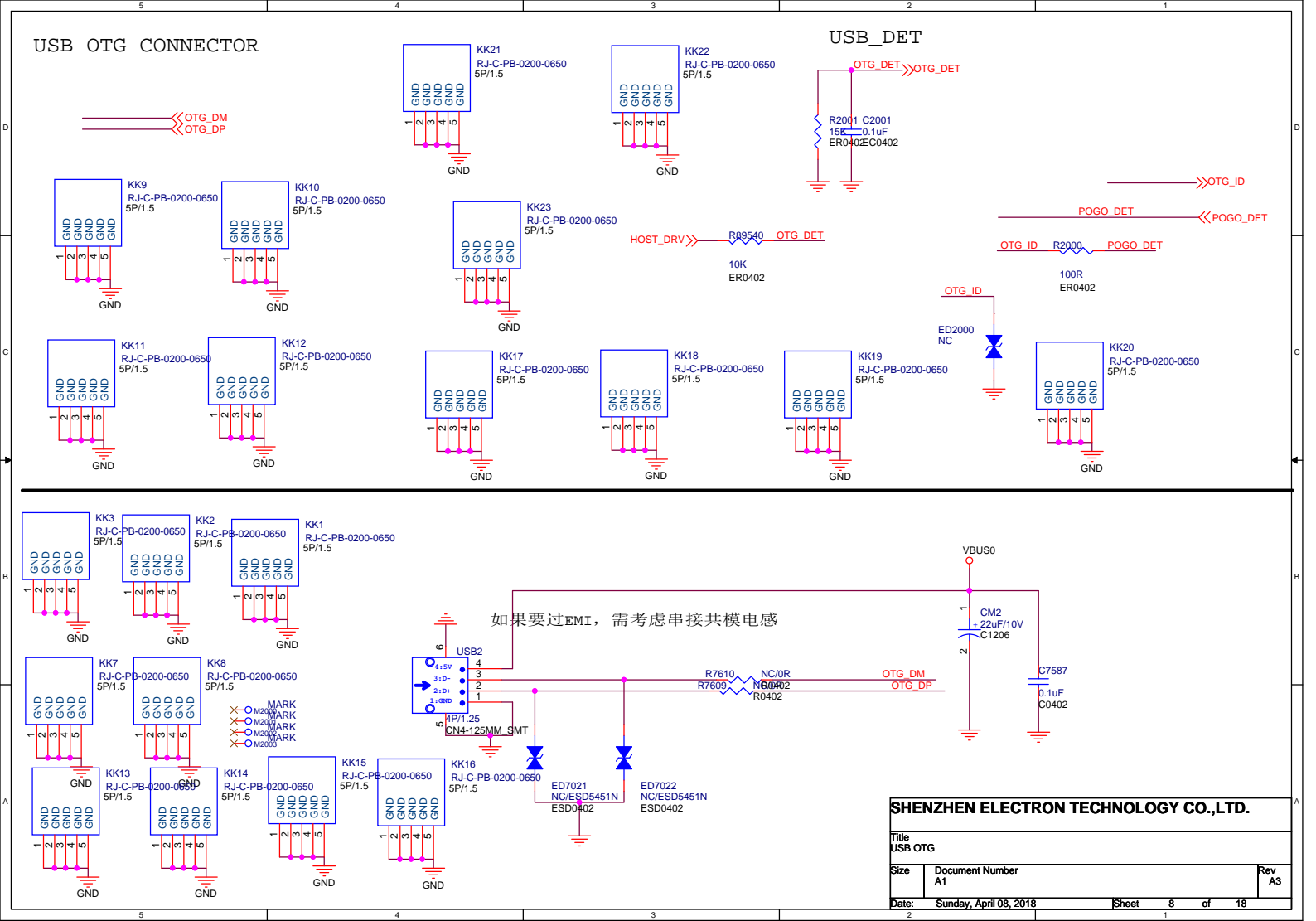
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USB OTG CONNECTOR

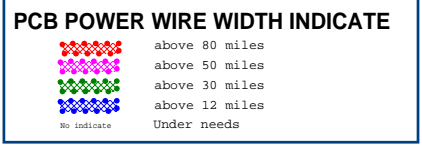
USB\_DET



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Title USB OTG		
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Only the USB Port  
VCC5\_USB connected to the VCC\_RK819INOUT

VBUS0  
C8243 10uF  
XGR 10V  
EC0805

VBUS0  
C8244 10uF  
XGR 10V  
EC0805

VBUS0  
C8239 22uF/10V  
C8240 0.1uF  
C8241 22uF/10V  
C8242 0.1uF

VBUS0  
C2301 10uF  
XGR 10V  
EC0603

VBUS0  
D2 ESD5V  
SQD323

VBUS0  
R2302 47K  
ERO402

VBUS0  
Q2302 S8050  
SOT23

VBUS0  
R2306 51K  
ERO402

VBUS0  
C232 22uF/10V  
EC1206

VBUS0  
C230 22uF/10V  
EC1206

VBUS0  
C231 104  
EC0402

MP1495

BST 5  
SW 1  
FB 3

R178 10R  
ERO402

R179 33K/1%  
ERO402

C229 104  
EC0402

R180 101K/1%  
ERO402

R181 36K/1%  
ERO402

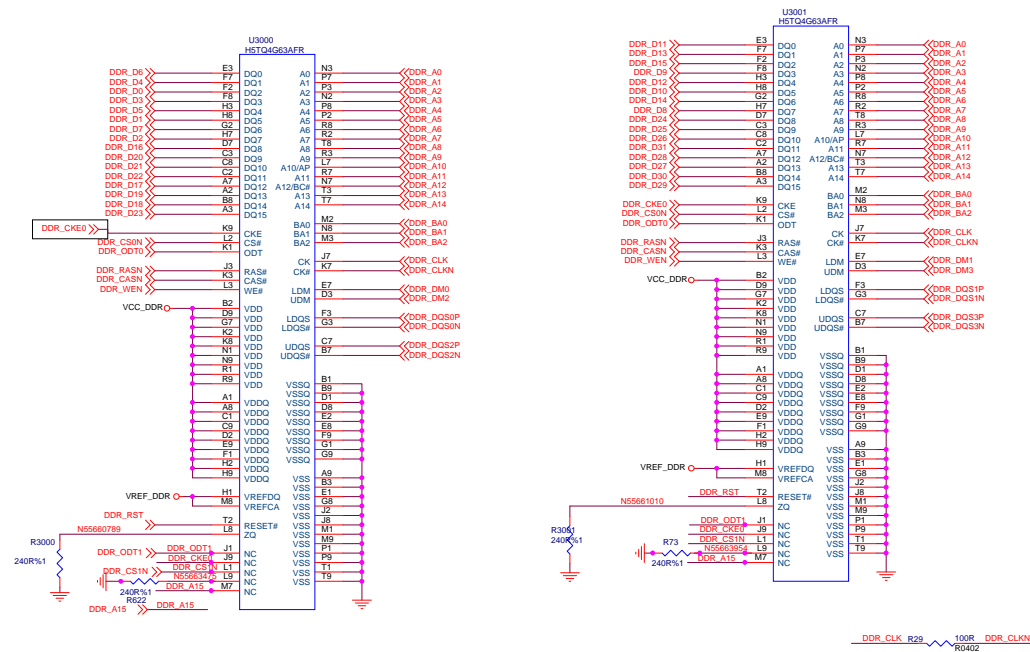
R182 10K/1%  
EC0402

STP

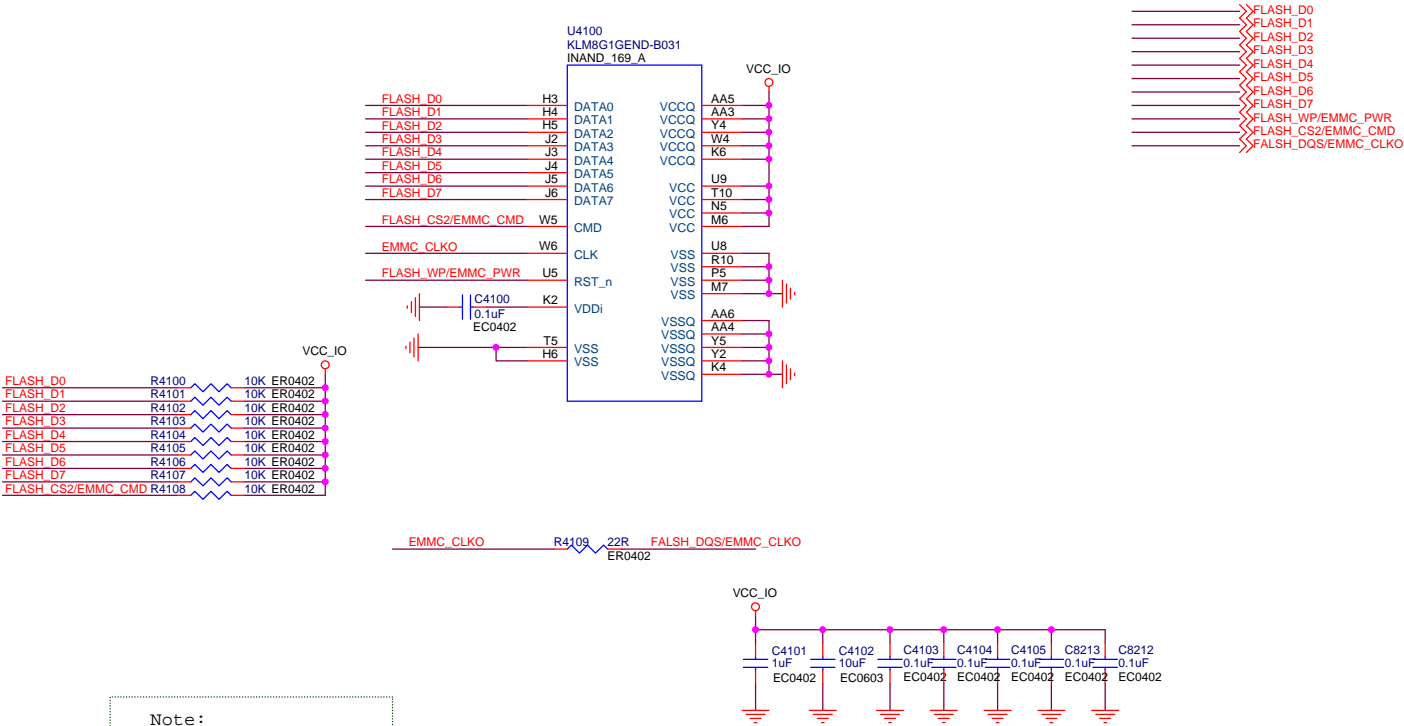
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Title		Power-RK818	
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		Rev	A3

# DDR3

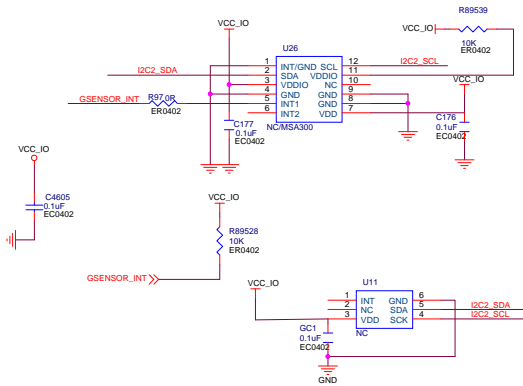


eMMC FLASH



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Title		
Memory-eMMC Flash(option)		
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When the system power on, the Adkey\_in level is 0V, RK2928 enter into loader mode.

# LVDS Panel

The schematic diagram illustrates the LVDS Panel interface circuit. It includes the following components and connections:

- Power Supply:** VCC\_SYS, VCC\_LEDA, VCC\_LEDK, and VCC\_LCD are connected to the microcontroller (U5100) and the panel (T5101-T5110). The VCC\_LCD section includes a 4.7kΩ resistor (R5116) and a 10kΩ resistor (R5117).
- Signal Conditioning:** The LVDS signals (D0-D9, D0N-D9N, D0P-D9P, D0PN-D9PN, D0M-D9M, D0MN-D9MN, D0Q-D9Q, D0QN-D9QN, D0R-D9R, D0RN-D9RN, D0S-D9S, D0SN-D9SN, D0T-D9T, D0TN-D9TN, D0U-D9U, D0UN-D9UN, D0V-D9V, D0VN-D9VN, D0W-D9W, D0WN-D9WN, D0X-D9X, D0XN-D9XN, D0Y-D9Y, D0YN-D9YN, D0Z-D9Z, D0ZN-D9ZN) are connected to the panel (T5101-T5110) and the microcontroller (U5100).
- Panel Connector:** The panel connector (J8103) is connected to the panel (T5101-T5110) and the microcontroller (U5100).
- Panel Cable:** The panel cable (VR1) is connected to the panel (T5101-T5110) and the microcontroller (U5100).

The diagram also shows the connection to a panel connector (J8103) and a panel cable (VR1).

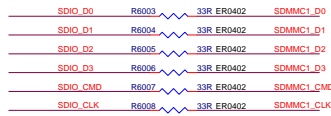
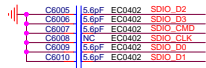
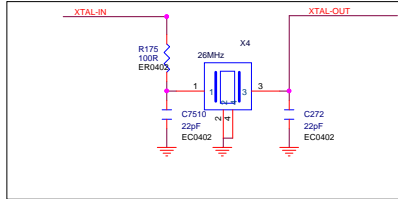
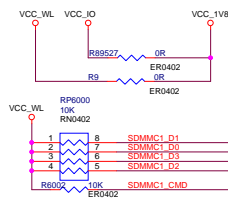
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File: CM-LVDS Panel (option)  
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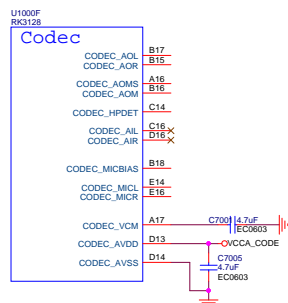
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LCM-LVDS Panel (option)			
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Note:  
F23BDSM23-W2 VCC\_WL=3.2~3.6V  
F23BDSM25-W1 VCC\_WL=3.5~5.0V

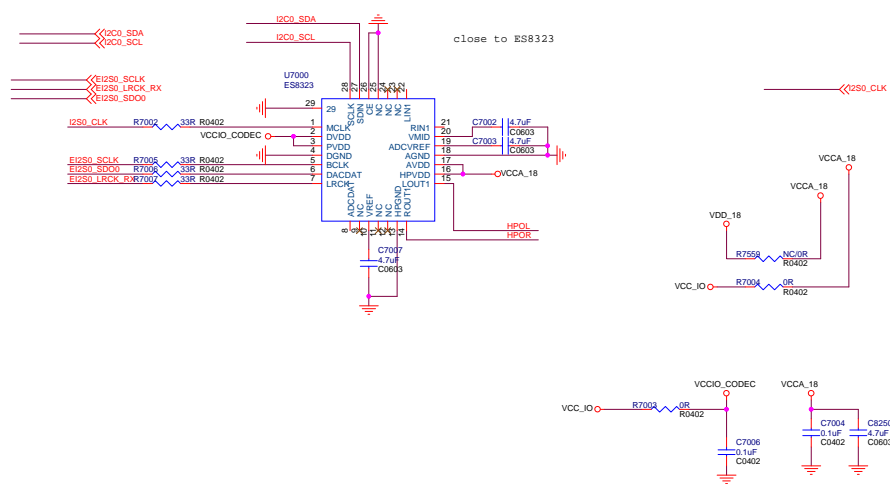
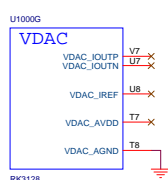
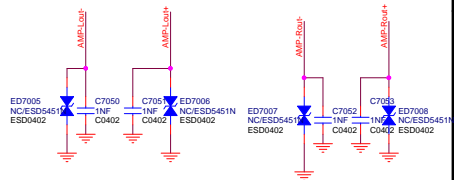
Note:  
F23BDSM23-W2 VCC\_WL=3.2~3.6V  
F23BDSM25-W1 VCC\_WL=3.5~5.0V



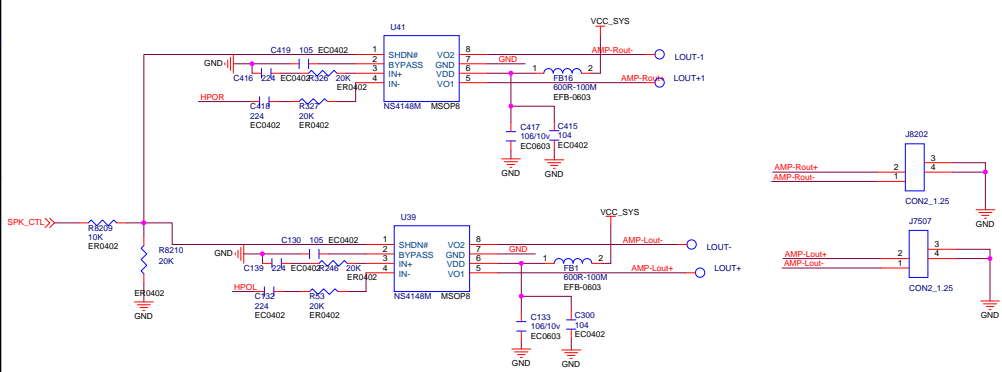
Title RK3128_WIFI			
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ESD

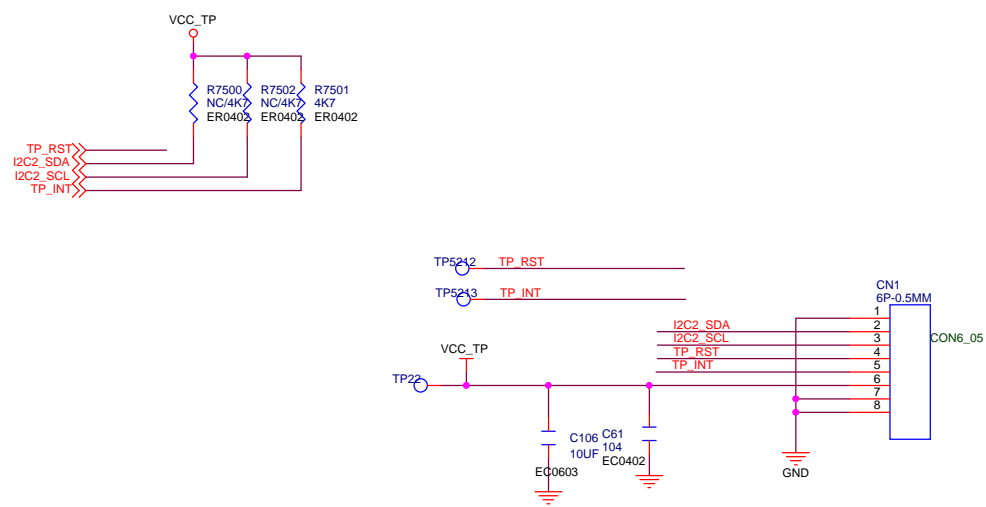


SPEAKER DRIVER



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Title AUDIO			
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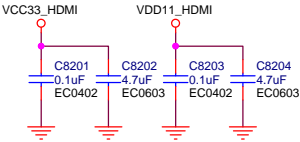
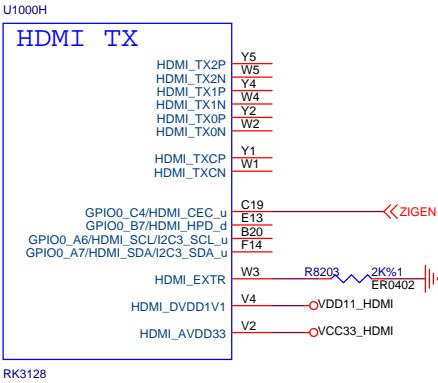
# TOUCH CONNECTOR



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Title HDMI			
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