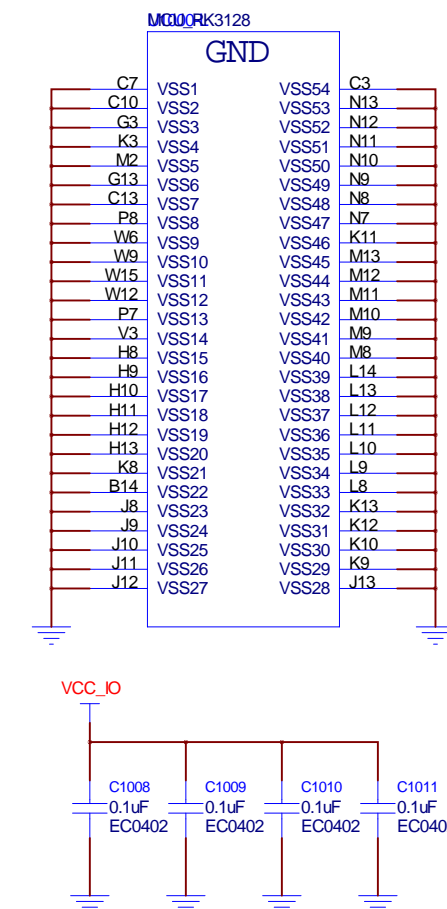
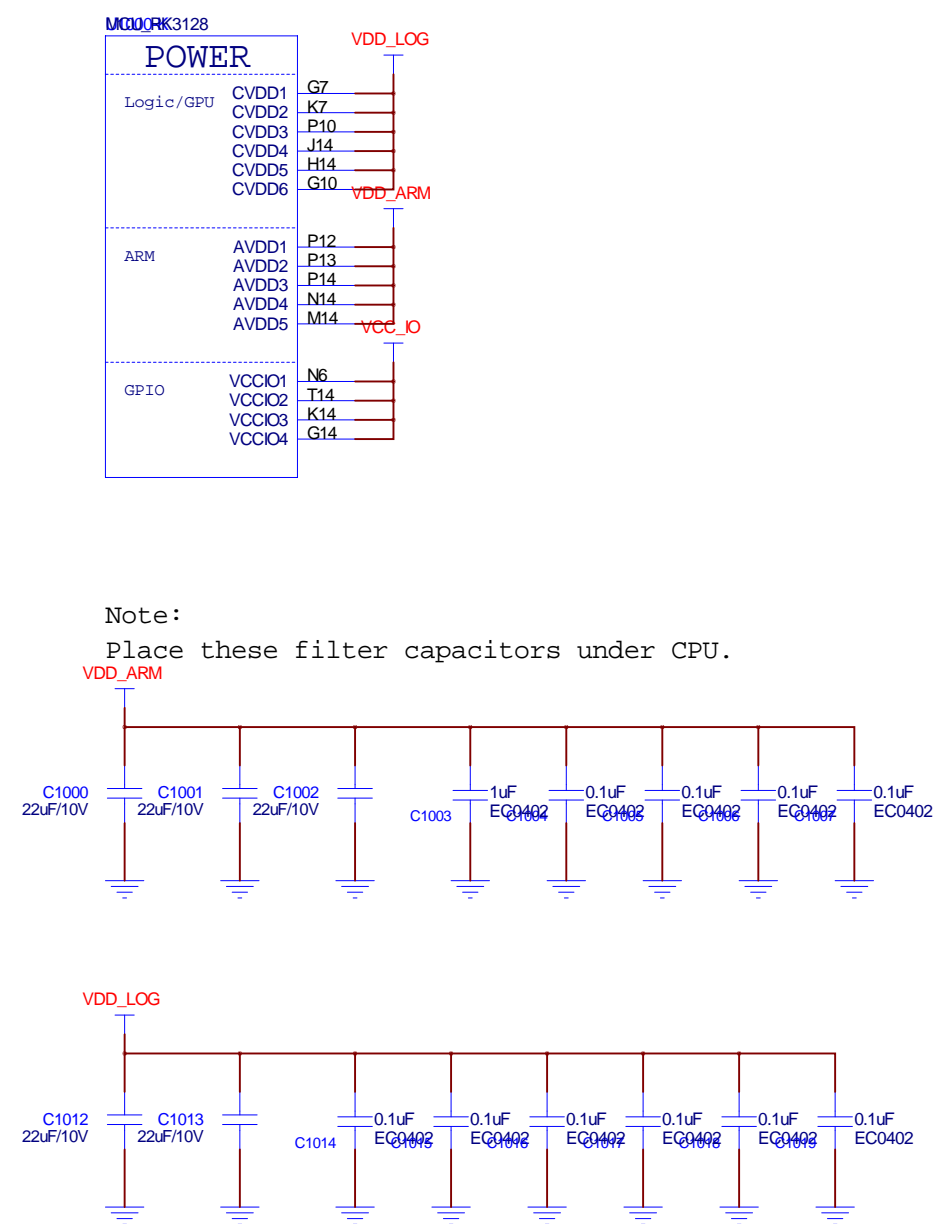
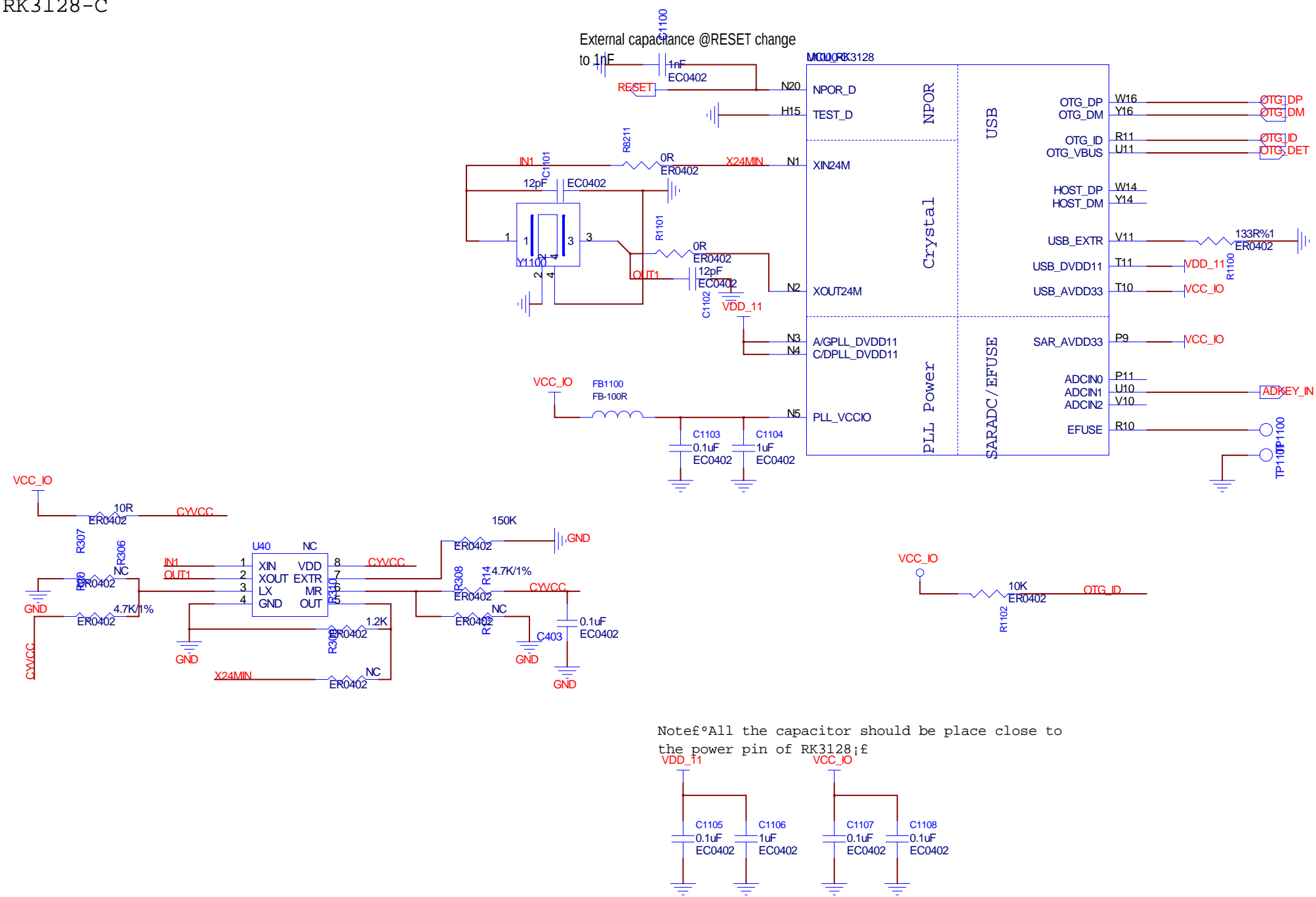


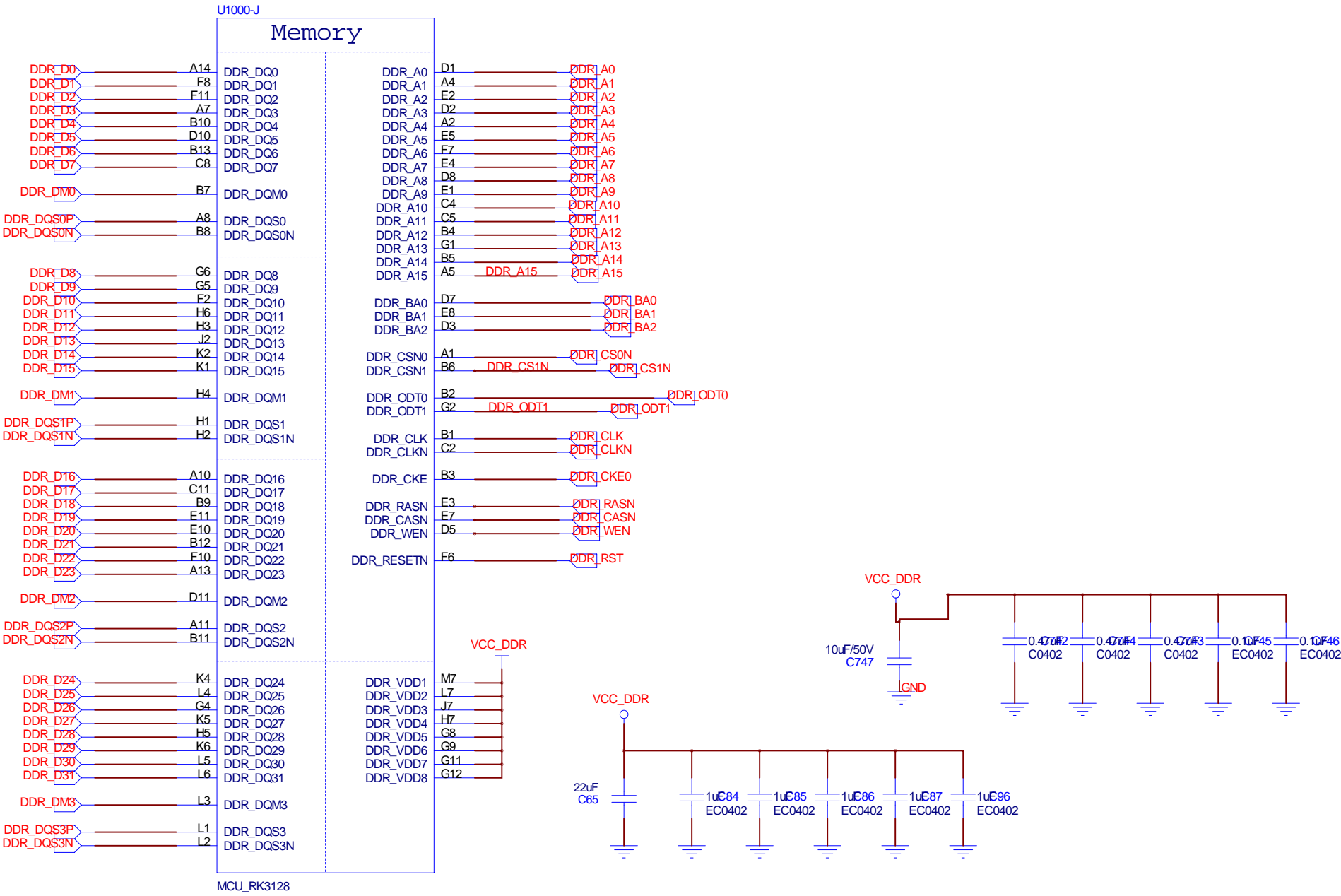
MICROPHOTO					
Title MICROPHOTO					
Size A2	Document Number A1	Author	ÖAÄİ	Revision	V1.1
Date: Tuesday, January 02, 2018	Sheet 1 of 18				

RK3128-L



RK3128-C

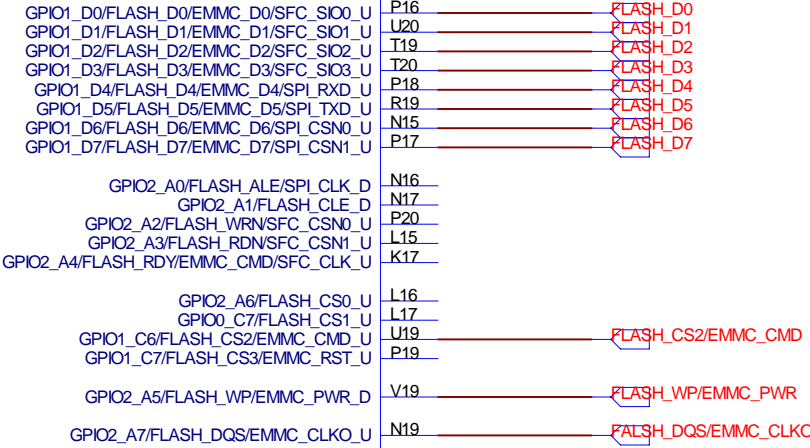




RK3128-D

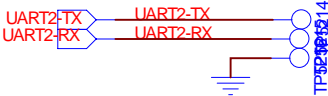
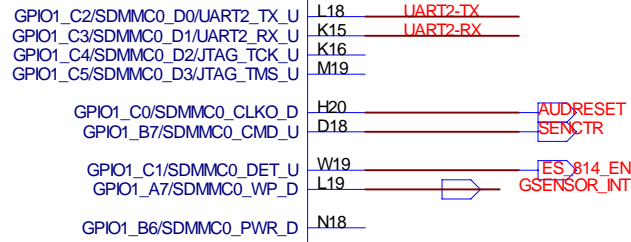
MCU:RK3128

Nand Flash/eMMC/SFC/SPI



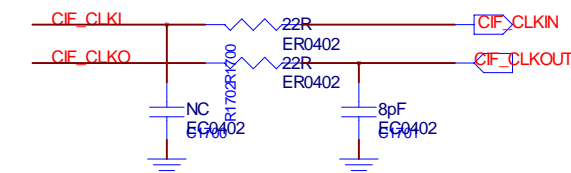
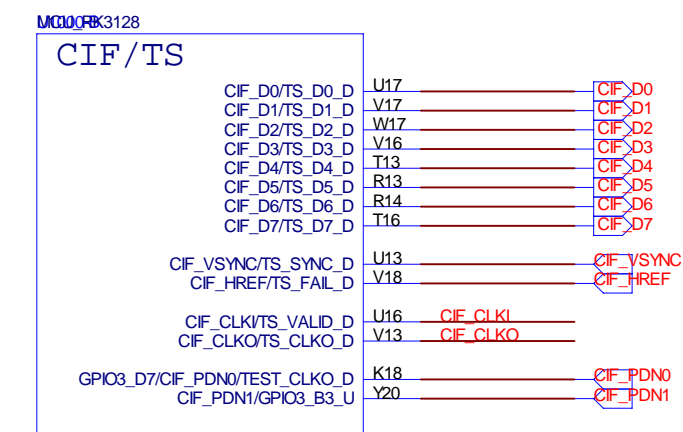
MCU:RK3128

SDMMC0



RK3128_FLASH/SD_CONTROLLER					
Title RK3128_FLASH/SD_CONTROLLER					
Size	Document Number	Author	ÖAÄİ	Revision	V1.1
A2	A.1				
Date:	Tuesday, January 02, 2018	Sheet	5	of	18

RK3128-B



Correspondence between LCDC DATA and RGB

Model: RK3128

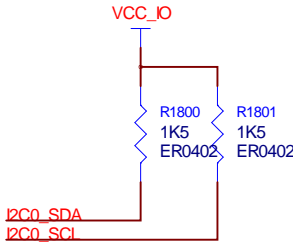
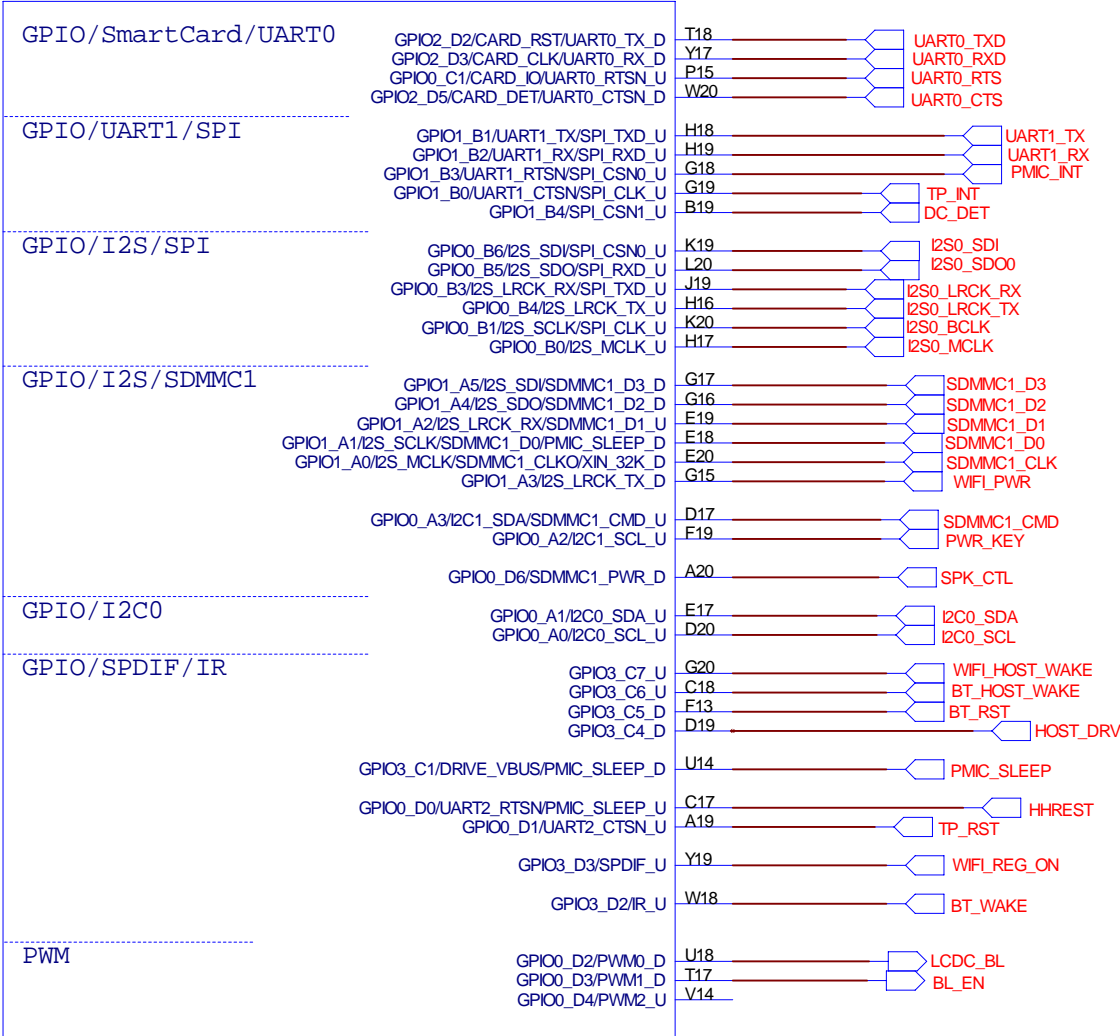
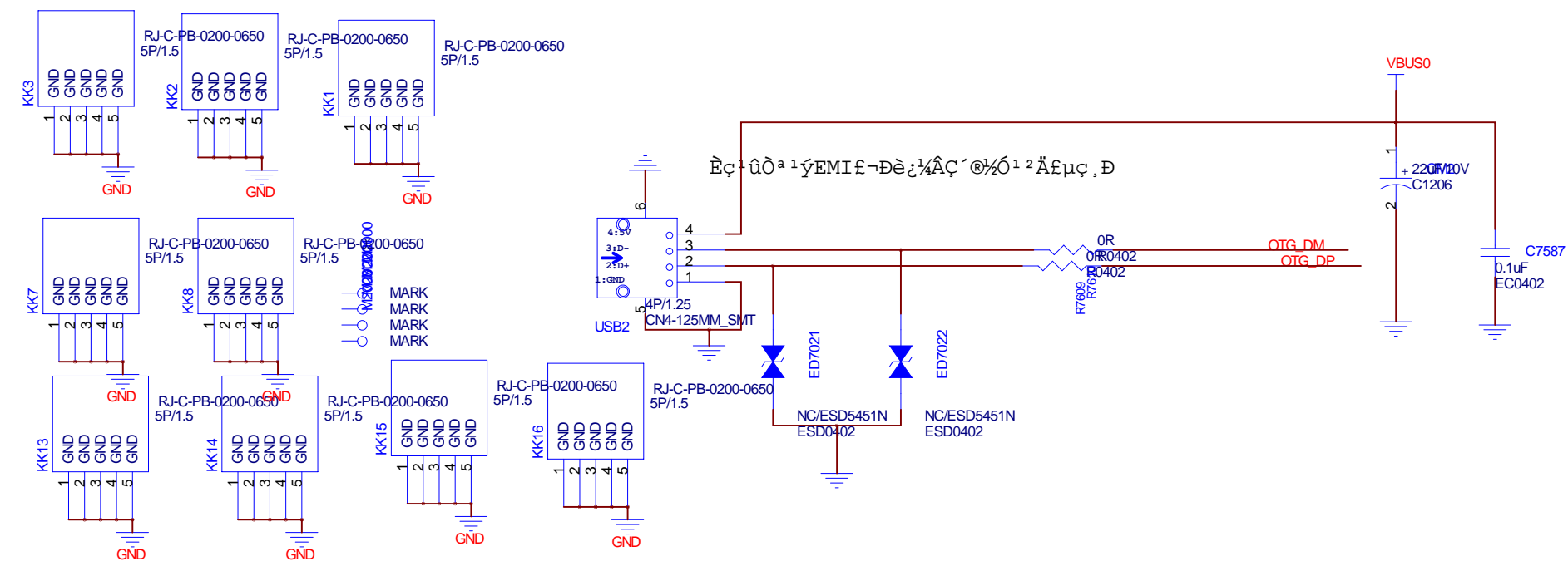



Diagram showing four RJ-C-PB-0200-0650 5P/1.5 connectors (KK9, KK10, KK11, KK12) connected to a common ground. The top two connectors are labeled OTG\_DM and OTG\_DP. The bottom two are labeled OTG\_DM and OTG\_DP. Each connector has five pins, with pins 1, 2, and 3 connected to ground.

The schematic diagram illustrates the internal circuitry for several USB connector pins:

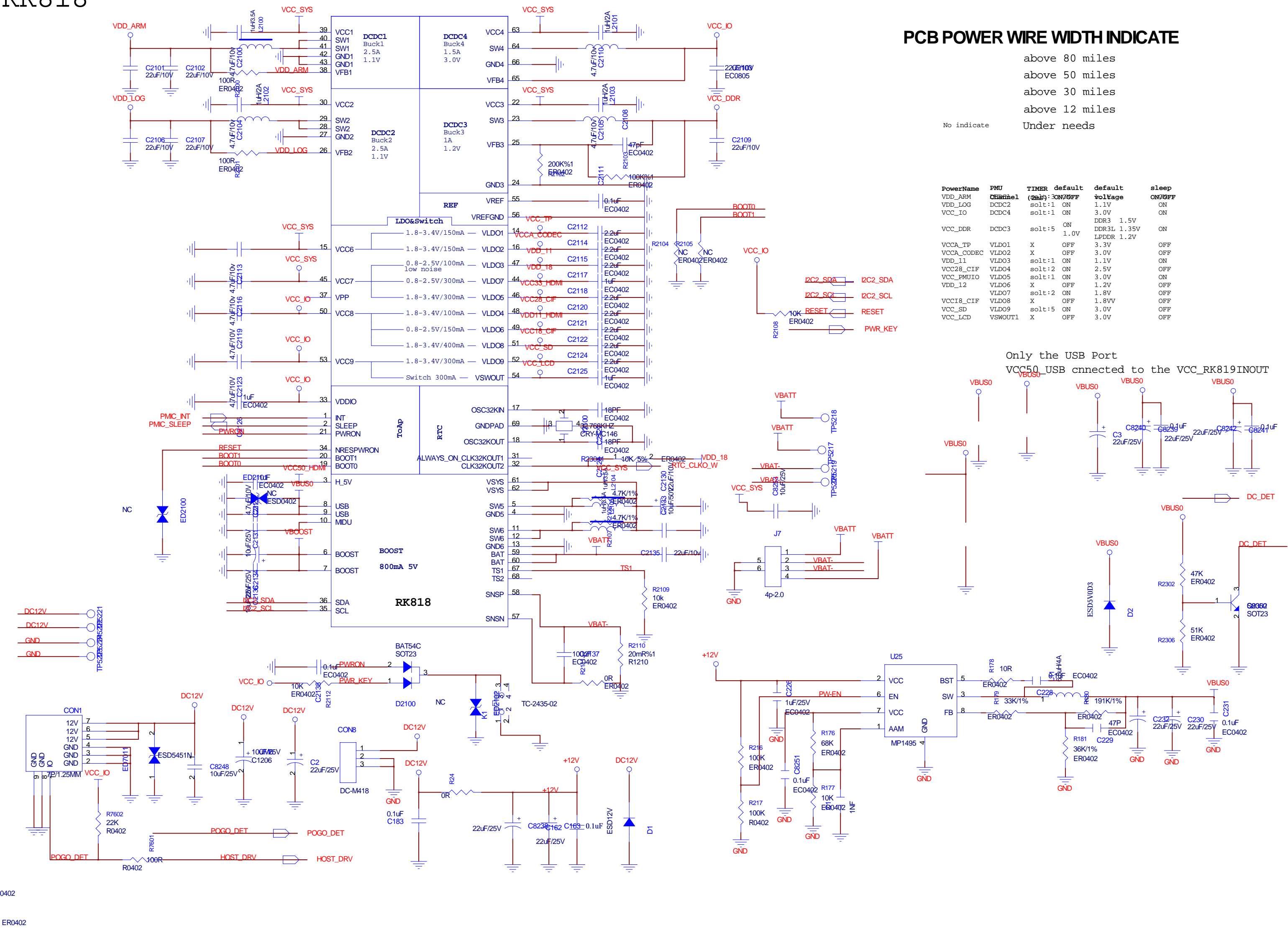
- OTG\_DET**: Connected to a pull-up resistor **R2001** (15K) and a capacitor **C2001** (0.1μF), both connected to ground.
- HOST\_DRV**: Connected to a pull-up resistor (10K) and a capacitor (0.1μF), both connected to ground.
- POGO\_DET**: Connected to a pull-up resistor (100R) and a capacitor (0.1μF), both connected to ground.
- NC**: Not Connected, shown as a pin without internal circuitry.
- ED2000**: A diode component connected to ground.



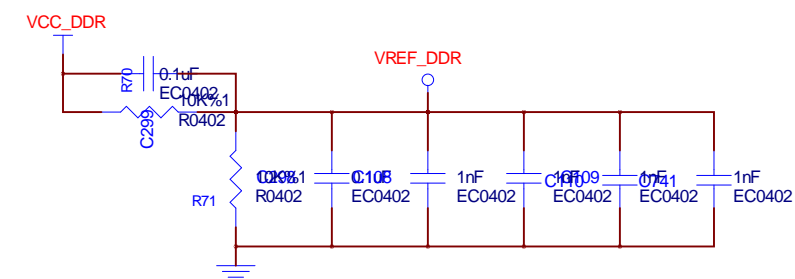
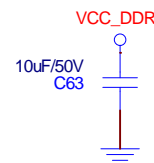
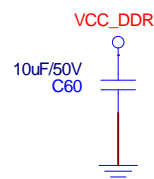
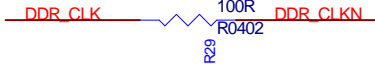
<div style="text-align: center;">  <p> <b>AZƏRBAYCAN RESPUBLİKASI</b>  <b>ƏLİMLİK, TƏBİQİYYAT VƏ</b>  <b>ƏSƏRİYYAT VƏZIRLIĞI</b>  <b>YÜKSEK TƏBİQİYYAT VƏ</b>  <b>ƏSƏRİYYAT VƏZIRLIĞI</b> </p> </div>				
<div style="text-align: center;"> <b>Title</b> </div> <div style="text-align: center;"> <b>USB_PORT</b> </div>				
<div style="text-align: center;"> <b>Size</b>  <b>A2</b> </div>	<div style="text-align: center;"> <b>Document Number</b>  <b>A1</b> </div>	<div style="text-align: center;"> <b>Author</b>  <b>Ö.Ə.İ.</b> </div>	<div style="text-align: center;"> <b>Revision</b>  <b>V1.1</b> </div>	<div style="text-align: center;"> <b>Date:</b> </div> <div style="text-align: center;"> <b>Tuesday, January 02, 2018</b> </div>
<div style="text-align: center;"> <b>Sheet</b> </div> <div style="text-align: center;"> <b>8</b> </div>		<div style="text-align: center;"> <b>of</b> </div> <div style="text-align: center;"> <b>18</b> </div>		



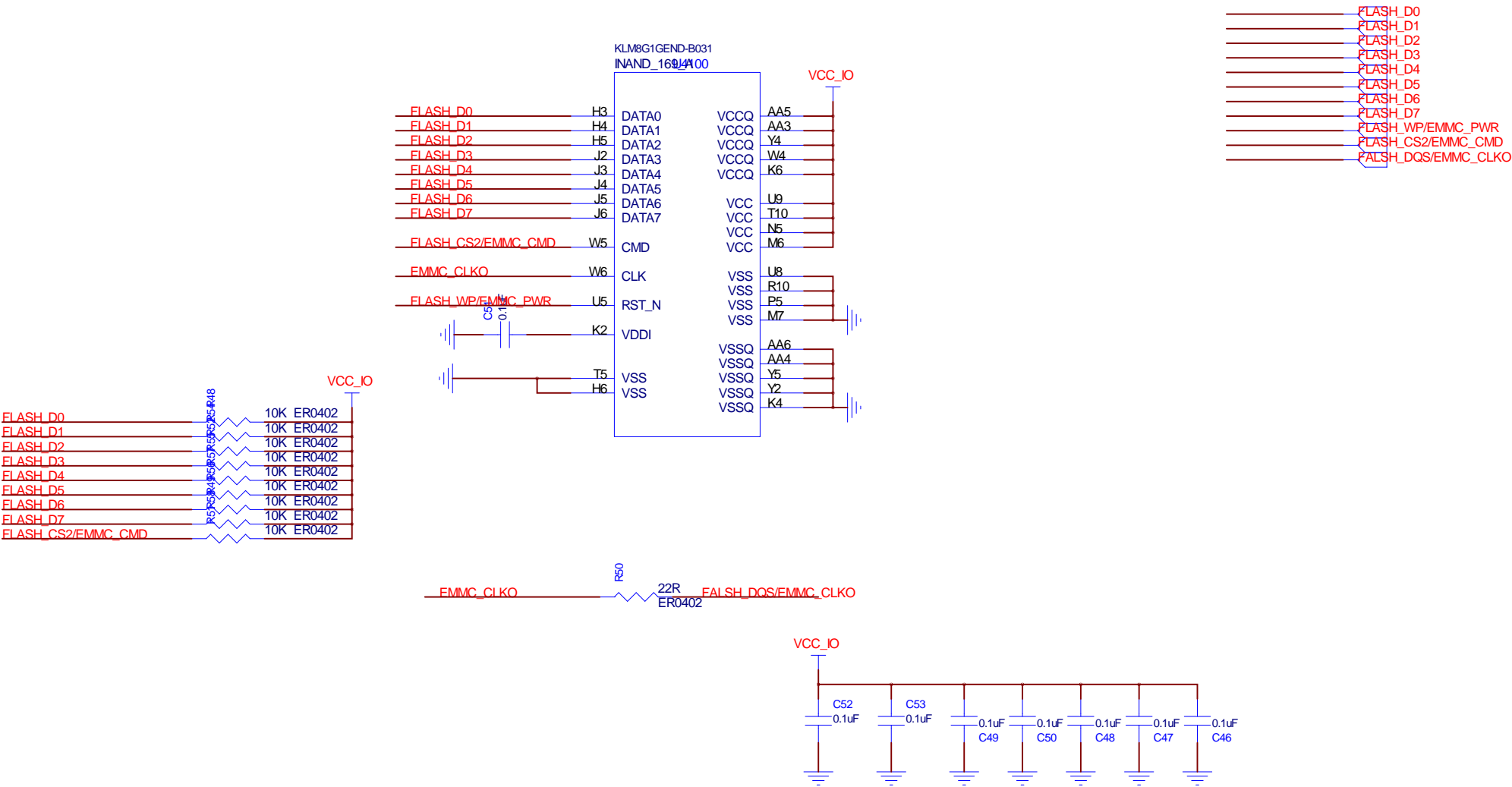
RK818



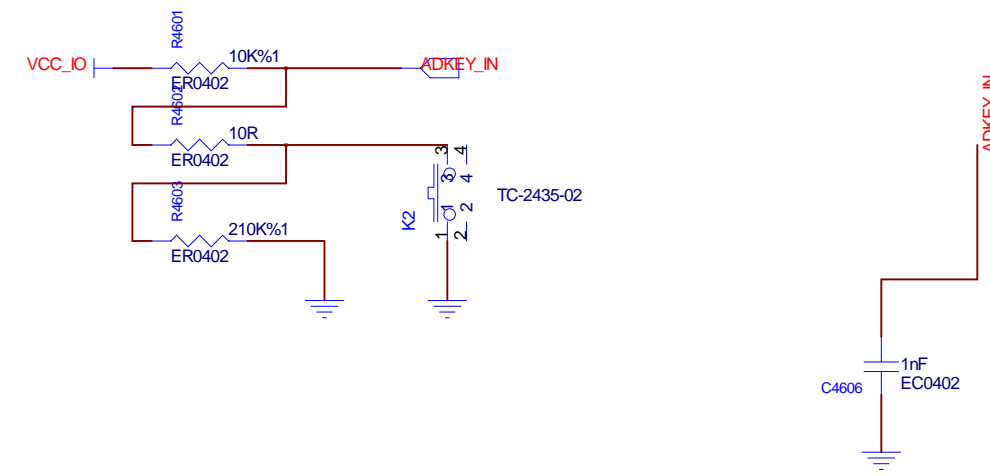
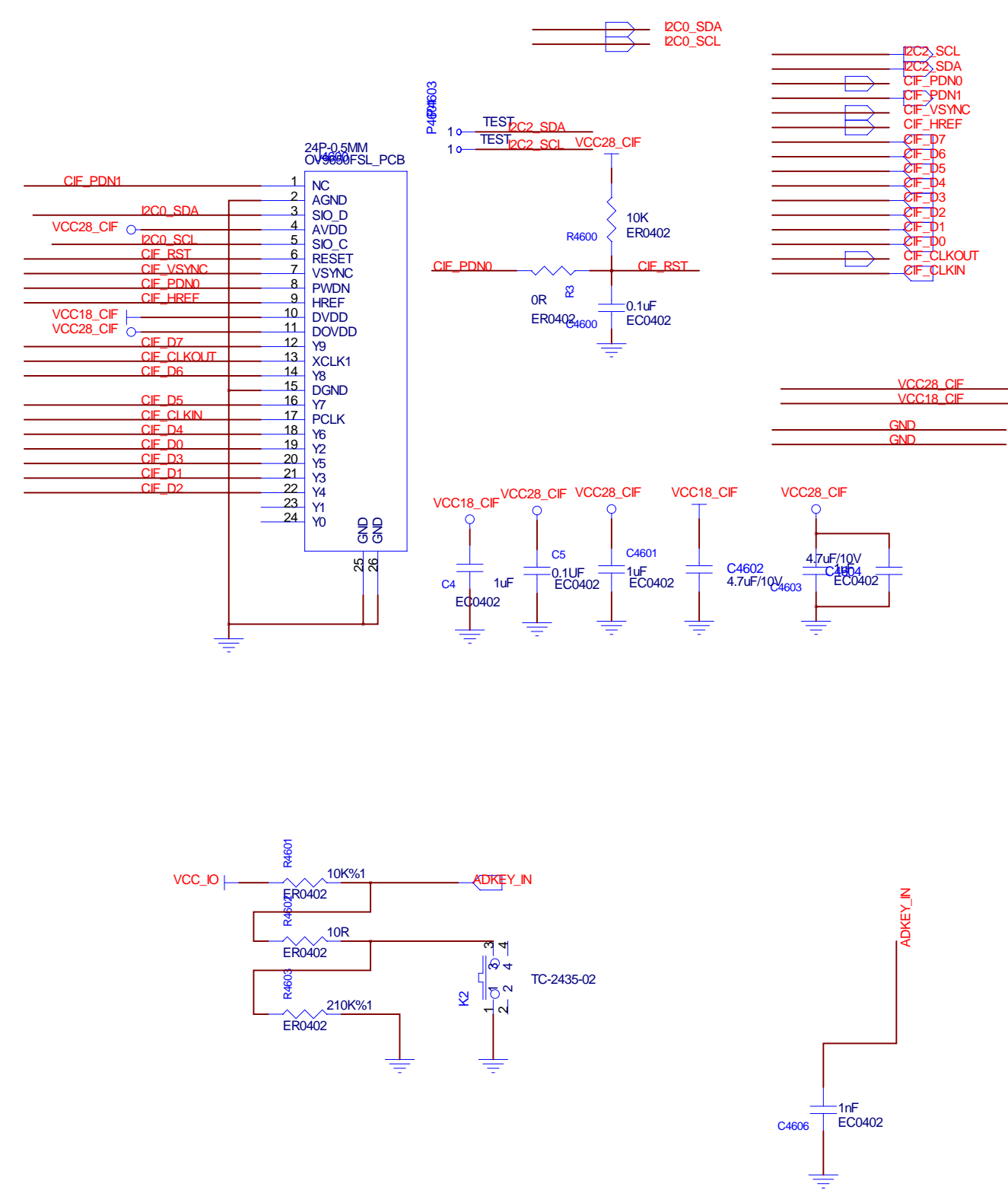
## DDR3



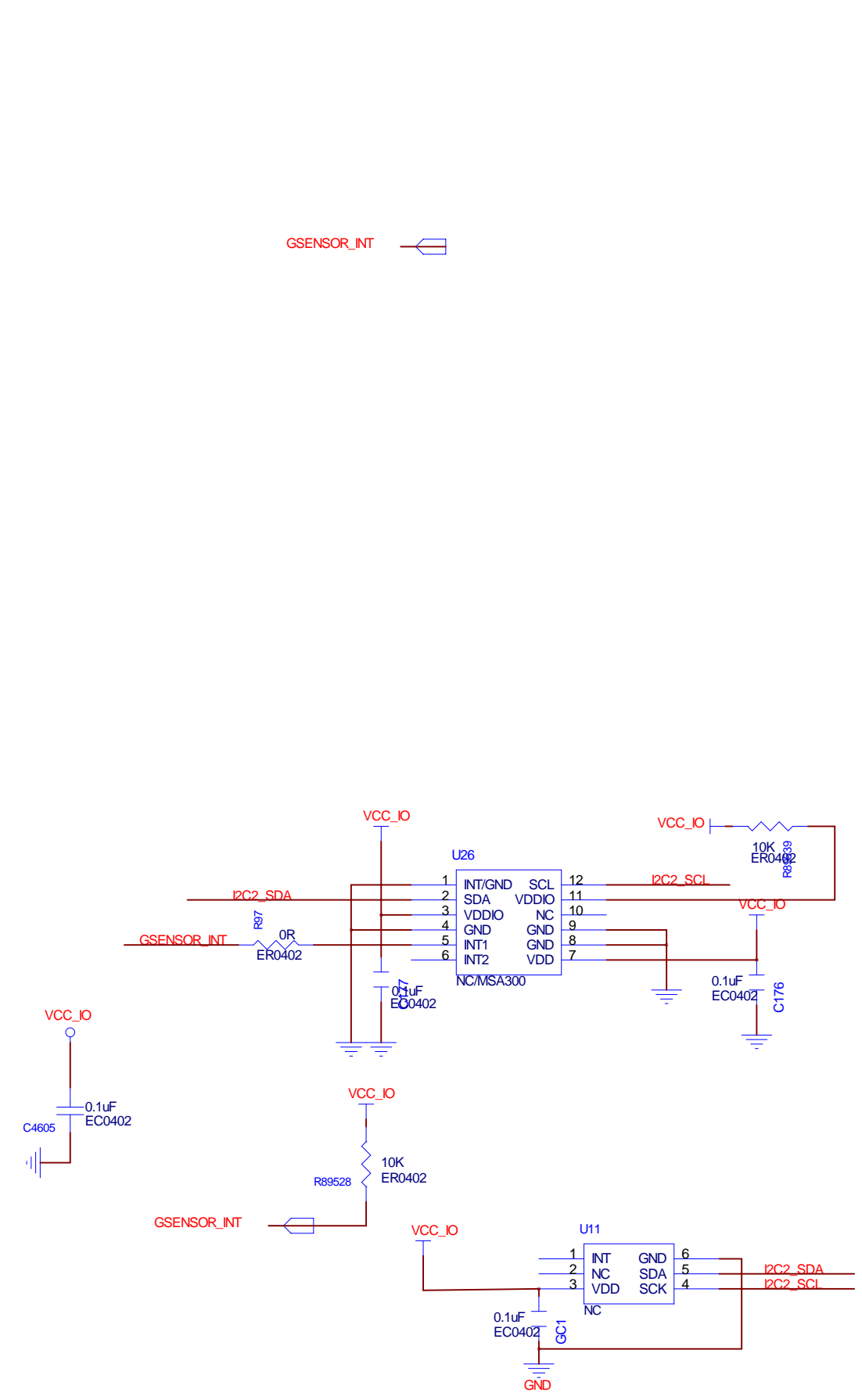
eMMC FLASH



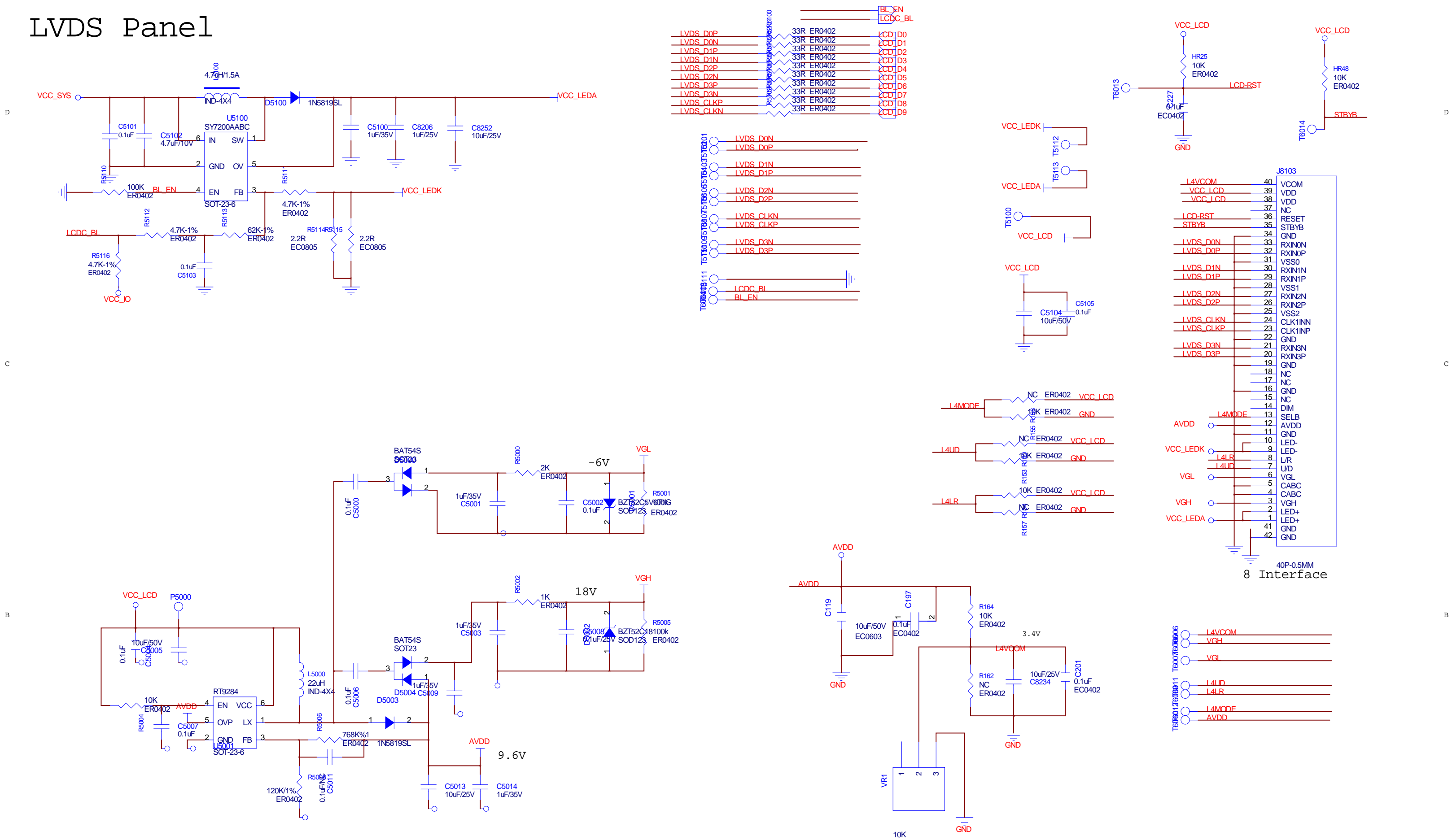
Note:  
NC or reserve a PAD.



When the system power on, the Adkey\_in level is 0V,  
RK2928 enter into loader mode.



# LVDS Panel



Document Number

Title RK3128 POWER

Size A2

Document Number A1

Author

Revision

V1.1

Date: Tuesday, January 02, 2018

Sheet 13

of 18

## WIFI/WIFI

Note:

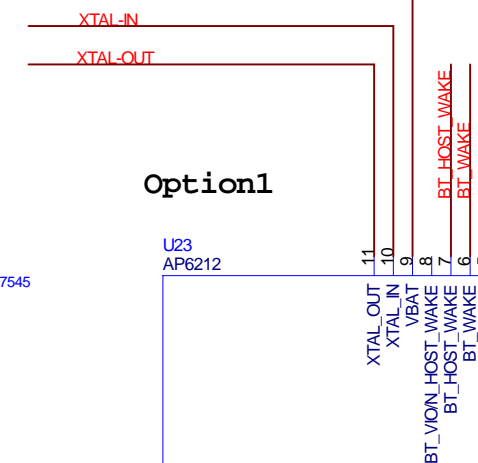
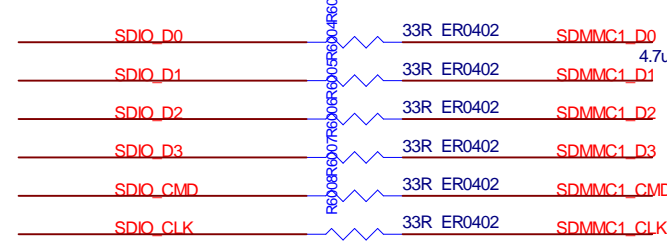
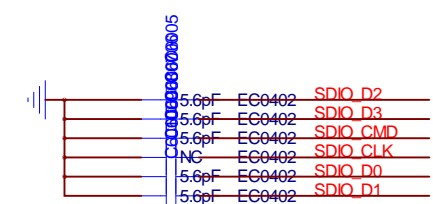
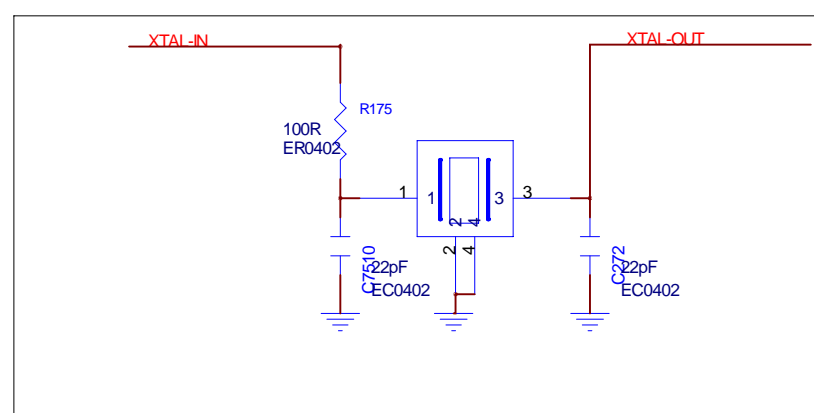
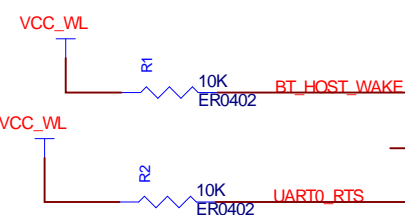
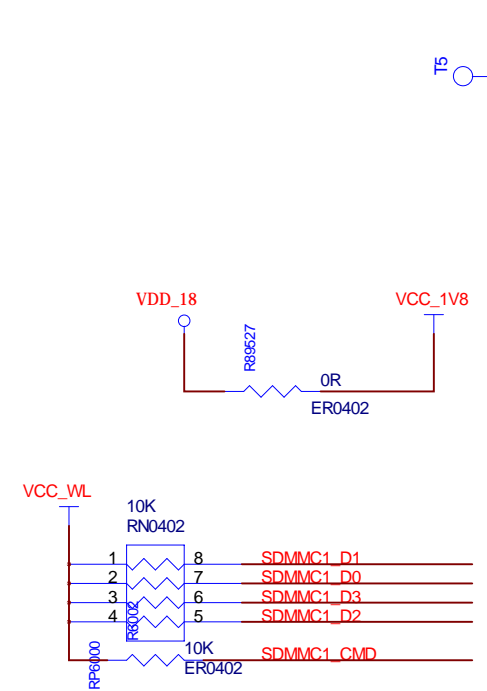
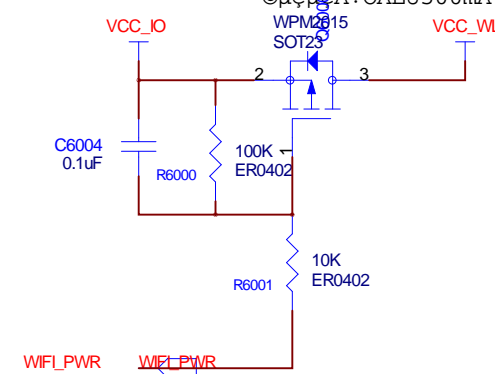
F23BDSM23-W2 VCC\_WL=3.2~3.6V

F23BDSM25-W1 VCC\_WL=3.5~5.0V

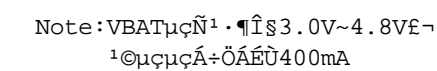
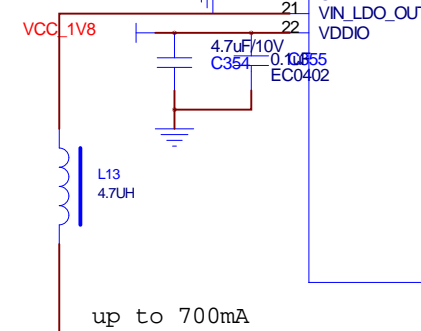
Note: VBAT $\mu$ çN<sup>1</sup> • ¶Î§3.2V~3.6V£¬

<sup>1</sup>©μçμgÁ÷ÖÁÉÛ500mA

WPM2615  
SOT23

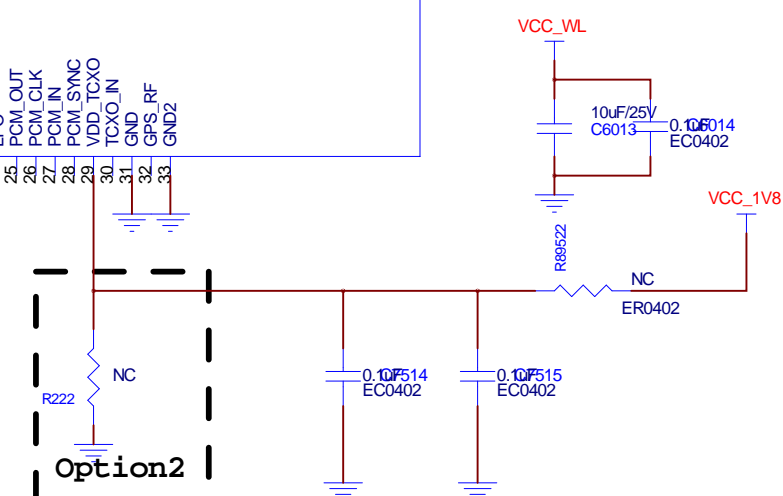
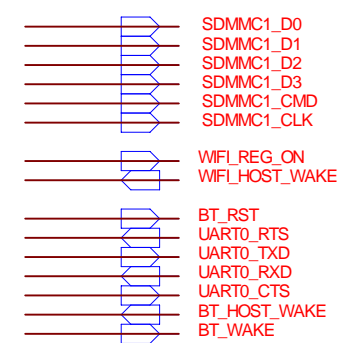
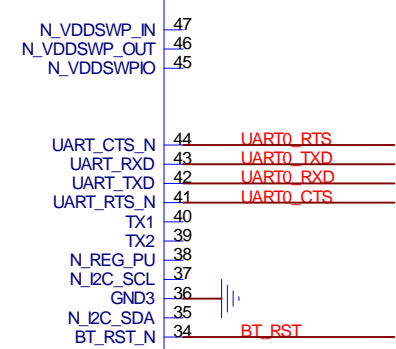
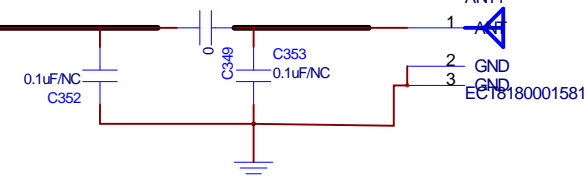


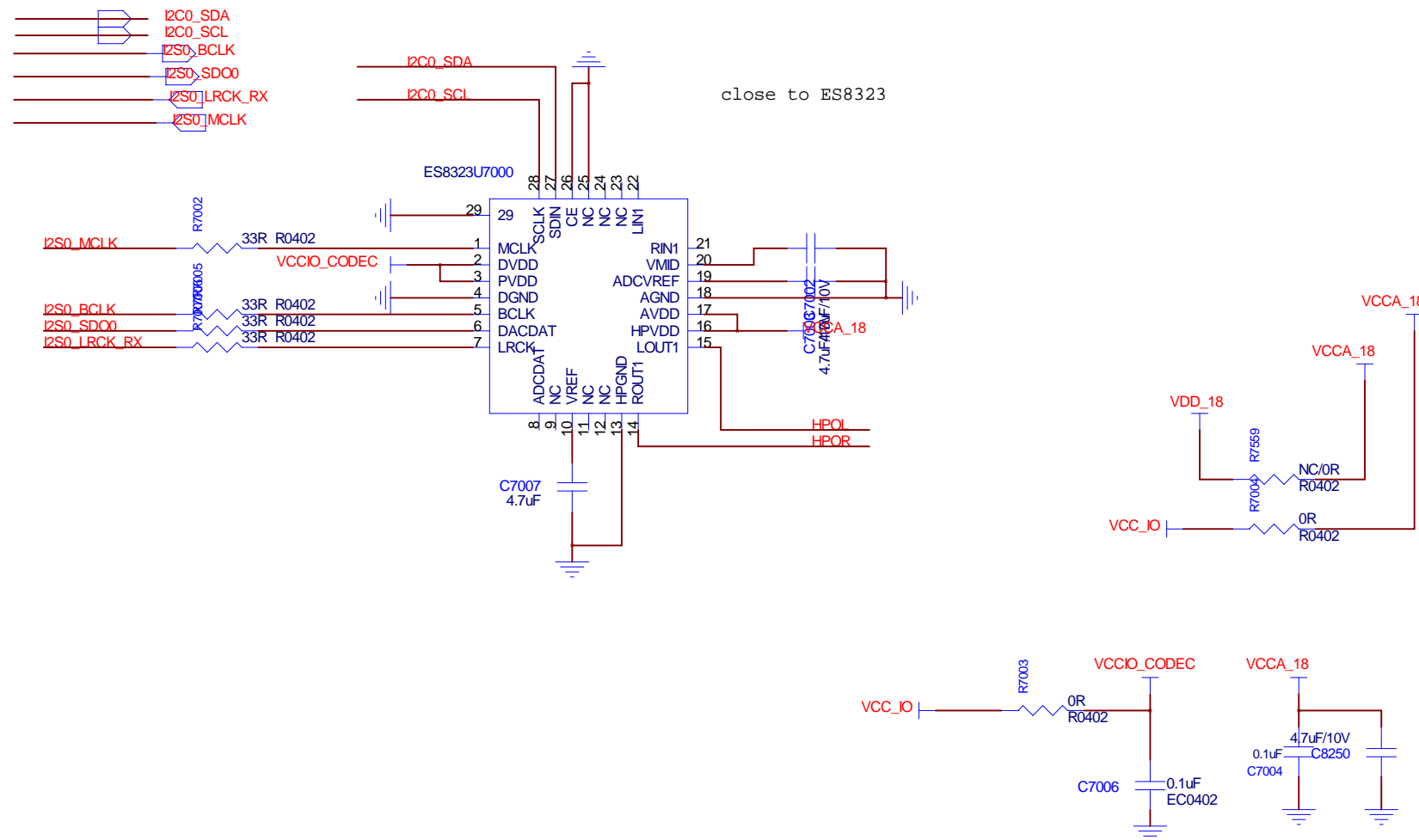
Option1



Note: VBAT $\mu$ cN<sup>1</sup> • ¶Î§3.0V~4.8V£¬  
<sup>1</sup>©μçμçÃ÷ÖÃËÛ400mA

50 Ohm RF trace



[illegible]

U1000-G

VDAC

VDAC\_IOUTP V7

VDAC\_IOUTN U7

VDAC\_IREF U8

VDAC\_AVDD T7

VDAC\_AGND T8

NC/ESD5451N ESD0402

AMP2.ROUT+

AMP2.ROUT-

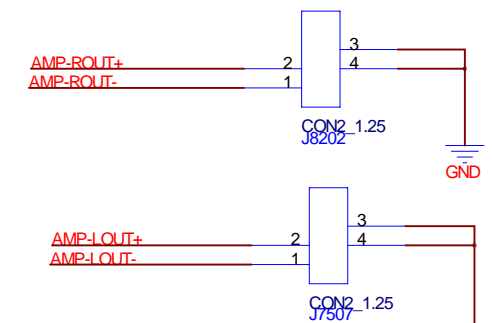
D7005

C7050

1nF C7051

D7006

NC/ESD5451N ESD0402





TOUCH CONNECTOR

