Schematic: MCU2_BD.SchDoc

J1 — 67996-420HLF (CONNECT_MCUBD J1,J3)
- MCU2_IN_3.3V → A1
- A5 GIOA_7
- A7 EQEP 1A
- A8 GIOA_6
- B2 ADCIN_0 → GND

J2 — 67996-420HLF (CONNECT_MCUBD_SITE1 J4,J2)
- A1 EPWM 3A
- A7 GIOA_5
- A8 GIOA_2
- A9 GIOA_1
- B1 GND
- A10 GIOA_0 → MPU_INT
- B9 I2C_1_SCL → MCU2_I2C1_SCL
- B8 I2C_1_SDA → MCU2_I2C1_SDA

P3 — Header 2 (MPU_INT_PORT)
- 1, 2
- L1 beed MPZ2012 → MPU_INT / GND

J3 — 67996-420HLF (CONNECT_MCUBD J5,J7)
- MCU2_IN_3.3V → A1
- A5 EQEP 1I
- A7 MIBSPI1CLK ← MCU2_SPICLK
- B2 → GND

J4 — 67996-420HLF (CONNECT_MCUBD J8,J6)
- A7 GIOB_1
- A8 GIOB_0
- B7 MIBSPI1SOMI_0
- B6 MIBSPI1SIMO_0
- B3 MIBSPI1NCS_0
- MCU2_SPI_CS
- MCU2_SPI_SIMO
- MCU2_SPI_SOMI

P1 — Header 2 (TO_MCU1_I2C_PORT)
- MCU2_IN_3.3V
- R5 1k
- R6 1k
- MCU2_I2C1_SCL → 1
- MCU2_I2C1_SDA → 2

P2 — Header 4 (TO_MCU1_SPI_PORT)
- MCU2_IN_3.3V
- R1 1k, R2 1k, R3 1k, R4 1k
- MCU2_SPI_CS → 1
- MCU2_SPI_SIMO → 2
- MCU2_SPI_SOMI → 3
- MCU2_SPICLK → 4

Title			
Size A4	Number		Revision
Date:	2019-06-18	Sheet	of
File:	C:\Users\..\MCU2_BD.SchDoc	Drawn By:	

Bill of Materials

<Parameter Title not found>

Source Data From: MCU2_BD.PrjPcb

Project: MCU2_BD.PrjPcb

Variant: None

Creation Date: 2019-06-18   오후 8:11:20

Print Date: 18-Jun-19   8:11:34 PM

Footprint	Comment	LibRef	Designator	Description	Quantity
67996-420HLF_67996-420HLF(Primary)	67996-420HLF	67996-420HLF	J1, J2, J3, J4		4
0402-A	beed	Inductor	L1	Inductor	1
HDR1X2	Header 2	Header 2	P1, P3	Header, 2-Pin	2
HDR1X4	Header 4	Header 4	P2	Header, 4-Pin	1
AXIAL-0.3	Res1	Res1	R1, R2, R3, R4, R5, R6	Resistor	6
					14

Approved		Notes	