







CSP: 1.54 x 0.84 x 0.60mm

Moisture Sensitivity Level (MSL) – 1

> FEATURES:

- Factory programmable output frequency: 1Hz to 32.768kHz
- Available in two types of ultra-miniature packages:
 2.0 x 1.2 x 0.6mm (SMD);
 1.54 x 0.84 x 0.6mm (CSP)
- Supply Voltage: 1.2V to 3.63V ($-10 \sim +70^{\circ}$ C); 1.5V to 3.63V ($-40 \sim +85^{\circ}$ C)
- Ultra-Low Current Consumption: 1.4µA max. (core current, no load)
- Frequency Stabilities include:
 - ± 75 ppm over -10 to ± 70 °C, ± 100 ppm over -40 to ± 85 °C
- Internal power supply filtering eliminates external bypass capacitor for Vdd port.
- Proprietary NanoDrive[™] Technology by SiTime enables programmable output swing for lower power

> APPLICATIONS:

- · Timekeeping
- Battery Management
- · Mobile devices
- RTC reference clock
- Wireless accessories
- Fitness/Medical monitoring sensors
- · Sport video cams

STANDARD SPECIFICATIONS:

Param	Parameters		Тур	Max	Unit	Notes
Output Frequency R	Output Frequency Range (Fout)			32768	Hz	Factory programmed between 1Hz and 32.768kHz, in the powers of 2
Initial Frequency To	-20		+20	ppm	T_A = +25°C, post reflow, V_{dd} :1.5-3.63V	
Frequency Stability over Temperature		-75		+75		T_A = -10°C to +70°C, V_{dd} :1.5-3.63V
$(F_{\text{stab}})^{(2)}$	over remperature	-100		+100	ppm	T_A = -40°C to +85°C, V_{dd} :1.5-3.63V
(1 stab)	-250		+250		T_A = -10°C to +70°C, V_{dd} :1.2-1.5V	
Aging (@+25°C)		-1		+1	ppm	First year
Supply Voltage (V)	1.2		3.63	V	T_A = -10°C to +70°C
Supply Voltage (V _{do}	1)	1.5		3.63	V	$T_A = -40$ °C to $+85$ °C
			0.90			T_A = +25°C, V_{dd} : 1.8V. No load.
Core Operating Cur	rent (I_{dd}) (3)			1.3	μΑ	T _A = -10°C to +70°C, V _{dd} max: 3.63V. No load T _A = -40°C to +85°C,
				1.4		V _{dd} max: 3.63V. No load.
Output Stage Operator $(I_{dd out})^{(3)}$			0.065	0.125	$\mu A/V_{pp}$	T_A = -40°C to +85°C, V _{dd} max: 1.5-3.63V. No load.
Power Supply Ramp	(t_{Vdd_Ramp})			100	ms	T_A = -40°C to +85°C, 0 to 90%* V_{dd}
Start-up Time (T _{start}	. (4)			300+1 period		T_A = +25°C±10°C, valid output
Start-up Time (T _{start})) ` ′			500+1 period	ms	T_A = -40°C to +85°C, valid output
O : F	D (T)	-10		+70	00	Option "M"
Operating Temperat	ure Range (T _{use})	-40		+85	°C	Option "L"
Period Jitter			35		ns _{RMS}	Cycles=10000, T _A =+25°C, V _{dd} :1.5-3.63V
LVCMOS Output	Option (T _A = -40°C	to +85°C. Typ	ical values ar	e at $T_A = +25$ °C	C)	
Output Rise/Fall Tir	me (t_r/t_f)		100	200	ns	10-90%(V _{dd}), 15pF load, Vdd:1.5- 3.63V
Output Clock Duty	Cycle	48		52	%	
Output Voltage	V_{OH}	90%*V _{dd}			V	V_{dd} :1.5-3.63V. I_{OH} = -10 μ A, 15pF
1	V_{OL}			10%*V _{dd}	v	V_{dd} :1.5-3.63V. I_{OL} = 10 μ A, 15 p F
NanoDrive ^{TM (6)} Pro	ogr <mark>amma</mark> ble, Redu	iced Swing O	utput Optic			
Output Rise/Fall Time (t _r /t _f)				200	ns	30-70%(V _{OL} / V _{OH}), 10pF load
Output Clock Duty Cycle		48		52	%	
AC-coupled Programmable Output Swing (V_{SW})			0.20 to 0.80		V	ASTMK does not internally AC-couple. This output description is intended for a receiver that is AC-coupled. See Part Identification section for available AC-coupled signal swing options. $V_{dd}: 1.5-3.63V. \ 10pF \ load, \\ I_{OH}/I_{OL} = \pm 0.2 \mu A$









(Continued)

Parameters	Min	Тур	Max	Unit	Notes
DC-biased Programmable Output Voltage High Range (V _{OH})		0.60 to 1.225		V	V_{dd} :1.5-3.63V. I_{OH} =-0.2 μ A.10pF load. See Part Identification section for available V_{OH}/V_{OL} levels.
DC-biased Programmable Output Voltage Low Range (V _{OL})		0.35 to 0.80		V	V_{dd} :1.5-3.63V. I_{OL} =0.2 μ A.10pF load. See Part Identification section for available V_{OH}/V_{OL} levels.
Programmable Output Voltage Swing Tolerance	-0.055		+0.055	V	T_A = -40°C to +85°C, V_{dd} :1.5-3.63V

Note:

- 1. Measured peak-to-peak. Tested with Agilent 53132A frequency counter. Due to the low operating frequency, the gate time must be ≥100ms to ensure an accurate frequency measurement.
- 2. Stability is specified for two operating voltage ranges. Stability progressively degrades with supply voltage below 1.5V. Measured peak-to-peak. Inclusive of initial tolerance at +25°C, and variations over operating temperature, rated power supply voltage and load.
- 3. Core operating current does not include output driver operating current or load current. To derive total operating current (no load), add core operating current + output driver operating current, where output driver operating current = $C_{driver} *V_{out} *F_{out}$.
- 4. Measured from the time V_{dd} reaches 1.5V.
- 5. Board-level underfill (BLUF) is not recommended for 1508 CSP package as it will cause a shift in the frequency tolerance.
- 6. NanoDriveTM is a SiTime trademark.

Absolute Maximum Ratings

Attempted operation outside the absolute maximum ratings may cause permanent damage to the part. Actual performance of the IC is only guaranteed within the operational specifications, not at absolute maximum ratings.

Parameters	Test Condition	Value	Unit
Continuous Power Supply Voltage Range (V _{dd})		-0.5 to 3.63	V
Short Duration Max. Power Supply Voltage (V _{dd})	≤30 minutes	4.0	V
Continuous Maximum Operating Temperature Range	Vdd:1.5-3.63V	105	$^{\circ}\!\mathrm{C}$
Short Duration Max. Operating Temperature Range	Vdd:1.5-3.63V, ≤30 minutes	125	°C
Human Body Model (HBM) ESD Protection	JESD22-A114	3000	V
Charge-Device Model (CDM) ESD Protection	JESD22-C101	750	V
Machine Model (MM) ESD Protection	JESD22-A115	300	V
Latch-up Tolerance	JESD78 Compli	iant	
Mechanical Shock Resistance	Mil 883, Method 2002	10000	g
Mechanical Vibration Resistance	Mil 883, Method 2007	70	g
2012 SMD Junction Temperature		150	$^{\circ}\mathrm{C}$
1508 CSP Junction Temperature		150	°C
Storage Temperature		-65 to +150	°C

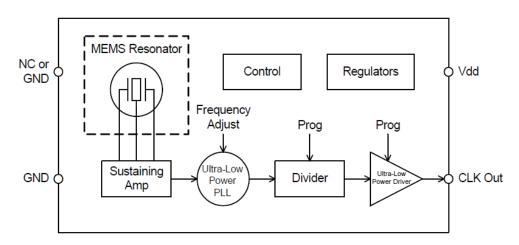




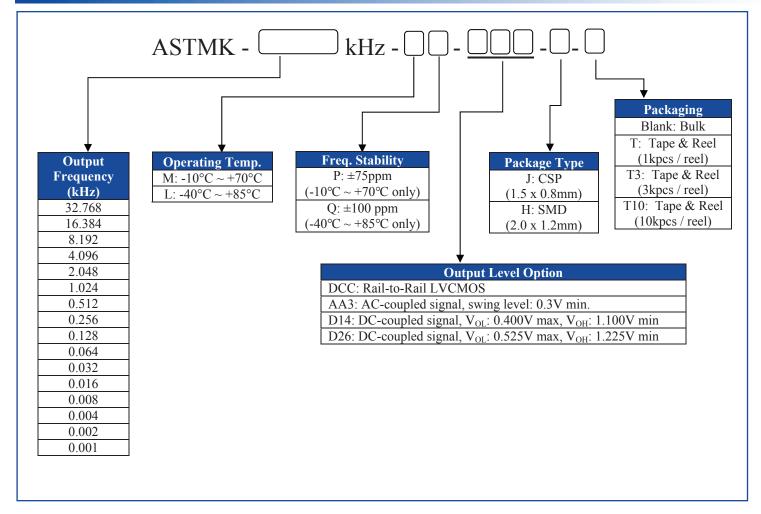




Block Diagram



> PART IDENTIFICATION:



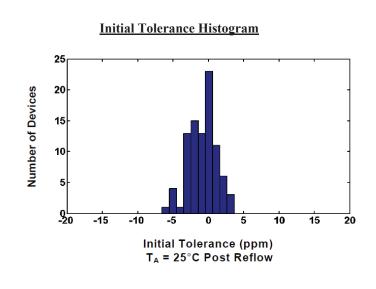


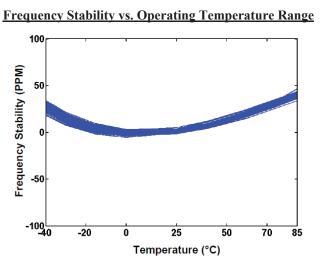


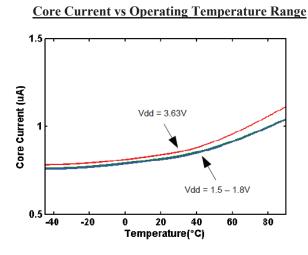


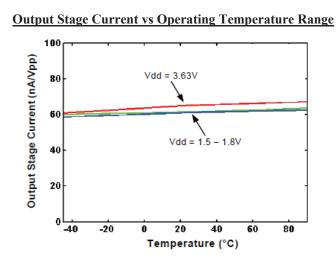


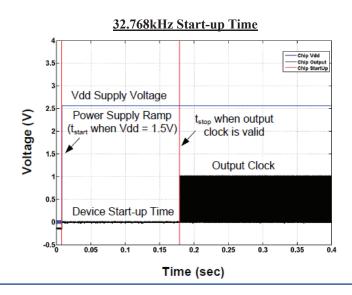
> Typical Performance Data (TA=25°C, Vdd=1.8V, unless otherwise stated)













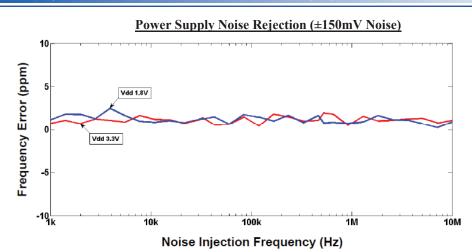




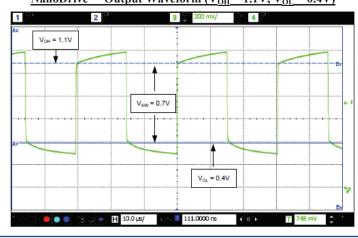




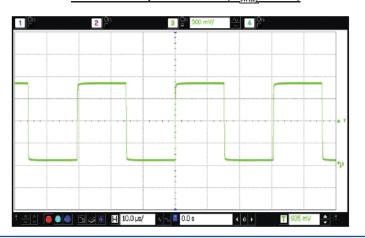
Typical Performance Data (TA=25°C, Vdd=1.8V, unless otherwise stated)---(Continued)



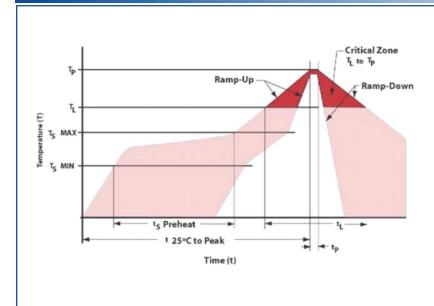
NanoDriveTM Output Waveform ($V_{OH} = 1.1V, V_{OL} = 0.4V$)



LVCMOS Output Waveform (V_{swing} = 1.8V)



REFLOW PROFILE:



Item	Conditions
T_SMAX to $T_L(Ramp\mbox{-up Rate})$	3°C/second max
Preheat	
Temperature Minimum (T _S MIN)	150°C
Temperature Typical (T _S TYP)	175°C
Temperature Maximum (T _S MAX)	200°C
Time (t _s)	60 – 180 seconds
Ramp-up Rate (T _L to T _P)	3°C/second max
Time Maintained Above	
Temperature (T _L)	217°C
Time (t_L)	60 – 150 seconds
Peak Temperature (T _P)	260°C max
Target Peak Temperature (T _P Target)	255°C
Time within 5°C of actual peak (t _P)	20 – 40 seconds
Max. Number of Reflow Cycles	3
Ramp-down Rate	6°C/second max
Time 25°C to Peak Temperature (t)	8 minutes max





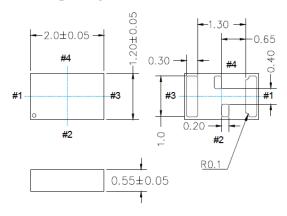




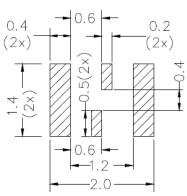


OUTLINE DIMENSION:

2012 SMD package:

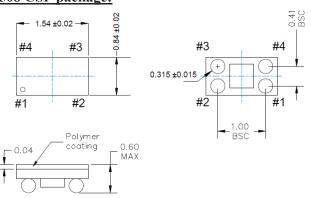


Recommended Land Pattern

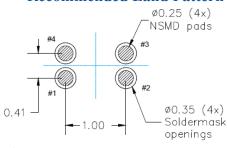


Pin	Name	I/O	Functionality
1	NC	No Connect	No connection. Will not respond to any input signal.
2	GND	Power Supply Ground	Connect to ground. All GND pins must be connected to power supply ground.
3	CLK Out	OUT	Oscillator clock output.
4	$ m V_{dd}$	Power Supply	Connect to power supply 1.5V \leq V _{dd} \leq 3.63V for operation over -40°C to +85°C temperature range. Under normal operating conditions, V _{dd} doesn't require external bypass/decoupling capacitor(s). Internal power supply filtering will reject more than \pm 150mVpp with frequency components through 10MHz.

1508 CSP package:



Recommended Land Pattern



(soldermask openings shown with heavy dashed line)

Recommend 4-mil (0.1mm) stencil thickness

Pin	Name	I/O	Functionality
1,4	GND	Power Supply Ground	Connect to ground. Acceptable to connect pin 1 and 4 together. Both pins must be connected to GND.
2	CLK Out	OUT	Oscillator clock output.
3	V_{dd}	Power Supply	Connect to power supply 1.2V \(\subseteq \text{V}_{dd} \leq 3.63V\). Under normal operating conditions, V _{dd} doesn't require external bypass/decoupling capacitor(s). Internal power supply filtering will reject more than \(\pm 150 \text{mVpp}\) with frequency components through 10MHz.

Dimensions: mm



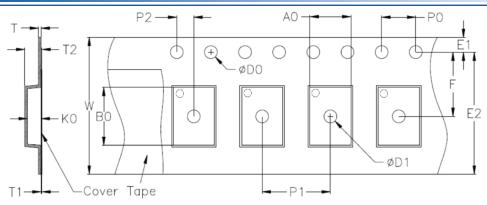








> TAPE & REEL:

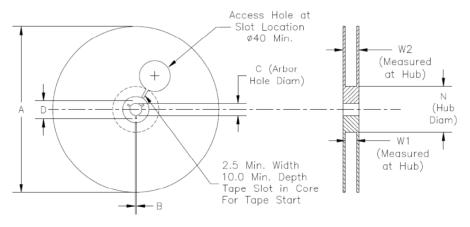


Tape Dimensions for 2012 SMD package (Unit: mm)

D0	D1 min.	E 1	E2 min.	F	P0	P1	P2
1.55±0.05	1.0	1.75±0.1	6.05	3.5±0.05	4.0±0.1	4.0±0.1	2.0±0.05
T	T1 max.	T2 max.	W max.	A0	В0	K0	
0.25±0.05	NA	NA	8.3	1.6±0.05	2.25±0.10	0.65±0.05	

Tape Dimensions for 1508 CSP package (Unit:mm)

D0	D1 min.	E 1	E2 min.	F	P0	P1	P2
1.55±0.05	0.18	1.75±0.1	6.05	3.5±0.05	4.0±0.1	4.0±0.1	2.0±0.05
T	T1 max.	T2 max.	W max.	A0	B0	K0	
0.20 ± 0.02	NA	NA	8.3	0.96±0.03	1.66±0.03	0.63±0.03	



Option	A max.	B min.	C	D min.	N	W1	W2 max.
T & T3	180.5	1.5	13.0+0.6/-0.2	20.2	60±0.5	8.4+1.5/-0	14.4
T10	330	1.5	13.0±0.2	20.2	100±0.5	8.4+1.5/-0	14.4

T= Tape and reel (1,000pcs/reel)

T3= Tape and reel (3,000pcs/reel)

T10= Tape and reel (10,000pcs/reel)

Unit: mm

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