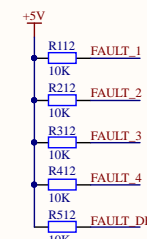
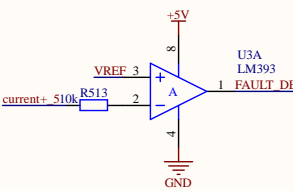
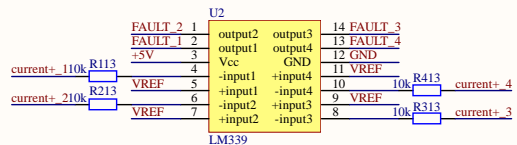
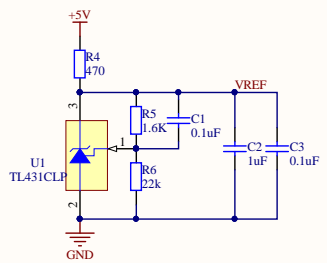
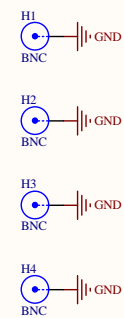
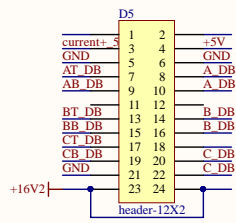
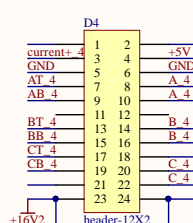
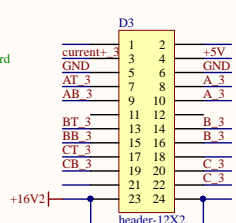


FPGA_Signal_to_Booster_Board



Title		
Size	Number	Revision
A3		
Date:	2018/11/27	Sheet of
File:	C:\Users\...\BaseBoardV2.3.SchDoc	Drawn By:

