



The diagram illustrates the Core Board Interface, showing the connection between three connectors: P25A, P25B, and P25C. The interface is divided into three main sections: P25A, P25B, and P25C.

**P25A Connector:** This connector is connected to a +5V power supply and a +3.3V power supply. It includes a TXD signal line and a RXD signal line. The P25A connector is connected to a P25B connector via a connector labeled "CONNECTOR\_FPGA Bank3".

**P25B Connector:** This connector is connected to a +3.3V power supply and a +5V power supply. It includes a TXD signal line and a RXD signal line. The P25B connector is connected to a P25C connector via a connector labeled "CONNECTOR\_FPGA Bank3".

**P25C Connector:** This connector is connected to a +3.3V power supply and a +5V power supply. It includes a TXD signal line and a RXD signal line. The P25C connector is connected to a P25D connector via a connector labeled "CONNECTOR\_FPGA Bank3".

**AT23C8:** This is a 1-wire I2C-to-1-wire bridge IC. It is connected to the P25C connector via a connector labeled "CONNECTOR\_FPGA Bank3". The AT23C8 is connected to a +3.3V power supply and a +5V power supply. It includes a TXD signal line and a RXD signal line.

**Legend:** The legend indicates the following connections:

- NC: Not Connected
- LED: LED signal line
- TXD: TXD signal line
- RXD: RXD signal line
- CLK: CLK signal line
- DATA: DATA signal line
- SDA: SDA signal line
- RESET: RESET signal line

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