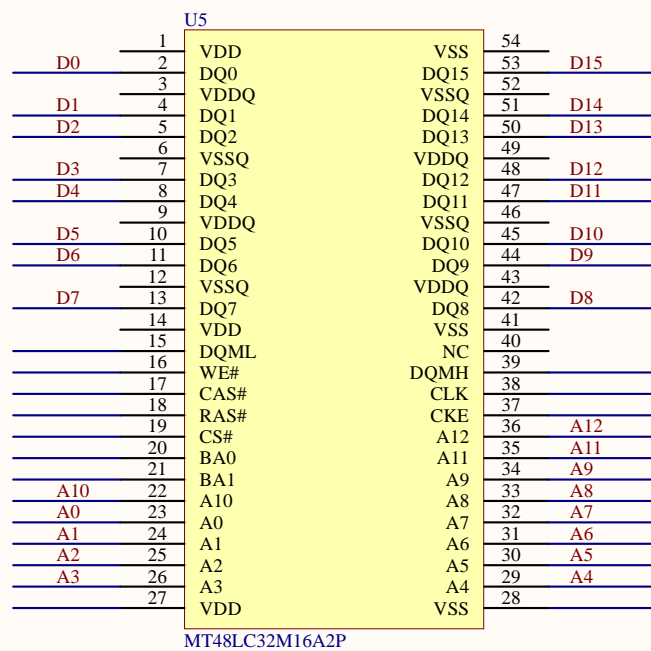
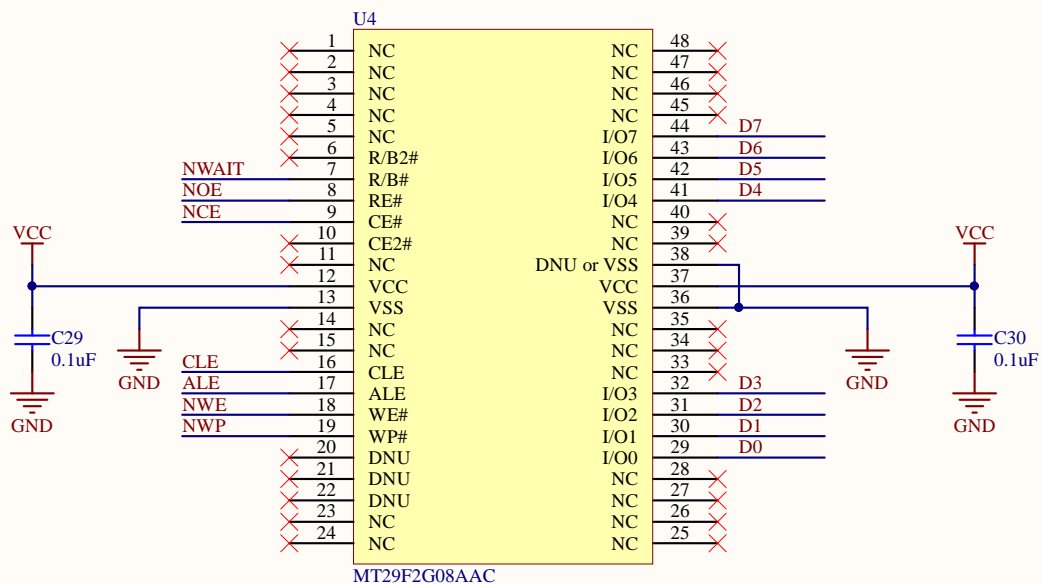


MEM_BUS

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D1 D1
D2 D2
D3 D3
D4 D4
D5 D5
D6 D6
D7 D7
D8 D8
D9 D9
D10 D10
D11 D11
D12 D12
D13 D13
D14 D14
D15 D15
A0 A0
A1 A1
A2 A2
A3 A3
A4 A4
A5 A5
A6 A6
A7 A7
A8 A8
A9 A9
A10 A10
A11 A11
A12 A12
CLE CLE
ALE ALE
NOE NOE
NWE NWE
NCE NCE
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NWAIT NWAIT

MEM_BUS



Title

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Date:

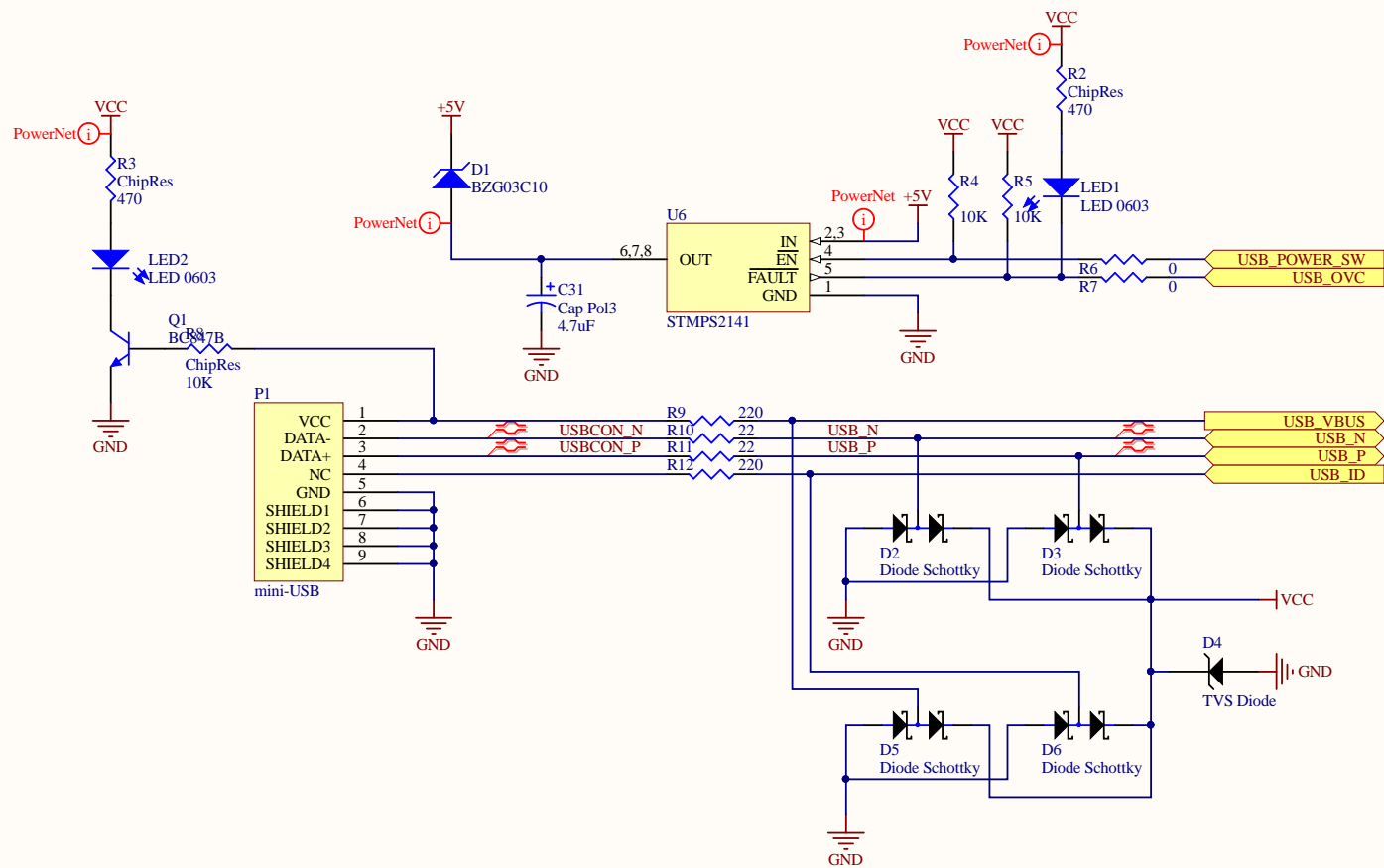
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