J2 — TFT-2.8-240x320-i9341

Pin	Name	Signal
1	LEDK	
2	LEDA1	R5 NA → GND
3	LEDA2	R6 NA
4	LEDA3	R7 NA → PDVDD
5	LEDA4	R8 NA
6	IM0	IM0
7	IM1	IM1
8	IM2	IM2 → R9 NA → GND
9	IM3	IM3 → R10 NA
10	RESET	TFT_RST → R11 NA → PDVDD
11	VSYNC	VSYNC / R12 NA
12	HSYNC	HSYNC
13	DOTCLK	DOTCLK
14	DE	DE
15	DB17	TFT_DB07
16	DB16	TFT_DB06
17	DB15	TFT_DB05
18	DB14	TFT_DB04
19	DB13	TFT_DB03
20	DB12	TFT_DB02
21	DB11	TFT_DB01
22	DB10	TFT_DB00
23	DB09	
24	DB08	TFT_DA07
25	DB07	TFT_DA06
26	DB06	TFT_DA05
27	DB05	TFT_DA04
28	DB04	TFT_DA03
29	DB03	TFT_DA02
30	DB02	TFT_DA01
31	DB01	TFT_DA00
32	DB00	
33	SDO	
34	SDI/SDA	TFT_SDI
35	RD	TFT_RD
36	WR/DC	TFT_WR/SCL
37	DC/SCL	TFT_DC/SCL
38	CS	TFT_CS
39	TE	
40	VDDI	PDVDD
41	VDDI	
42	VCI	
43	GND	GND
44	X+	
45	Y+	
46	X-	
47	Y-	
48	GND	
49	GND	
50	GND	GND

J1 — TFT-2.8-240x320-i9320

Pin	Name	Signal
1	DB00	TFT_DA00
2	DB01	TFT_DA01
3	DB02	TFT_DA02
4	DB03	TFT_DA03
5	GND	GND
6	VDD	PDVDD
7	CS	TFT_CS
8	RS	TFT_DC/SCL
9	WR	TFT_WR/SCL
10	RD	TFT_RD
11	NC	IM0
12	X+	XR
13	Y+	YU
14	X-	XL
15	Y-	YD
16	LED+A	PDVDD
17	LED-K1	R1 NA
18	LED-K2	R2 NA → GND
19	LED-K3	R3 NA
20	LED-K4	R4 NA
21	NC	
22	DB04	TFT_DA04
23	DB10	TFT_DB00
24	DB11	TFT_DB01
25	DB12	TFT_DB02
26	DB13	TFT_DB03
27	DB14	TFT_DB04
28	DB15	TFT_DB05
29	DB16	TFT_DB06
30	DB17	TFT_DB07
31	RESET	TFT_RST → PDVDD
32	VDD	
33	VDD	
34	GND	
35	DB05	TFT_DA05
36	DB06	TFT_DA06
37	DB07	TFT_DA07

P2 — WIKIPWR

1	PDVDD	GND	2
3	VBAT	GND	4
5	PVDD	NRST	6

PDVDD, GND, TFT_RST

P1 — WIKIGPIO-FULL

Left		Right
IO13	IO12	
IO11	IO10	
IO9	IO8	
IO7	IO6	
VSYNC R13 NA — IO5	IO4 — R14 NA HSYNC	
DOTCLK R15 NA		R16 NA DE
TFT_DA07 — DA07	DA06 — TFT_DA06	
TFT_DA05 — DA05	DA04 — TFT_DA04	
TFT_DA03 — DA03	DA02 — TFT_DA02	
TFT_DA01 — DA01	DA00 — TFT_DA00	
PDVDD	GND	
GND — TFT_RST — NRST	SDI — TFT_SDI	
TFT_DC/SCL — RS/DC	NCS — TFT_CS	
TFT_WR/SCL — WR/SCL	RD — TFT_RD	
TFT_DB01 — DB01	DB00 — TFT_DB00	
TFT_DB03 — DB03	DB02 — TFT_DB02	
TFT_DB05 — DB05	DB04 — TFT_DB04	
TFT_DB07 — DB07	DB06 — TFT_DB06	
IO1	IO0	
IO3	IO2	

JP1

1	GND
2	IM0
3	PDVDD

JP2

1	GND
2	IM2
3	PDVDD

Note (IM3 =1, IM1 =0):

IM2 IM0
- 0   0   16-bit   DC(DC_SCL) WR(WR_DC)
- 0   1   8-bit   DC(DC_SCL) WR(WR_DC)
- 1   1   3-wire 9-bit SCL(DC_SCL)

Note (IM3 =0, IM1 =0):

IM2 IM0
- 0   0   16-bit
- 0   1   8-bit
- 1   1   3-wire 9-bit SCL(WR_SCL)

WikiMaker
http://www.cnblogs.com/wikimaker/

Title	WikiTFT	Checked By	Jacob
Size	A4	Number	SP-WikiTFT	Revision	A1
Date:	2015-03-27		Sheet of	1/1
File:	D:\wiki\SP-WikiTFT-Rev.A\WikiTFT.SchDoc	Drawn By:	Jacob

Comment	Description	Designator	Footprint	LibRef	Quantity
TFT-2.8-240x320-i9320	i9320-240x320	J1	TFT-PIN37-1.0MM	TFT-2.8-240x320-i9320	1
TFT-2.8-240x320-i9341		J2	FPC-0.5-PIN50	TFT-2.8-240x320-i9341-PIN50-FPC	1
Jumper3	Jumper3	JP1, JP2	HDR1X3/2.00mm	Jumper3	2
WIKIGPIO-FULL	Header, 19-Pin, Dual row	P1	WIKIGPIO-FULL	WIKIGPIO-FULL	1
WIKIPWR	PWR HDR	P2	WIKIPWR	WIKIPWR	1
NA	Resistor	R1, R2, R3, R4, R5, R6, R	0603M	Res	16