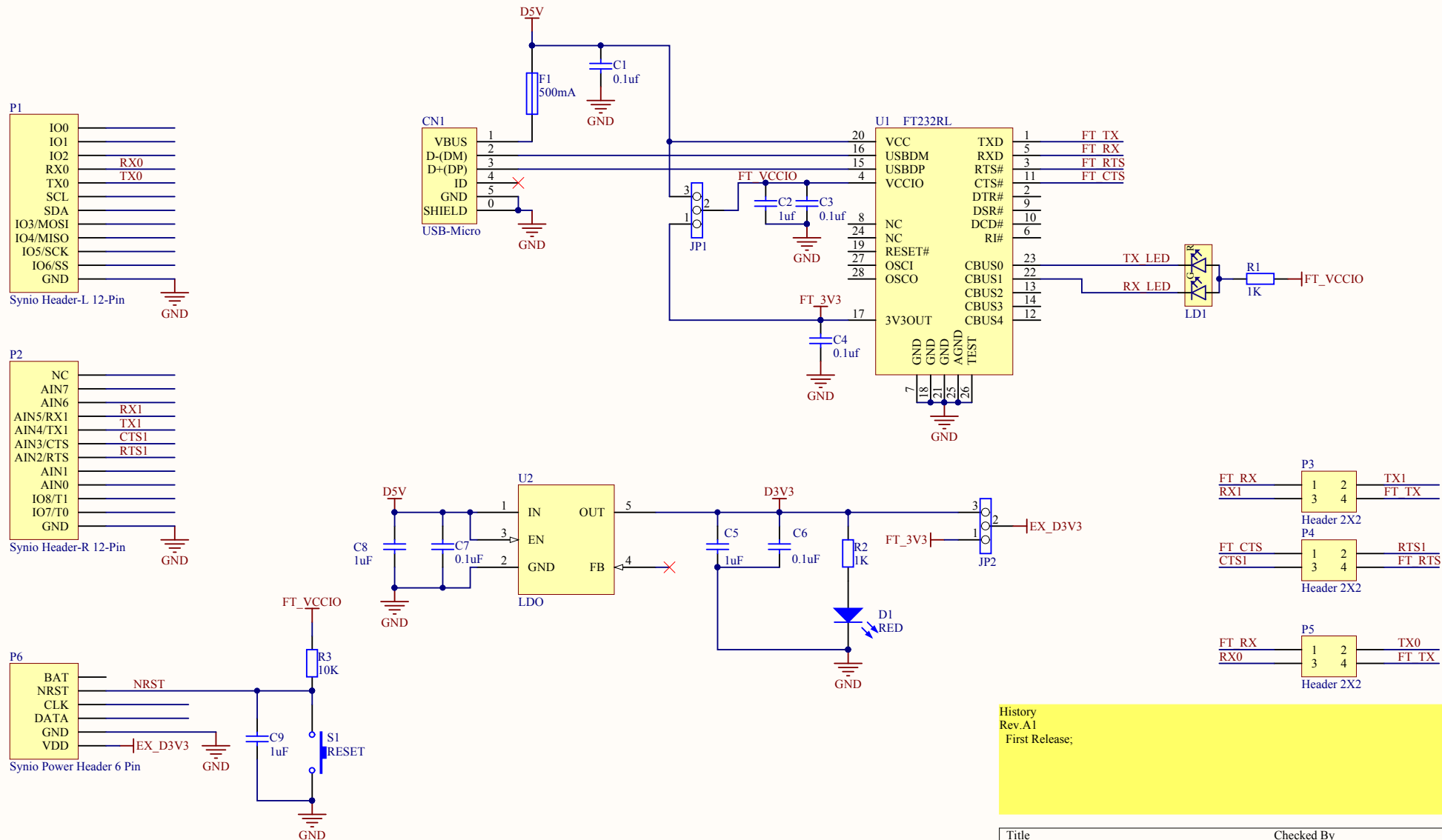


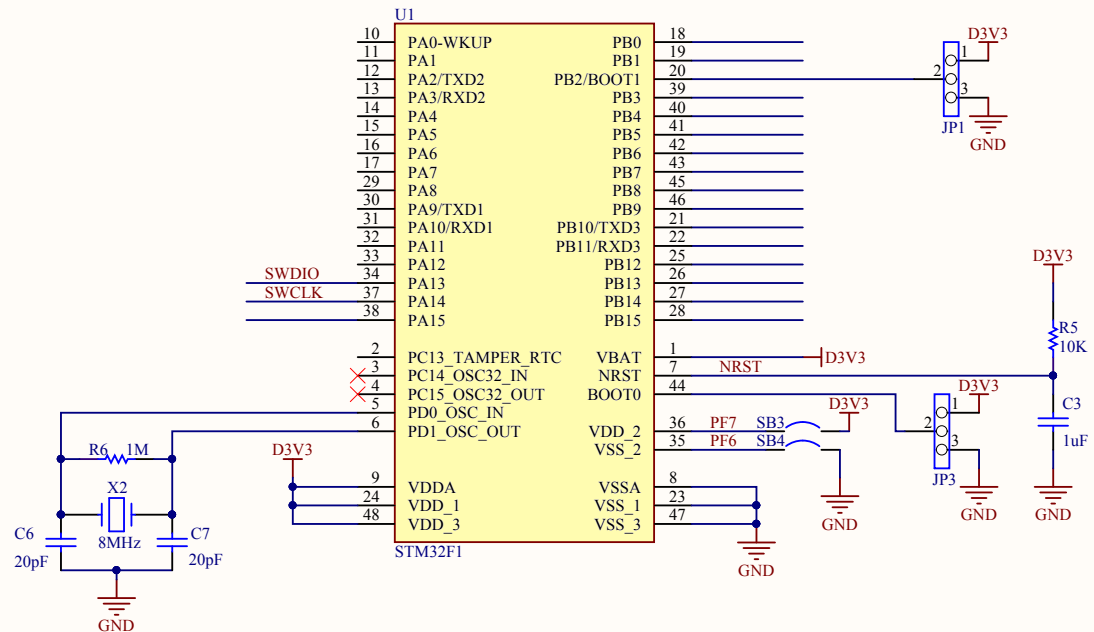
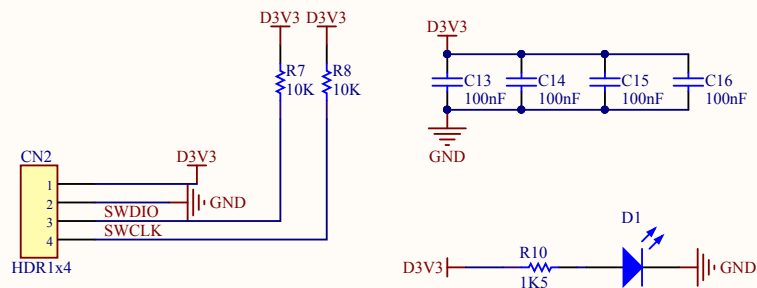
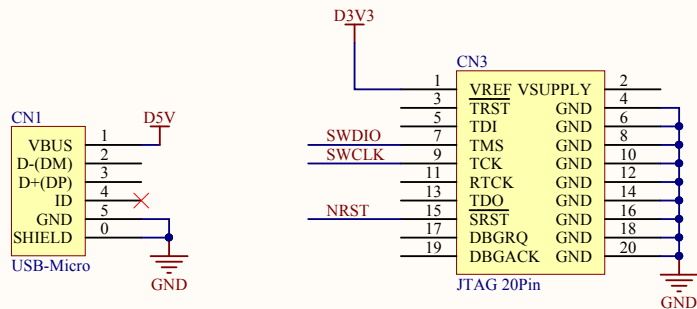
History
Rev.A1
First Release;

Title		Checked By	
Power&CH340T		Jacob	
Size	Number	Revision	
A4	SP-Wikiot	A1	
Date:	2016/6/7	Sheet of	1/1
File:	H:\Braches\..Power&CH340T.SchDoc	Drawn By:	Jacob



History
Rev.A1
First Release;

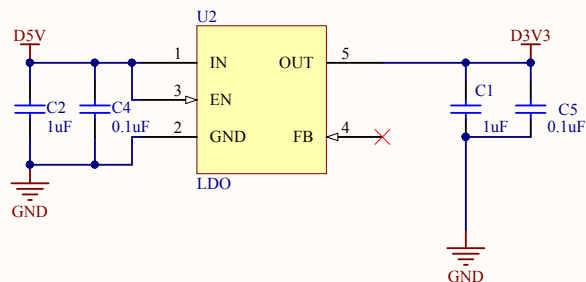
Title		Checked By	
Power&FT232		Jacob	
Size	Number	Revision	
A4	SP-Wikiot	A1	
Date:	2016/6/7	Sheet of	1/1
File:	H:\Braches\Power&FT232.SchDoc	Drawn By:	Jacob



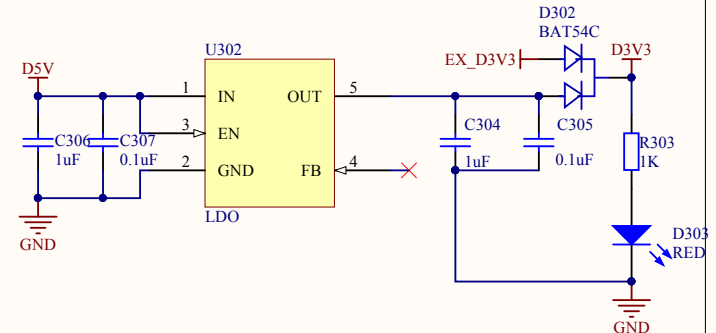
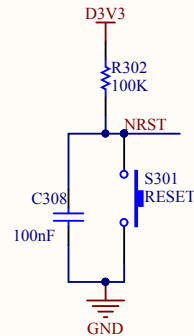
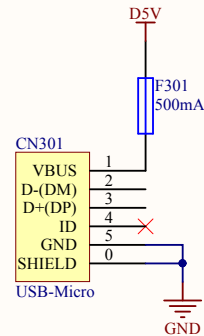
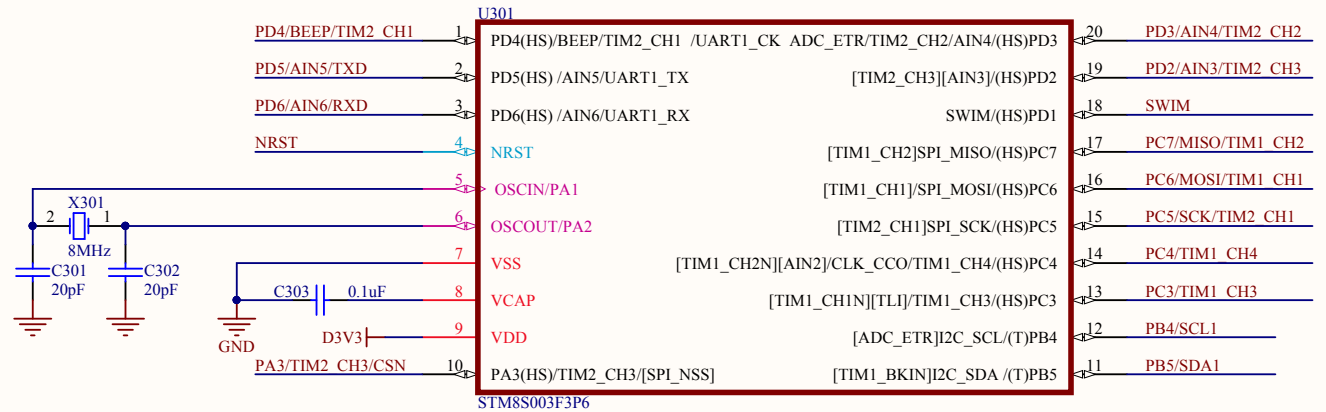
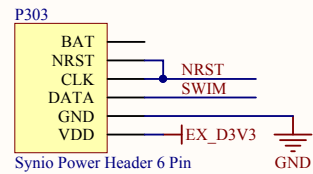
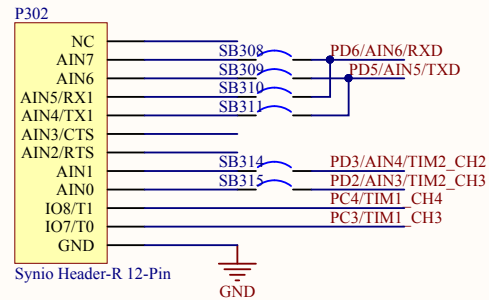
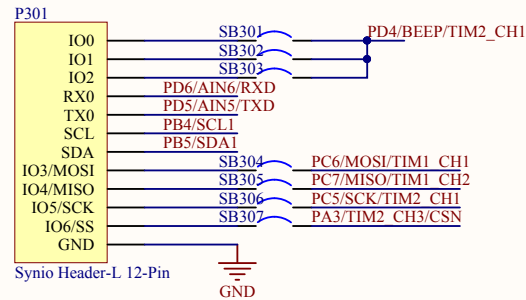
For STM32F0 Devices SB2, SB3, SB4 open-bridged; Boot0/Boot1 options;

For STM32F1 Devices SB2, SB3, SB4 short-bridged; Boot0/Boot1 options;

For STM32F3 Devices SB2, SB3, SB4 short-bridged; Boot0 option;



Title		Checked By	
WikiBoard-STM32P48		Jacob	
Size	Number	Revision	
A4	SP-WikiBoard-STM32P48	A2	
Date:	2016/6/7	Sheet of	1/1
File:	H:\Braches\STM32-Flasher.SchDoc	Drawn By:	Jacob



Title		
Size	Number	Revision
A4		
Date:	2016/6/7	Sheet of
File:	H:\Braches\STM8S103.SchDoc	Drawn By:

