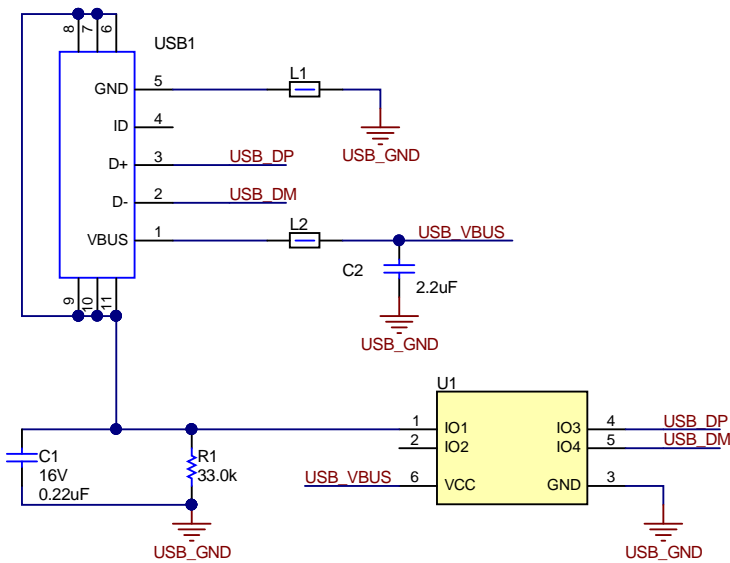
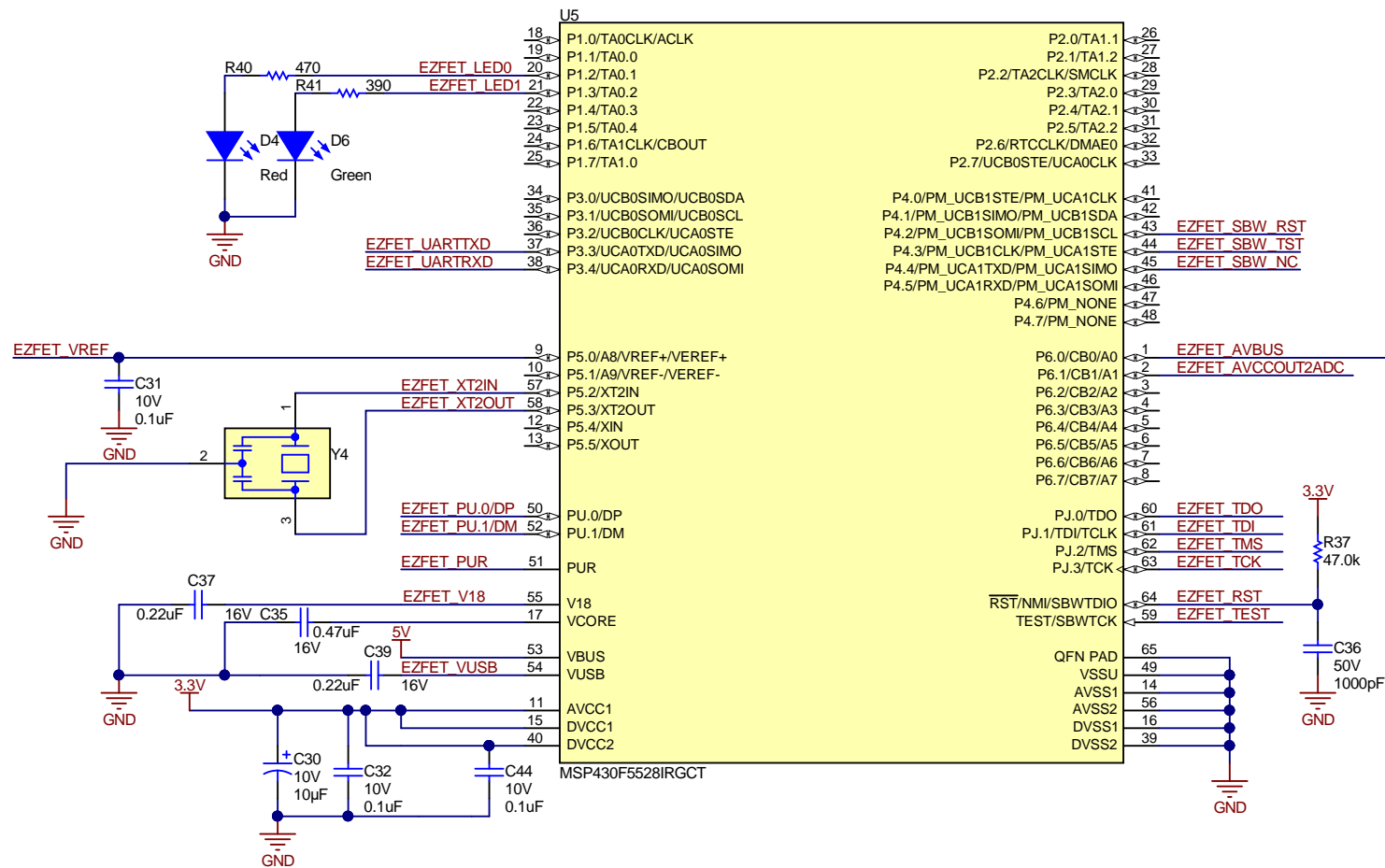


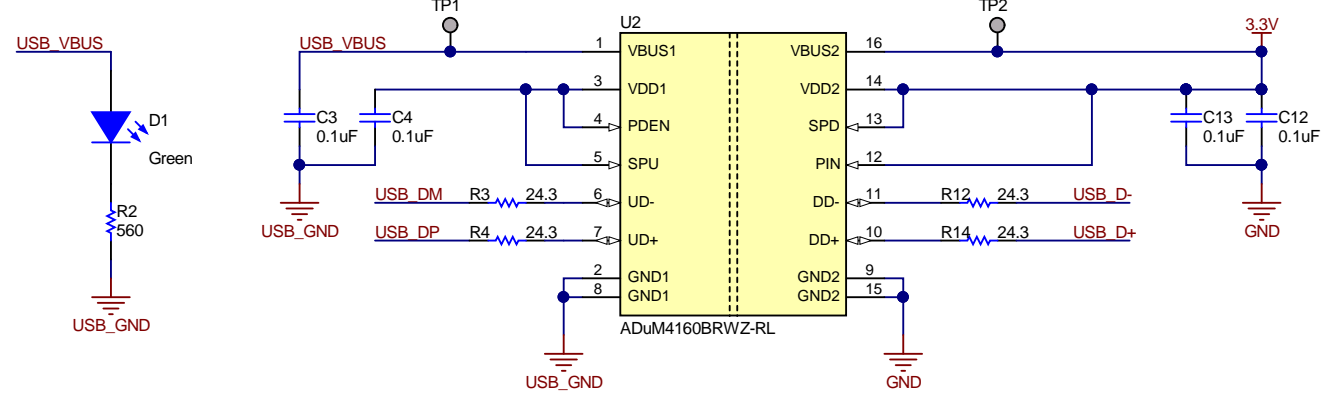
USB CONN & ESD PROT



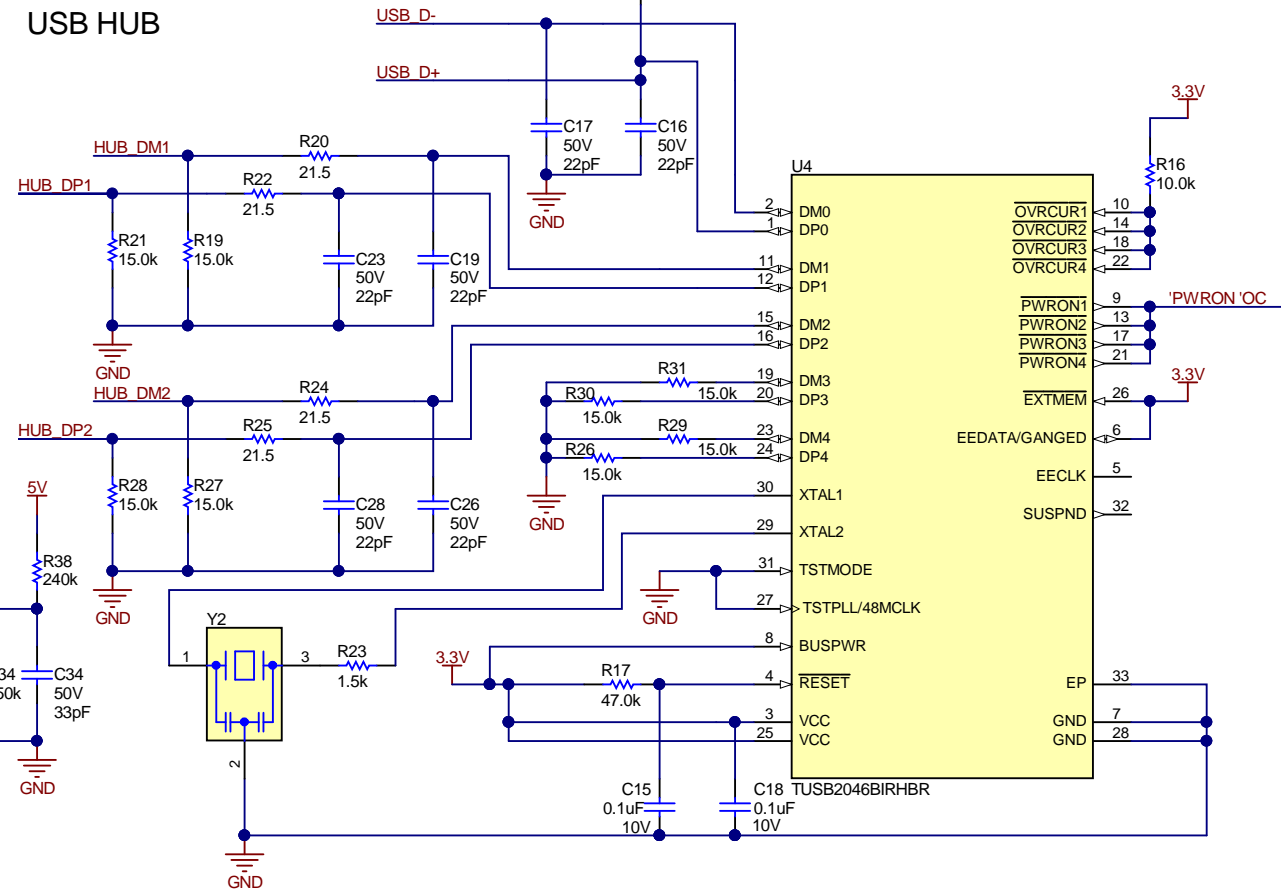
EZFET Lite



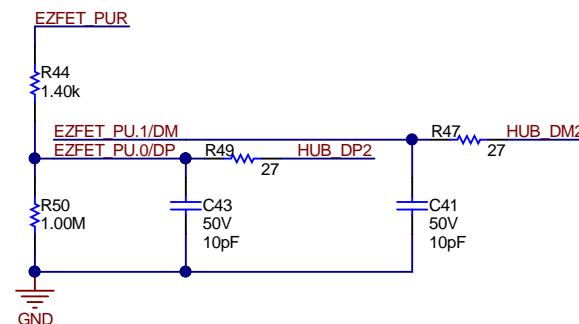
Isolation Boundary



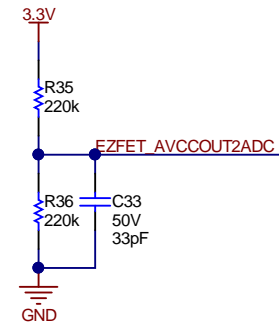
USB HUB



EZFET PUR



TARGET VCC SENSE



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Orderable: DRV8350S-EVM	Designed for: Public Release	Mod. Date: 11/29/2018
TID #: N/A	Project Title: DRV8350X 3-Phase Smart Gate Driver EVM	
Number: MD015	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 4
Drawn By:	File: MD015A_MCU_USB2HUB.SchDoc	Size: B
Engineer: Hector Hernandez	Contact: http://www.ti.com/support	

A



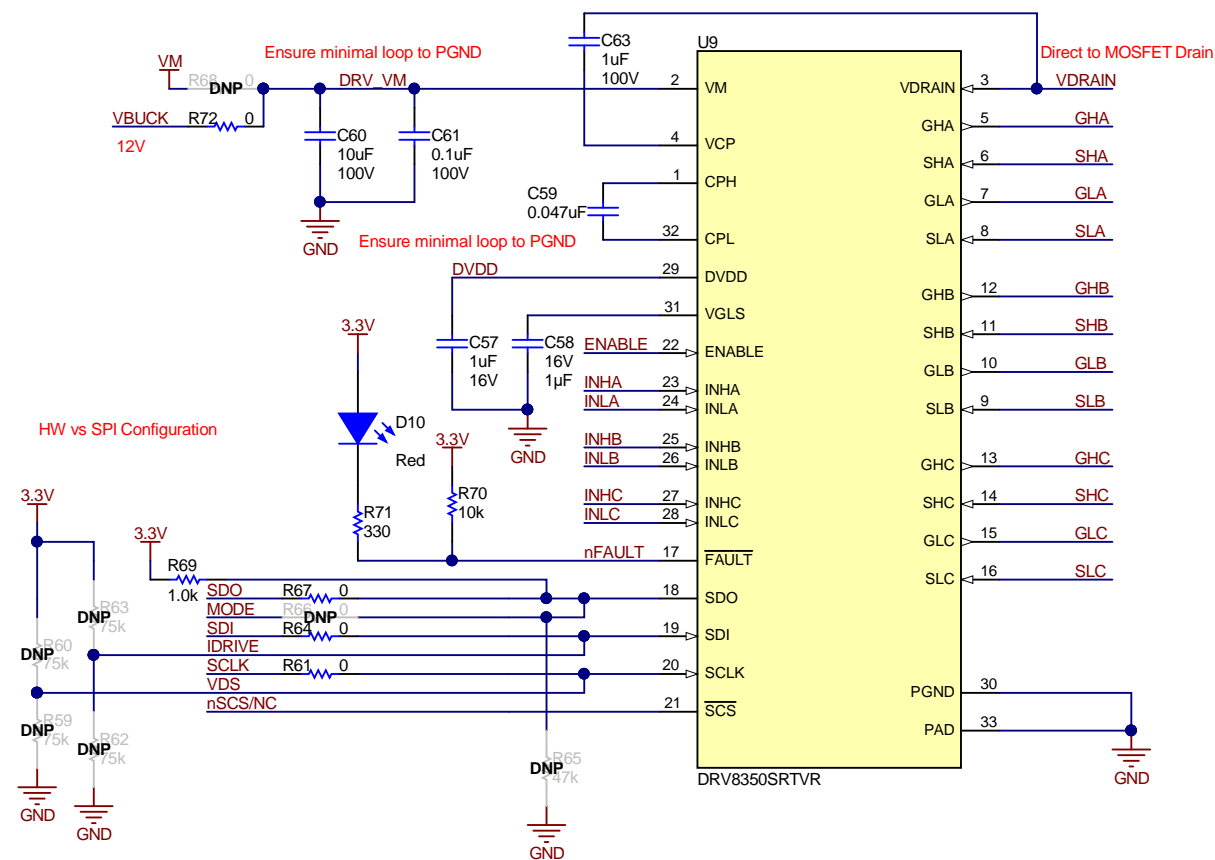
C



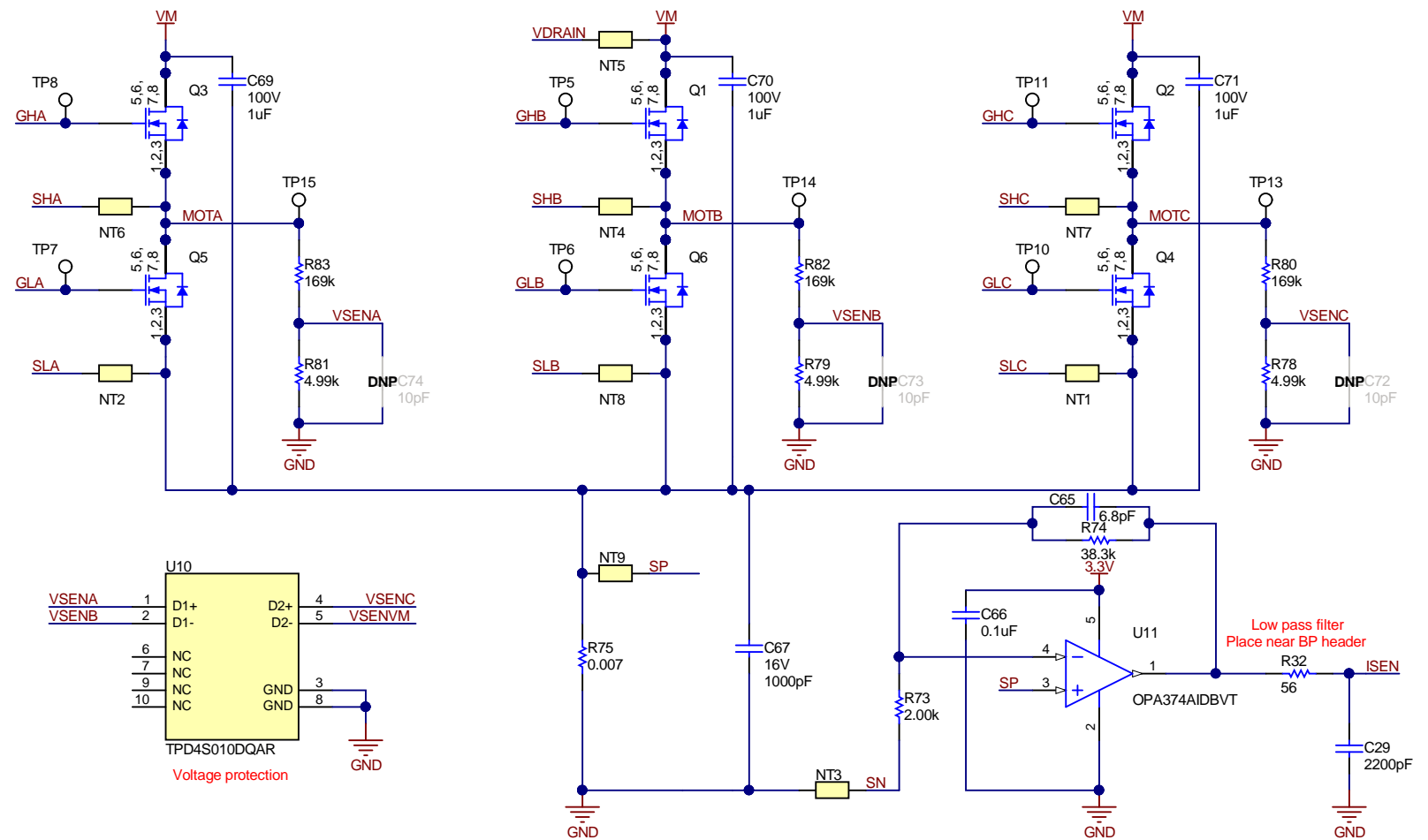
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