

Title: **CAN Transceivers**

Approved: NO

Rev: 01

Project: **CW308T-MPC5Y**

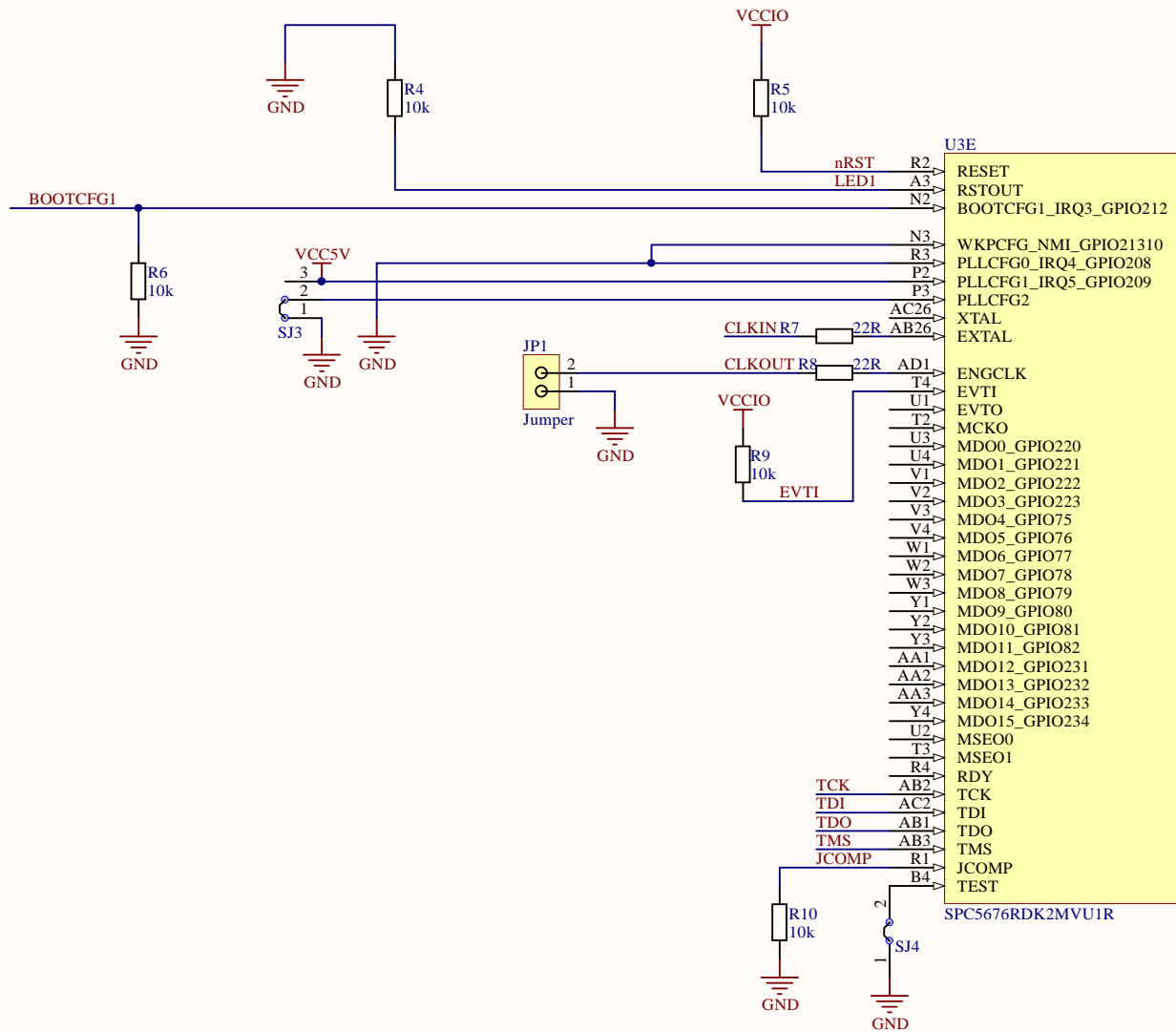
License: **GPL**

Date: 2020-01-12 Time: 12:18:27 PM Sheet1 of 5

Copyright © NewAE Technology Inc. NewAE.com

File: [mpc56_can.SchDoc](#)





Title: **Clock/Reset Drivers**

Approved: NO

Rev: 01

Project: **CW308T-MPC5Y**

License: **GPL**

Date: 2020-01-12 Time: 12:18:27 PM Sheet2 of 5

Copyright © NewAE Technology Inc. NewAE.com

File: **mpc56_clk.SchDoc**



A

A

B

B

C

C

D

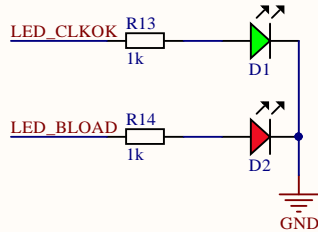
D

U3B		
A4	ANA0	
B5	ANA1	
C5	ANA2	
D6	ANA3	
A5	ANA4	
B6	ANA5	
C6	ANA6	
D7	ANA7	
A6	ANA8	
C7	ANA9	
B7	ANA10	
A7	ANA11	
D8	ANA12	
C8	ANA13	
B8	ANA14	
A8	ANA15	
D9	ANA16	ANB23
C9	ANA17	ANB22
D10	ANA18	ANB21
C10	ANA19	ANB20
D11	ANA20	ANB19
C11	ANA21	ANB18
D12	ANA22	ANB17
C12	ANA23	ANB16
B12	ANA24	ANB15
D13	ANA25	ANB14
C13	ANA26	ANB13
B13	ANA27	ANB12
A13	ANA28	ANB11
B14	ANA29	ANB10
C14	ANA30	ANB9
D14	ANA31	ANB8
A14	ANA32	ANB7
B15	ANA33	ANB6
C15	ANA34	ANB5
D15	ANA35	ANB4
A15	ANA36	ANB3
C16	ANA37	ANB2
D17	ANA38	ANB1
C17	ANA39	ANB0
D16		

SPC5676RDK2MVU1R

U3D		
AD4	FR_A_TX_GPIO248	
AE3	FR_A_RX_GPIO249	
AF3	FR_A_TX_EN_GPIO250	
AD5	FR_B_TX_GPIO251	
AE4	FR_B_RX_GPIO252	
AF4	FR_B_TX_EN_GPIO253	
AF19	CNTXA_TXDA_GPIO083	
AE19	CNRXA_RXDA_GPIO084	
AD19	CNTXB_PCSC3_GPIO085	
AC19	CNRXB_PCSC4_GPIO086	
AF20	CNTXC_PCSD5_GPIO087	
AE20	CNRXC_PCSD4_GPIO088	
AD20	CNTXD_GPIO246	
AC20	CNRXD_GPIO247	
M2	TXDA_GPIO89	
M3	RXDA_GPIO90	
P1	TXDB_PCSD1_GPIO91	
N1	RXDB_PCSD5_GPIO92	
AF23	TXDC_ETRIG1_GPIO244	
AD22	RXDC_GPIO245	
AD8	SCKA_PCSC1_GPIO93	
AF7	SINA_PCSC2_GPIO94	
AD7	SOUTA_PCSC5_GPIO95	
AE6	PCSA0_PCSD2_GPIO96	
AC6	PCSA1_PCSE0_GPIO97	
AC7	PCSA2_SOUTE_GPIO98	
AE7	PCSA3_SINE_GPIO99	
AE5	PCSA4_SCKE_GPIO100	
AD6	PCSA5_ETRIG1_GPIO101	
AE8	SCKB_GPIO102	
AE9	SINB_GPIO103	
AF9	SOUTB_GPIO104	
AD9	PCSB0_PCSD2_GPIO105	
AC9	PCSB1_PCSD0_GPIO106	
AF8	PCSB2_SOUTC_GPIO107	
AD10	PCSB3_SINC_GPIO108	
AC8	PCSB4_SCKC_GPIO109	
AF6	PCSB5_PCSC0_GPIO110	
AD21	SCKC_SCK_C_LVDSP_GPIO235	
AE22	SINC_SCK_C_LVDSP_GPIO236	
AF21	SOUTC_SOUT_C_LVDSP_GPIO237	
AE21	PCSC0_SOUT_C_LVDSP_GPIO238	
AC22	PCSC1_GPIO239	
AE23	PCSC2_GPIO240	
AD23	PCSC3_GPIO241	
AF24	PCSC4_GPIO242	
AE24	PCSC5_GPIO243	

SPC5676RDK2MVU1R



U3A		
L1	TCRCLKA_IRQ7_GPIO113	
L2	ETPUA0_ETPUA12_GPIO114	EMIOS31_PCSC5_GPIO437
L3	ETPUA1_ETPUA13_GPIO115	EMIOS30_PCSC2_GPIO436
L4	ETPUA2_ETPUA14_GPIO116	EMIOS29_PCSC1_GPIO435
K1	ETPUA3_ETPUA15_GPIO117	EMIOS28_PCSC0_GPIO434
K2	ETPUA4_ETPUA16_GPIO118	EMIOS27_PCSC3_GPIO433
K3	ETPUA5_ETPUA17_GPIO119	EMIOS26_PCSC2_GPIO432
K4	ETPUA6_ETPUA18_GPIO120	EMIOS25_PCSC1_GPIO431
J1	ETPUA7_ETPUA19_GPIO121	EMIOS24_PCSC0_GPIO430
J2	ETPUA8_ETPUA20_GPIO122	EMIOS23_ETPUB7_GPIO202
J3	ETPUA9_ETPUA21_GPIO123	EMIOS22_ETPUB6_GPIO201
J4	ETPUA10_ETPUA22_GPIO124	EMIOS21_ETPUB5_GPIO200
H1	ETPUA11_ETPUA23_GPIO125	EMIOS20_ETPUB4_GPIO199
H2	ETPUA12_PCSC1_GPIO126	EMIOS19_ETPUB3_GPIO198
H4	ETPUA13_PCSC3_GPIO127	EMIOS18_ETPUB2_GPIO197
H3	ETPUA14_PCSC4_GPIO128	EMIOS17_ETPUB1_GPIO196
G1	ETPUA15_PCSC5_GPIO129	EMIOS16_ETPUB0_GPIO195
G2	ETPUA16_PCSD1_GPIO130	EMIOS15_IRQ1_GPIO194
G3	ETPUA17_PCSD2_GPIO131	EMIOS14_IRQ0_GPIO193
G4	ETPUA18_PCSD3_GPIO132	EMIOS13_SOUTD_GPIO192
F1	ETPUA19_PCSD4_GPIO133	EMIOS12_SOUTC_GPIO191
F2	ETPUA20_IRQ8_GPIO134	EMIOS11_SIND_GPIO190
F3	ETPUA21_IRQ9_GPIO135	EMIOS10_SCKD_GPIO189
F4	ETPUA22_IRQ10_GPIO136	EMIOS9_ETPUA9_GPIO188
E1	ETPUA23_IRQ11_GPIO137	EMIOS8_ETPUA8_GPIO187
E2	ETPUA24_IRQ12_GPIO138	EMIOS7_ETPUA7_GPIO186
E3	ETPUA25_IRQ13_GPIO139	EMIOS6_ETPUA6_GPIO185
E4	ETPUA26_IRQ14_GPIO140	EMIOS5_ETPUA5_GPIO184
D1	ETPUA27_IRQ15_GPIO141	EMIOS4_ETPUA4_GPIO183
D2	ETPUA28_PCSC1_GPIO142	EMIOS3_ETPUA3_GPIO182
D3	ETPUA29_PCSC2_GPIO143	EMIOS2_ETPUA2_GPIO181
C1	ETPUA30_PCSC3_GPIO144	EMIOS1_ETPUA1_GPIO180
C2	ETPUA31_PCSC4_GPIO145	EMIOS0_ETPUA0_GPIO179
T23	TCRCLKB_IRQ6_GPIO146	ETPUC31_SIND_GPIO472
T24	ETPUB0_ETPUB16_GPIO147	ETPUC30_SOUTD_GPIO471
T25	ETPUB1_ETPUB17_GPIO148	ETPUC29_SCKD_GPIO470
T26	ETPUB2_ETPUB18_GPIO149	ETPUC28_PCSD0_GPIO469
R23	ETPUB3_ETPUB19_GPIO150	ETPUC27_PCSD1_GPIO468
R24	ETPUB4_ETPUB20_GPIO151	ETPUC26_PCSD2_GPIO467
R25	ETPUB5_ETPUB21_GPIO152	ETPUC25_PCSD3_GPIO466
R26	ETPUB6_ETPUB22_GPIO153	ETPUC24_PCSD4_GPIO465
P23	ETPUB7_ETPUB23_GPIO154	ETPUC23_PCSD5_GPIO464
P24	ETPUB8_ETPUB24_GPIO155	ETPUC22_RXDB_GPIO463
P25	ETPUB9_ETPUB25_GPIO156	ETPUC21_TXDB_GPIO462
P26	ETPUB10_ETPUB26_GPIO157	ETPUC20_RXDA_GPIO461
N24	ETPUB11_ETPUB27_GPIO158	ETPUC19_TXDA_GPIO460
N25	ETPUB12_ETPUB28_GPIO159	ETPUC18_FR_A_TX_EN_GPIO459
N26	ETPUB13_ETPUB29_GPIO160	ETPUC17_FR_A_RX_GPIO458
M25	ETPUB14_ETPUB30_GPIO161	ETPUC16_FR_A_TX_GPIO457
M24	ETPUB15_ETPUB31_GPIO162	ETPUC15_GPIO456
U26	ETPUB16_PCSC1_GPIO163	ETPUC14_4_IRQ5_GPIO455
U25	ETPUB17_PCSC2_GPIO164	ETPUC13_3_IRQ4_GPIO454
U24	ETPUB18_PCSC3_GPIO165	ETPUC12_IRQ3_GPIO453
U23	ETPUB19_PCSC4_GPIO166	ETPUC11_IRQ2_GPIO452
V26	ETPUB20_GPIO167	ETPUC10_IRQ1_GPIO451
V25	ETPUB21_GPIO168	ETPUC9_IRQ0_GPIO450
V24	ETPUB22_GPIO169	ETPUC8_PCSE5_GPIO449
W26	ETPUB23_GPIO170	ETPUC7_PCSE4_GPIO448
W25	ETPUB24_GPIO171	ETPUC6_PCSE3_GPIO447
W24	ETPUB25_GPIO172	ETPUC5_PCSE2_GPIO446
V23	ETPUB26_GPIO173	ETPUC4_PCSE1_GPIO445
Y25	ETPUB27_GPIO174	ETPUC3_GPIO444
Y24	ETPUB28_GPIO175	ETPUC2_GPIO443
Y23	ETPUB29_GPIO176	ETPUC1_GPIO442
AA24	ETPUB30_GPIO177	ETPUC0_GPIO441
AB24	ETPUB31_GPIO178	TCRCLKC_GPIO440

SPC5676RDK2MVU1R

Title: CPU I/O Bank 2

Approved: NO

Rev: 01

Project: CW308T-MPC5Y

License: GPL

Date: 2020-01-12

Time: 12:18:28 PM

Sheet3 of 5

Copyright © NewAE Technology Inc. NewAE.com

File: mpc56_cpuio2.SchDoc



