

Title: **CAN Transceivers**

Approved: YES



Rev: 03

Project: **CW308T-MPC5Y**

License: GPL

Date: 2020-03-20

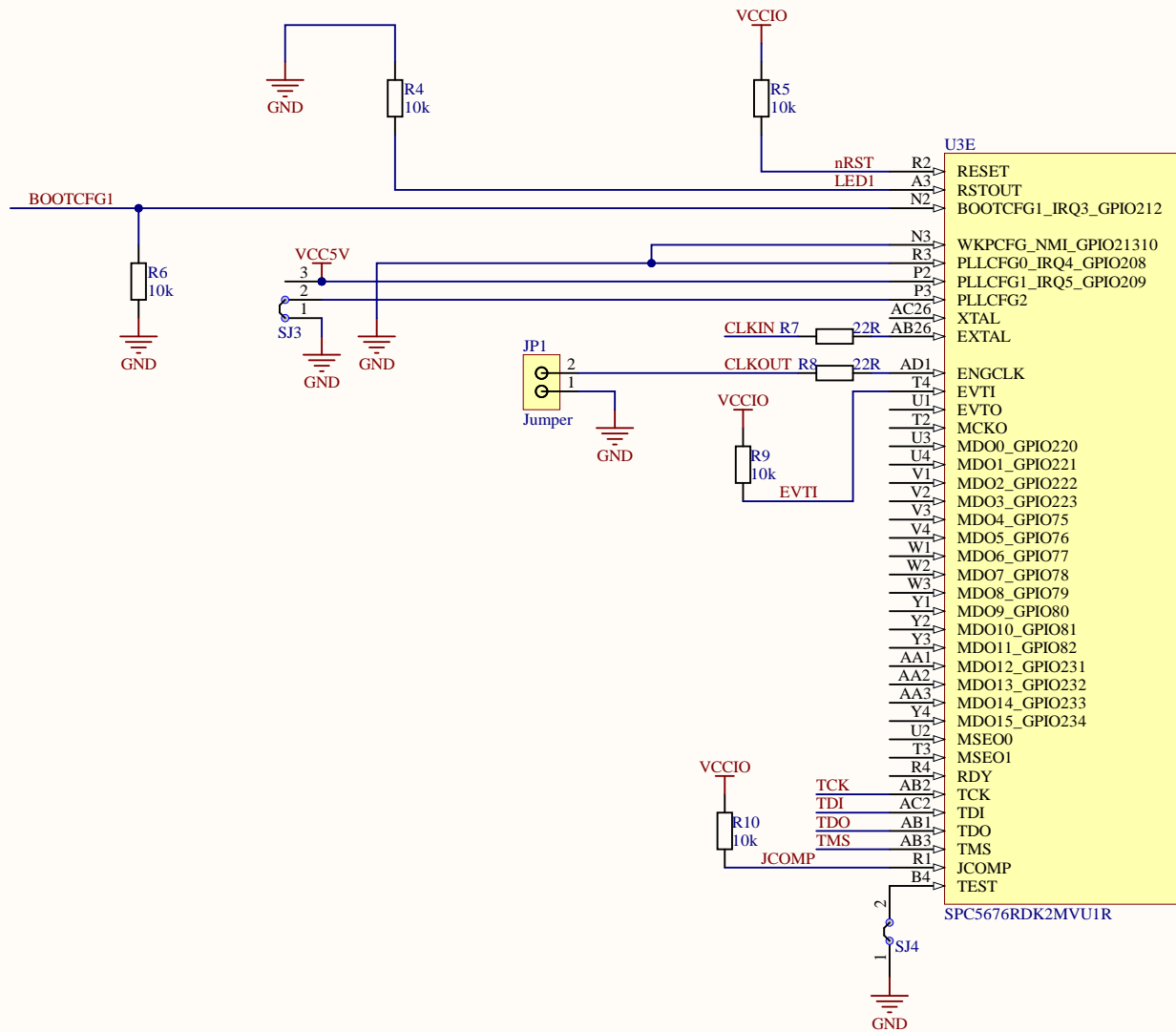
Time: 9:09:50 AM

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File: mpc56_can.SchDoc



Title: **Clock/Reset Drivers**

Approved: YES

Rev: 03

Project: CW308T-MPC5Y

License: GPL

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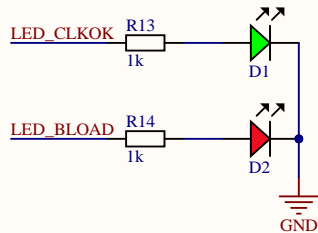


U3B		
A4	ANA0	
B5	ANA1	
C5	ANA2	
D6	ANA3	
A5	ANA4	
B6	ANA5	
C6	ANA6	
D7	ANA7	
A6	ANA8	
C7	ANA9	
B7	ANA10	
A7	ANA11	
D8	ANA12	
C8	ANA13	
B8	ANA14	
A8	ANA15	
D9	ANA16	ANB23
C9	ANA17	ANB22
D10	ANA18	ANB21
C10	ANA19	ANB20
D11	ANA20	ANB19
C11	ANA21	ANB18
D12	ANA22	ANB17
C12	ANA23	ANB16
B12	ANA24	ANB15
D13	AN25	ANB14
C13	AN26	ANB13
B13	AN27	ANB12
A13	AN28	ANB11
B14	AN29	ANB10
C14	AN30	ANB9
D14	AN31	ANB8
A14	AN32	ANB7
B15	AN33	ANB6
C15	AN34	ANB5
D15	AN35	ANB4
A15	AN36	ANB3
C16	AN37	ANB2
D17	AN38	ANB1
A16	AN39	ANB0

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U3D	
AD4	FR_A_TX_GPIO248
AE3	FR_A_RX_GPIO249
AF3	FR_A_TX_EN_GPIO250
AD5	FR_B_TX_GPIO251
AE4	FR_B_RX_GPIO252
AF4	FR_B_TX_EN_GPIO253
AF19	CNTXA_TXDA_GPIO83
AE19	CNRXA_RXDA_GPIO84
AD19	CNTXB_PCSC3_GPIO85
AC19	CNRXB_PCSC4_GPIO86
AF20	CNTXC_PCSD3_GPIO87
AE20	CNRXC_PCSD4_GPIO88
AD20	CNTXD_GPIO246
AC20	CNRXD_GPIO247
M2	TXDA_GPIO89
M3	RXDA_GPIO90
P1	TXDB_PCSD1_GPIO91
N1	RXDB_PCSD5_GPIO92
AF23	TXDC_ETRIG0_GPIO244
AD22	RXDC_GPIO245
AD8	SCKA_PCSC1_GPIO93
AF7	SINA_PCSC2_GPIO94
AD7	SOUTA_PCSC5_GPIO95
AE6	PCSA0_PCSD2_GPIO96
AC6	PCSA1_PCSE0_GPIO97
AC7	PCSA2_SOUTE_GPIO98
AE7	PCSA3_SINE_GPIO99
AE5	PCSA4_SCKE_GPIO100
AD6	PCSA5_ETRIG1_GPIO101
AE8	SCKB_GPIO102
AE9	SINB_GPIO103
AF9	SOUTB_GPIO104
AD9	PCSB0_PCSD2_GPIO105
AC9	PCSB1_PCSD0_GPIO106
AF8	PCSB2_SOUTC_GPIO107
AD10	PCSB3_SINC_GPIO108
AC8	PCSB4_SCKC_GPIO109
AF6	PCSB5_PCSC0_GPIO110
AD21	SCKC_SCK_C_LVDSP_GPIO235
AE22	SINC_SCK_C_LVDSP_GPIO236
AF21	SOUTC_SOUT_C_LVDSP_GPIO237
AE21	PCSC0_SOUT_C_LVDSP_GPIO238
AC22	PCSC1_GPIO239
AE23	PCSC2_GPIO240
AD23	PCSC3_GPIO241
AF24	PCSC4_GPIO242
AE24	PCSC5_GPIO243

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U3A	
L1	TCRCLKA_IRQ7_GPIO113
L2	ETPUA0_ETPUA12_GPIO114
L3	ETPUA1_ETPUA13_GPIO115
L4	ETPUA2_ETPUA14_GPIO116
K1	ETPUA3_ETPUA15_GPIO117
K2	ETPUA4_ETPUA16_GPIO118
K3	ETPUA5_ETPUA17_GPIO119
K4	ETPUA6_ETPUA18_GPIO120
J1	ETPUA7_ETPUA19_GPIO121
J2	ETPUA8_ETPUA20_GPIO122
J3	ETPUA9_ETPUA21_GPIO123
J4	ETPUA10_ETPUA22_GPIO124
H1	ETPUA11_ETPUA23_GPIO125
H2	ETPUA12_PCSC1_GPIO126
H4	ETPUA13_PCSC3_GPIO127
H3	ETPUA14_PCSC4_GPIO128
G1	ETPUA15_PCSC5_GPIO129
G2	ETPUA16_PCSD1_GPIO130
G3	ETPUA17_PCSD2_GPIO131
G4	ETPUA18_PCSD3_GPIO132
F1	ETPUA19_PCSD4_GPIO133
F2	ETPUA20_IRQ8_GPIO134
F3	ETPUA21_IRQ9_GPIO135
F4	ETPUA22_IRQ10_GPIO136
E1	ETPUA23_IRQ11_GPIO137
E2	ETPUA24_IRQ12_GPIO138
E3	ETPUA25_IRQ13_GPIO139
E4	ETPUA26_IRQ14_GPIO140
D1	ETPUA27_IRQ15_GPIO141
D2	ETPUA28_PCSC1_GPIO142
D3	ETPUA29_PCSC2_GPIO143
C1	ETPUA30_PCSC3_GPIO144
C2	ETPUA31_PCSC4_GPIO145
T23	TCRCLKB_IRQ6_GPIO146
T24	ETPUB0_ETPUB16_GPIO147
T25	ETPUB1_ETPUB17_GPIO148
T26	ETPUB2_ETPUB18_GPIO149
R23	ETPUB3_ETPUB19_GPIO150
R24	ETPUB4_ETPUB20_GPIO151
R25	ETPUB5_ETPUB21_GPIO152
R26	ETPUB6_ETPUB22_GPIO153
P23	ETPUB7_ETPUB23_GPIO154
P24	ETPUB8_ETPUB24_GPIO155
P25	ETPUB9_ETPUB25_GPIO156
P26	ETPUB10_ETPUB26_GPIO157
N24	ETPUB11_ETPUB27_GPIO158
N25	ETPUB12_ETPUB28_GPIO159
N26	ETPUB13_ETPUB29_GPIO160
M25	ETPUB14_ETPUB30_GPIO161
M24	ETPUB15_ETPUB31_GPIO162
U26	ETPUB16_PCSC1_GPIO163
U25	ETPUB17_PCSC2_GPIO164
U24	ETPUB18_PCSC3_GPIO165
U23	ETPUB19_PCSC4_GPIO166
V26	ETPUB20_GPIO167
V25	ETPUB21_GPIO168
V24	ETPUB22_GPIO169
W26	ETPUB23_GPIO170
W25	ETPUB24_GPIO171
W24	ETPUB25_GPIO172
V23	ETPUB26_GPIO173
Y25	ETPUB27_GPIO174
Y24	ETPUB28_GPIO175
Y23	ETPUB29_GPIO176
AA24	ETPUB30_GPIO177
AB24	ETPUB31_GPIO178
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EMIOS31_PCSC5_GPIO437	AC18
EMIOS30_PCSC2_GPIO436	AD18
EMIOS29_PCSC1_GPIO435	AE18
EMIOS28_PCSC0_GPIO434	AF18
EMIOS27_PCSC3_GPIO433	AC17
EMIOS26_PCSC2_GPIO432	AD17
EMIOS25_PCSC1_GPIO431	AE17
EMIOS24_PCSC0_GPIO430	AF17
EMIOS23_ETPUB7_GPIO202	AD16
EMIOS22_ETPUB6_GPIO201	AC16
EMIOS21_ETPUB5_GPIO200	AE16
EMIOS20_ETPUB4_GPIO199	AF16 LED_BLOOD
EMIOS19_ETPUB3_GPIO198	AD15
EMIOS18_ETPUB2_GPIO197	AC15
EMIOS17_ETPUB1_GPIO196	AE15
EMIOS16_ETPUB0_GPIO195	AF15 LED_CLKOK
EMIOS15_IRQ1_GPIO194	AD14
EMIOS14_IRQ0_GPIO193	AC14
EMIOS13_SOUTD_GPIO192	AE14
EMIOS12_SOUTC_GPIO191	AF14
EMIOS11_SIND_GPIO190	AE13
EMIOS10_SCKD_GPIO189	AD13
EMIOS9_ETPUA9_GPIO188	AC13
EMIOS8_ETPUA8_GPIO187	AF12
EMIOS7_ETPUA7_GPIO186	AE12
EMIOS6_ETPUA6_GPIO185	AD12
EMIOS5_ETPUA5_GPIO184	AF11
EMIOS4_ETPUA4_GPIO183	AE11
EMIOS3_ETPUA3_GPIO182	AD11
EMIOS2_ETPUA2_GPIO181	AF10
EMIOS1_ETPUA1_GPIO180	AE10
EMIOS0_ETPUA0_GPIO179	M23 MISO
ETPUC31_SIND_GPIO472	L26 MOSI
ETPUC30_SOUTD_GPIO471	L25 SCK
ETPUC29_SCKD_GPIO470	L24 CS
ETPUC28_PCSD0_GPIO469	L23
ETPUC27_PCSD1_GPIO468	K26
ETPUC26_PCSD2_GPIO467	K25
ETPUC25_PCSD3_GPIO466	K24
ETPUC24_PCSD4_GPIO465	K23
ETPUC23_PCSD5_GPIO464	J26
ETPUC22_RXDB_GPIO463	J25
ETPUC21_TXDB_GPIO462	J24
ETPUC20_RXDA_GPIO461	J23
ETPUC19_TXDA_GPIO460	H26
ETPUC18_FR_A_TX_EN_GPIO459	H25
ETPUC17_FR_A_RX_GPIO458	H24
ETPUC16_FR_A_TX_GPIO457	H23
ETPUC15_GPIO456	G26
ETPUC14_4_IRQ5_GPIO455	G25
ETPUC13_3_IRQ4_GPIO454	G24
ETPUC12_IRQ3_GPIO453	G23
ETPUC11_IRQ2_GPIO452	F26
ETPUC10_IRQ1_GPIO451	F25
ETPUC9_IRQ0_GPIO450	F24
ETPUC8_PCSE5_GPIO449	F23
ETPUC7_PCSE4_GPIO448	E26
ETPUC6_PCSE3_GPIO447	E25
ETPUC5_PCSE2_GPIO446	E24
ETPUC4_PCSE1_GPIO445	D26
ETPUC3_GPIO444	D25
ETPUC2_GPIO443	C26
ETPUC1_GPIO442	C25
ETPUC0_GPIO441	B26
TCRCLKC_GPIO440	

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Title: CPU I/O Bank 2

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