

NAND CONFIG

LDnWBE0 D4  
LDnWBE1 B5  
LDnWBE2 D5  
LDnWBE3 E5  
LLnSCS0 D2  
LLnSCAS D6  
LLnSRAS C6  
LLSCKE A2  
LLSCLK0 B4  
LLSCLK1 B3

FLALE D1  
FLCLE F5  
FLRnB G6  
NCON R12  
FLnFCE F4  
FLnFRE E1  
FLnFWE F3

N8  
K8  
R8  
M8  
P8  
J9

nBE0:nWBE0:DQM0  
nBE1:nWBE1:DQM1  
nBE2:nWBE2:DQM2  
nBE3:nWBE3:DQM3  
nGCS6:nSCS0  
nGCS7:nSCS1  
nSCAS  
nSRAS  
SCKE  
SCLK0  
SCLK1

SDRAM

ALE/GPA18  
CLE/GPA17  
FRnB  
NCON  
nFCEGPA22  
nFRE/GPA20  
nFWE/GPA19

NAND CTRL

SDCLK/GPE5  
SDCMD/GPE6  
SDDATA0/GPE7  
SDDATA1/GPE8  
SDDATA2/GPE9  
SDDATA3/GPE10

SDIO

UART

nCTS0/GPH0  
nRTS0/GPH1  
TXD0/GPH2  
RXD0/GPH3  
TXD1/GPH4  
RXD1/GPH5  
nRTS1/TXD2/GPH6  
nCTS1/RXD2/GPH7  
UCLK/GPH8

UART

nTRST  
TCK  
TDI  
TDO  
TMS

JTAG

LEND/GPC0  
VCLK/GPC1  
VLINE:HSYNC/GPC2  
VFRAME:VSYNC/GPC3  
VM:VDEN/GPC4  
LCD\_LPCOE/GPC5  
LCD\_LPCREV/GPC6  
LCD\_PWREN/EINT12/GPG4

LCD PWREN P11

L1  
L4  
M1  
M2  
M3  
M4  
P1

GPC4/VOLK  
GPC2/VLINE  
GPC3/VFRAME  
GPC4/VM

VD0/GPC8  
VD1/GPC9  
VD2/GPC10  
VD3/GPC11  
VD4/GPC12  
VD5/GPC13  
VD6/GPC14  
VD7/GPC15  
VD8/GPD0  
VD9/GPD1  
VD10/GPD2  
VD11/GPD3  
VD12/GPD4  
VD13/GPD5/USBTXDNI  
VD14/GPD6/USBTXDP1  
VD15/GPD7/USBOEN1  
VD16/GPD8/SPIMOSI0  
VD17/GPD9/SPIMOSI1  
VD18/GPD10/LPICLK1  
VD19/GPD11/USBRXDP1  
VD20/GPD12/USBRXDNI  
VD21/GPD13/USBRXD1  
VD22/nSSI/GPD14  
VD23/nSS0/GPD15

TSP

LCD DATA

LCD CTRL

N2  
L6  
N4 LVD2  
R1 LVD3  
N3 LVD4  
P2 LVD5  
M6 LVD6  
P3 LVD7  
R2  
M5  
N5 LVD10  
R3 LVD11  
P4 LVD12  
R4 LVD13  
P5 LVD14  
N6 LVD15  
M7  
T4  
R5 LVD18  
T5 LVD19  
P6 LVD20  
R6 LVD21  
N7 LVD22  
U5 LVD23

S3C2440X

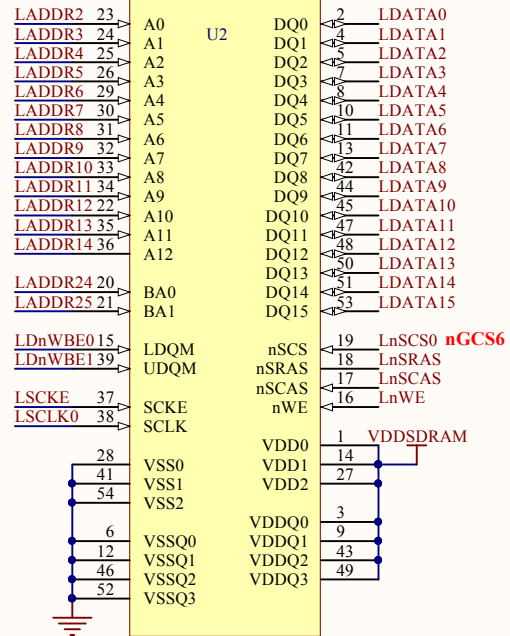
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2

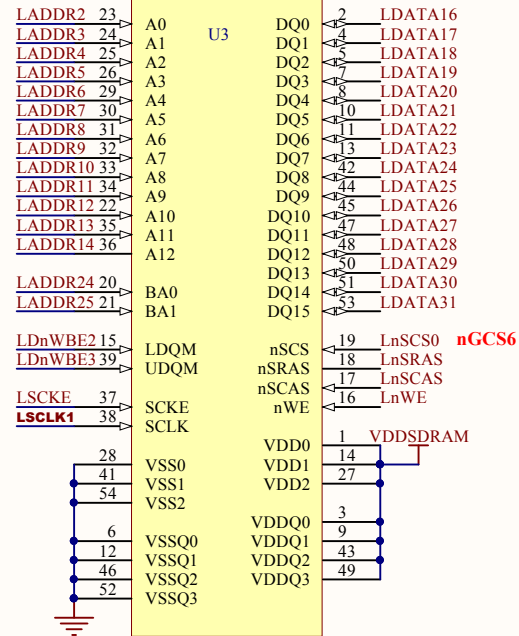
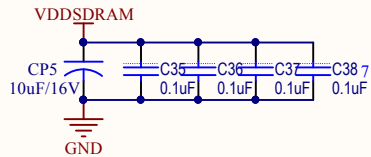
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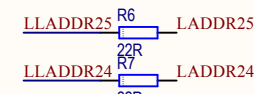
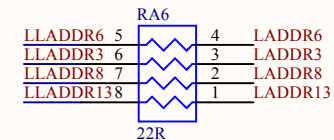
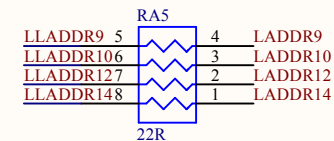
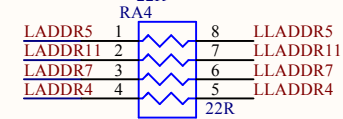
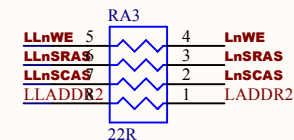
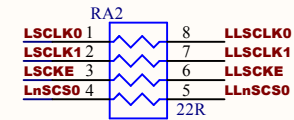
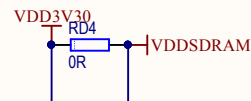
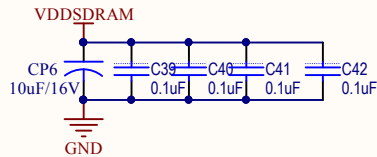
**BANK**  
**32M A[24:23]**  
**64M A[25:24]**



K4S561632C-TC75 (32MB)



K4S561632C-TC75 (32MB)



16x2 M: AD[24:23]  
32x2 M: AD[25:24]

DQM[3:0]数据屏蔽

1

2

3

4



1

2

3

4

A

A

B

B

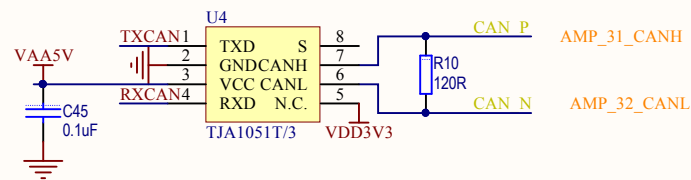
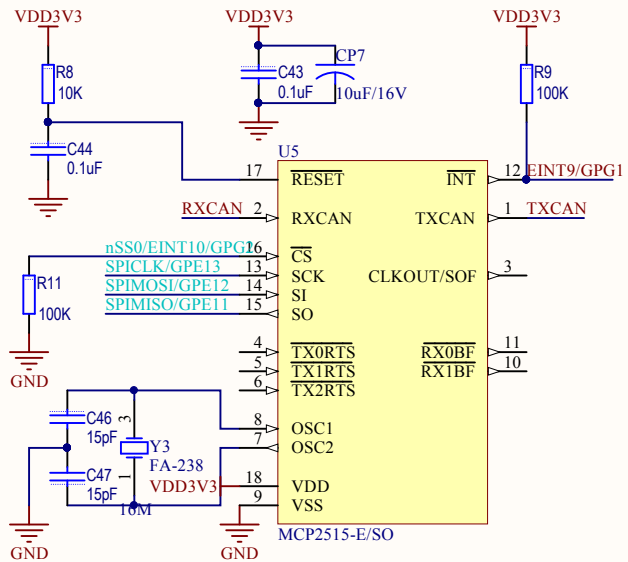
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C

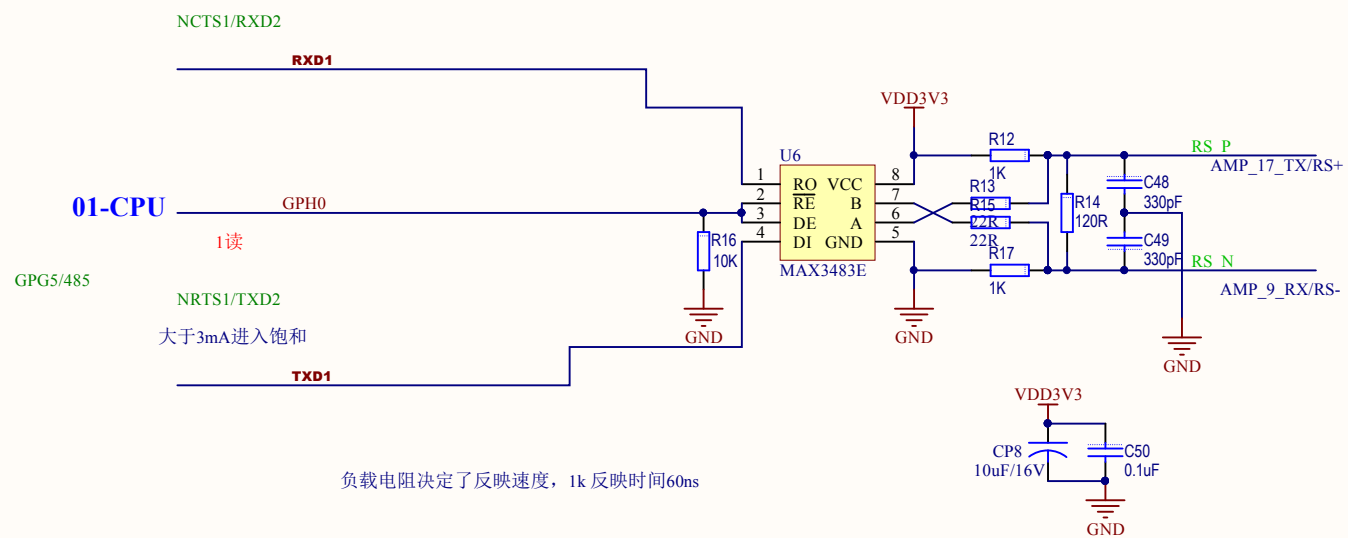
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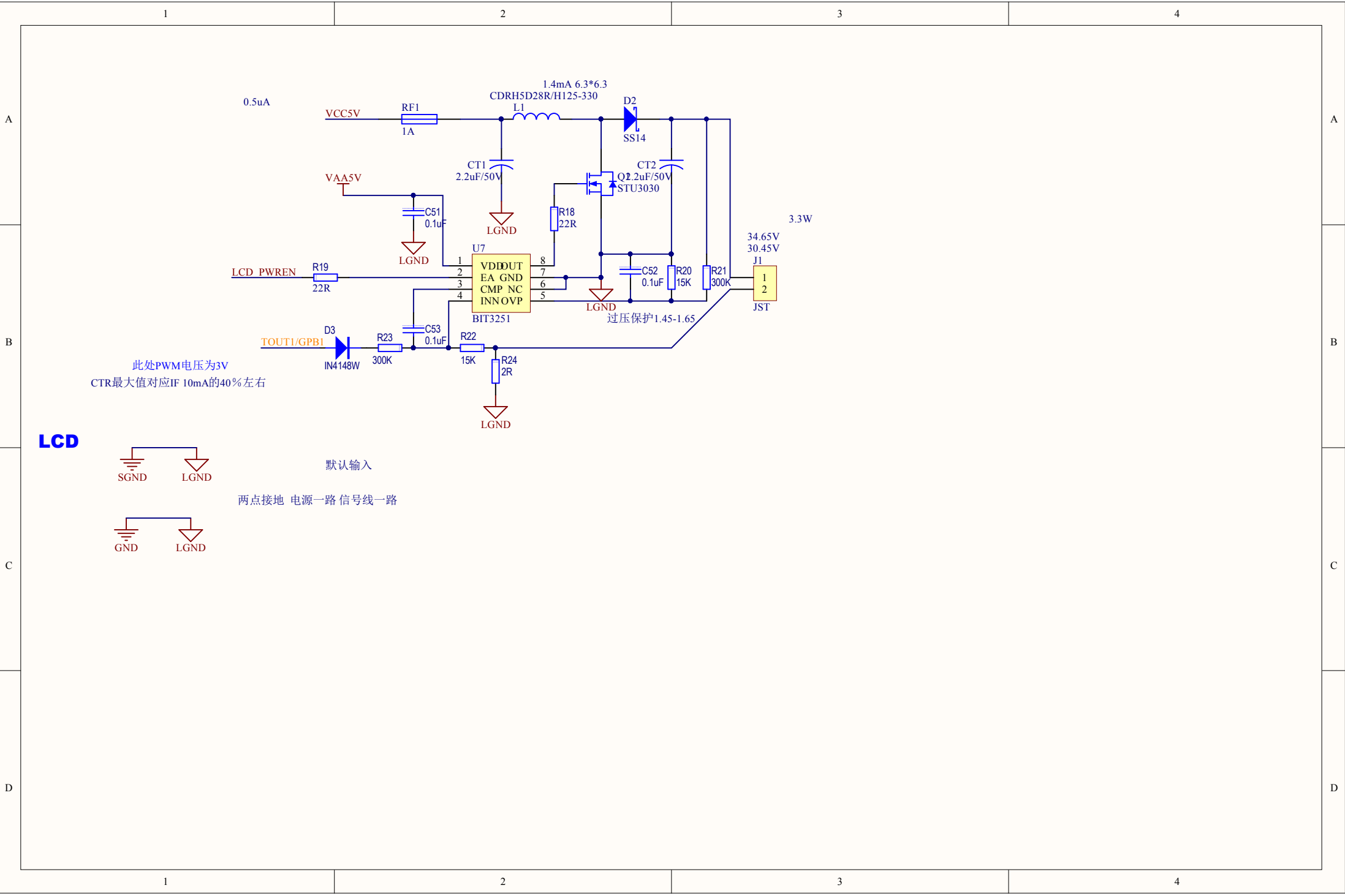
D

功耗: 10mA



CAN

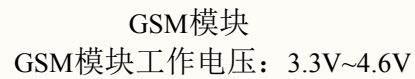




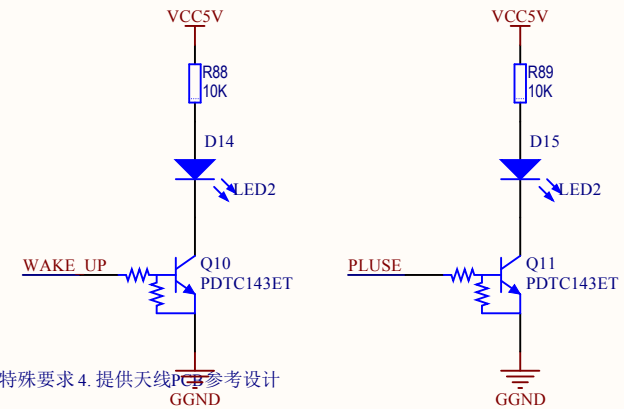
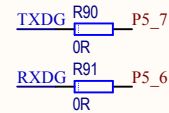
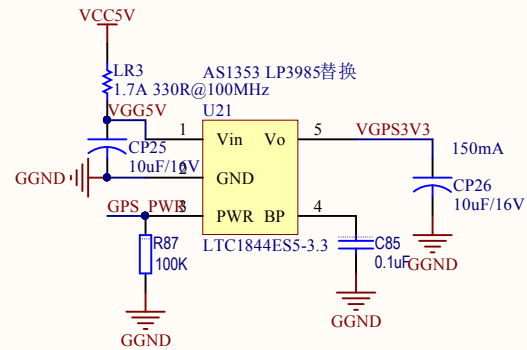




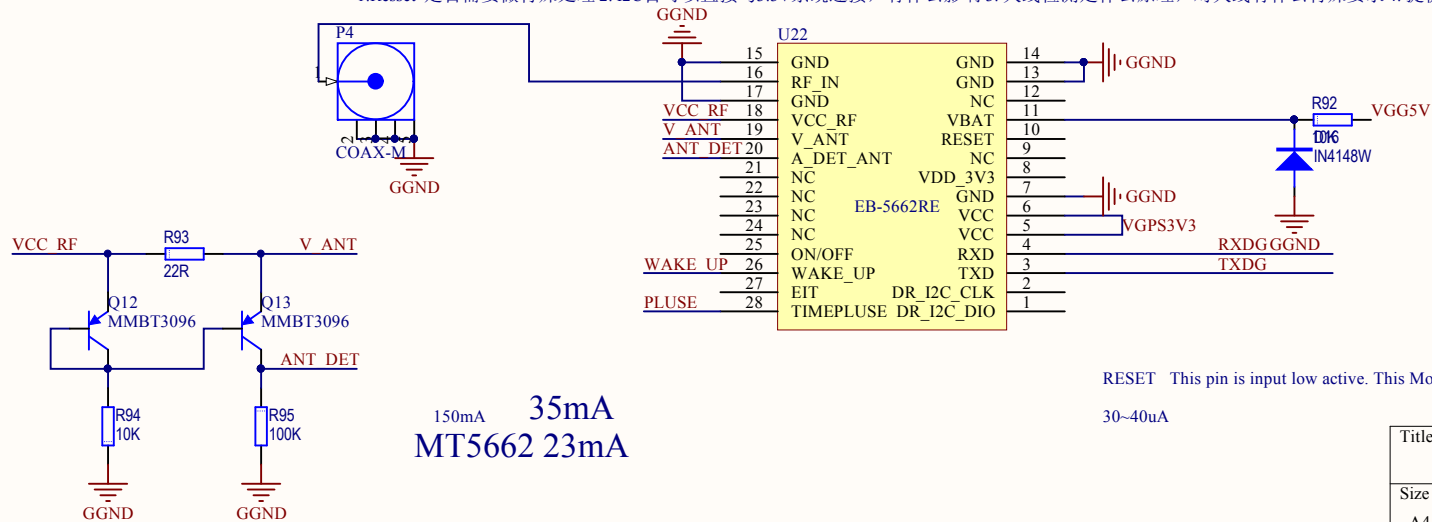
VBAT  $\overset{5\mu A}{\uparrow}$  关机模式下  $30\mu A$



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File:	Z:\MyPCB\13-GPRS SchDoc	Drawn By:



1. Reset 是否需要做特殊处理 2. I2C否可以直接与3.3V系统连接，有什么影响 3. 天线检测是什么原理，对天线有什么特殊要求 4. 提供天线PCB参考设计



RESET This pin is input low active. This Module has internal Power on Reset circuit.

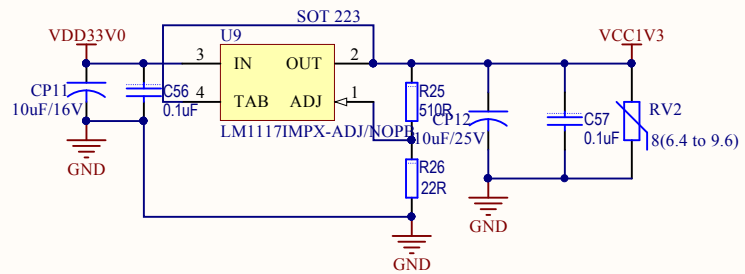
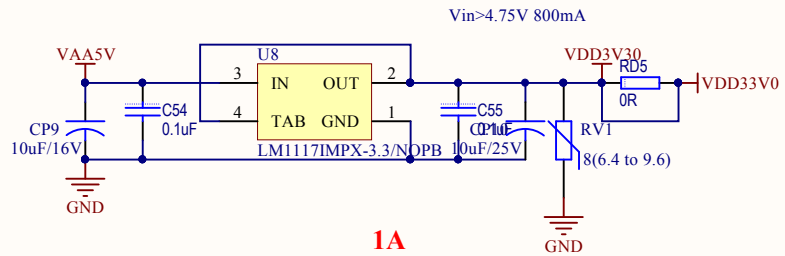
30~40uA

自带 上电复位电路

GPS模块

GPS模块工作电压：3.3V

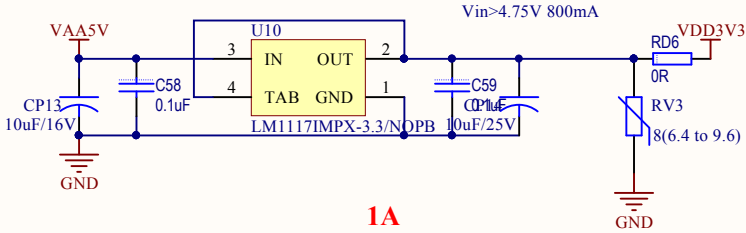
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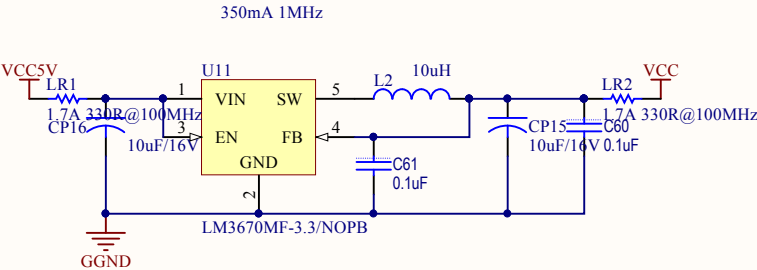


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C	<div>Designator 03-FLASH.Sch</div> <div></div>	<div>Designator 04-MEM.Sch</div> <div></div>			C																
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B	<div>Designator USB Device.SchDoc</div> <div></div> <div>Designator USB Host.SchDoc</div> <div></div> <div>Designator 蜂鸣器.SchDoc</div> <div></div> <div>Designator RESET.SchDoc</div> <div></div> <div>Designator IIC.SchDoc</div> <div></div> <div>Designator LVDS.SchDoc</div> <div></div>	<div>Designator JTAG.SchDoc</div> <div></div> <div>Designator 指示灯.SchDoc</div> <div></div> <div>Designator RTC.SchDoc</div> <div></div> <div>Designator 按键接口.SchDoc</div> <div></div>	
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Designator  
BAT POWER.SchDoc

Designator  
12-BAT POWER.SchDoc

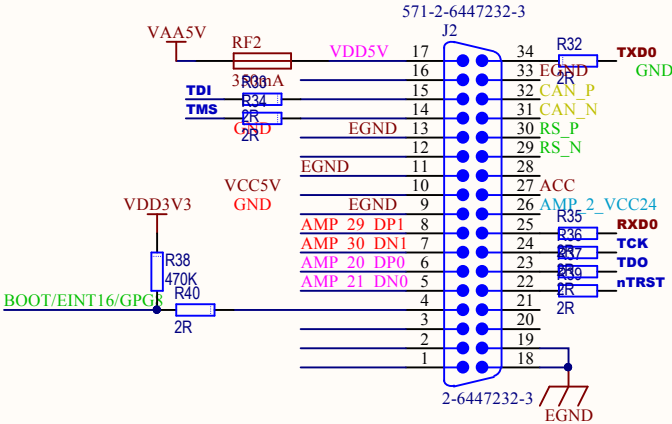
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Designator  
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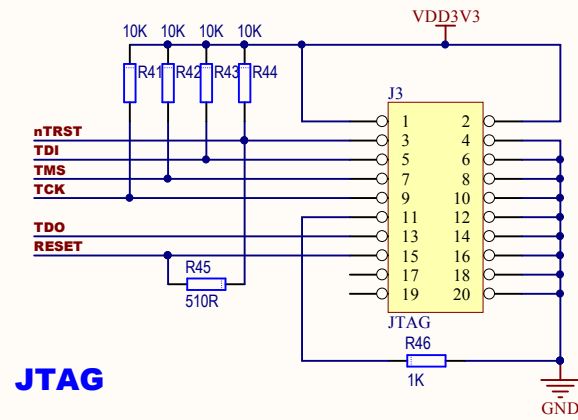
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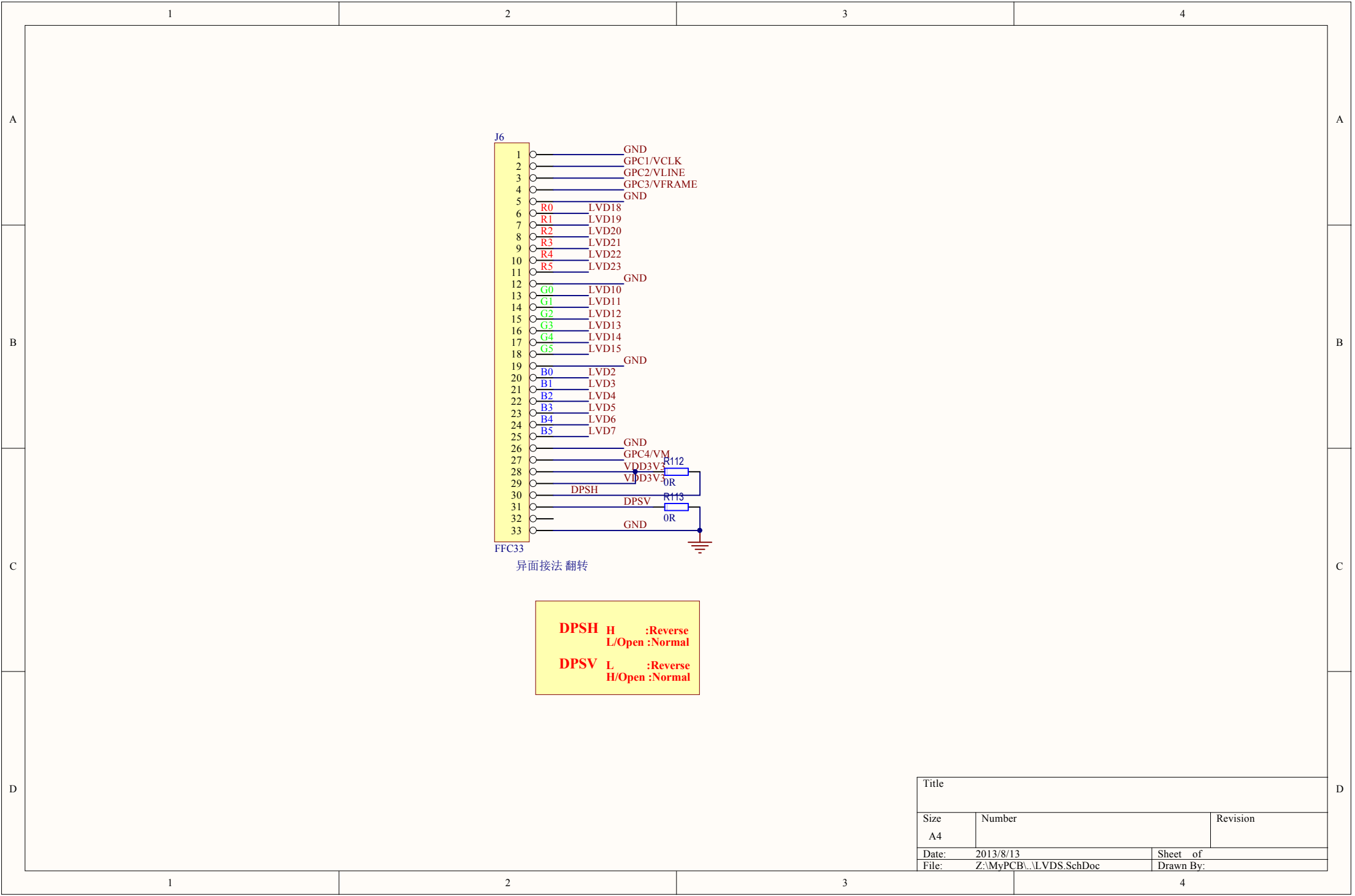




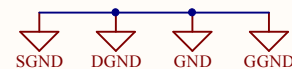
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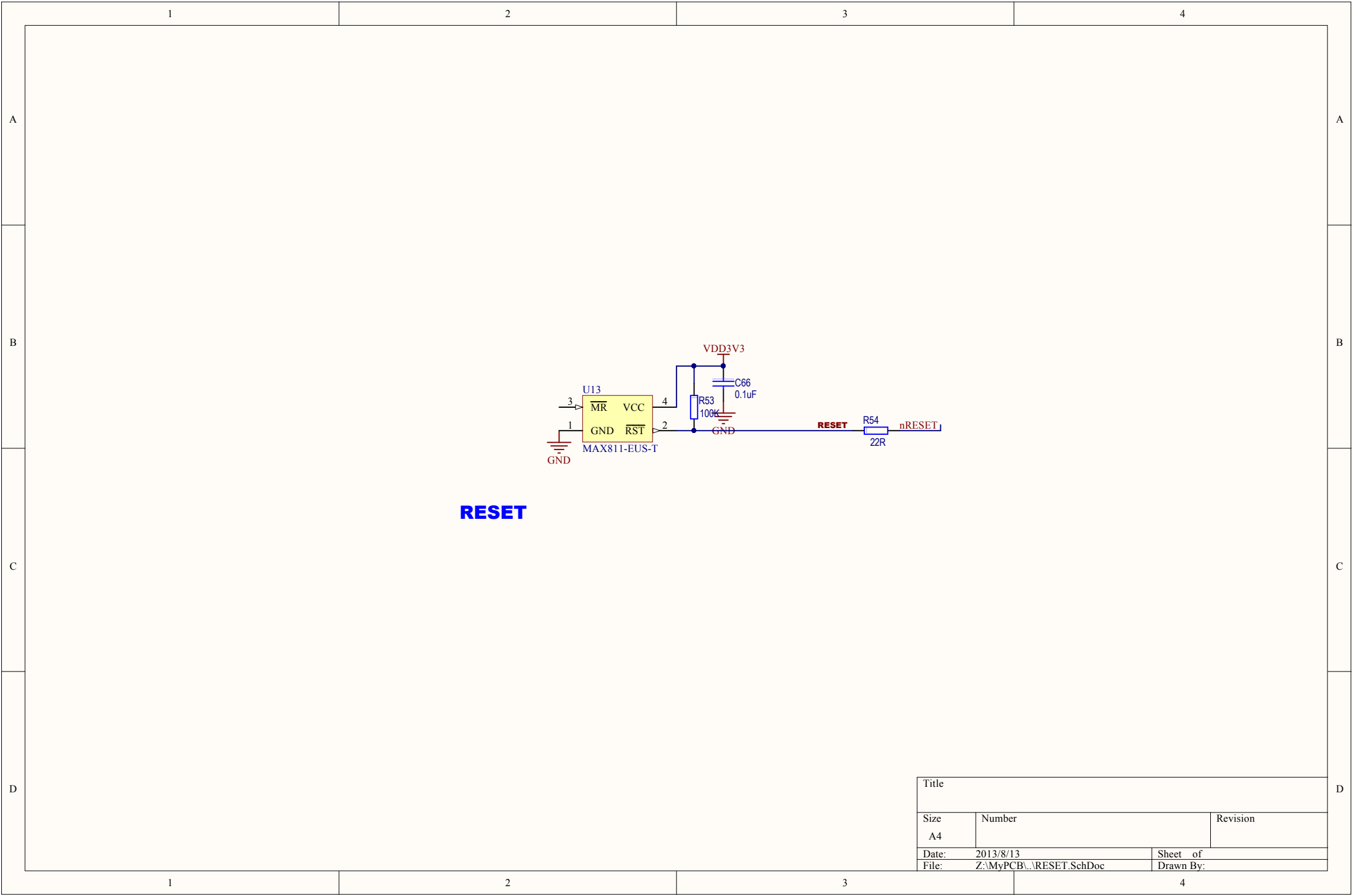
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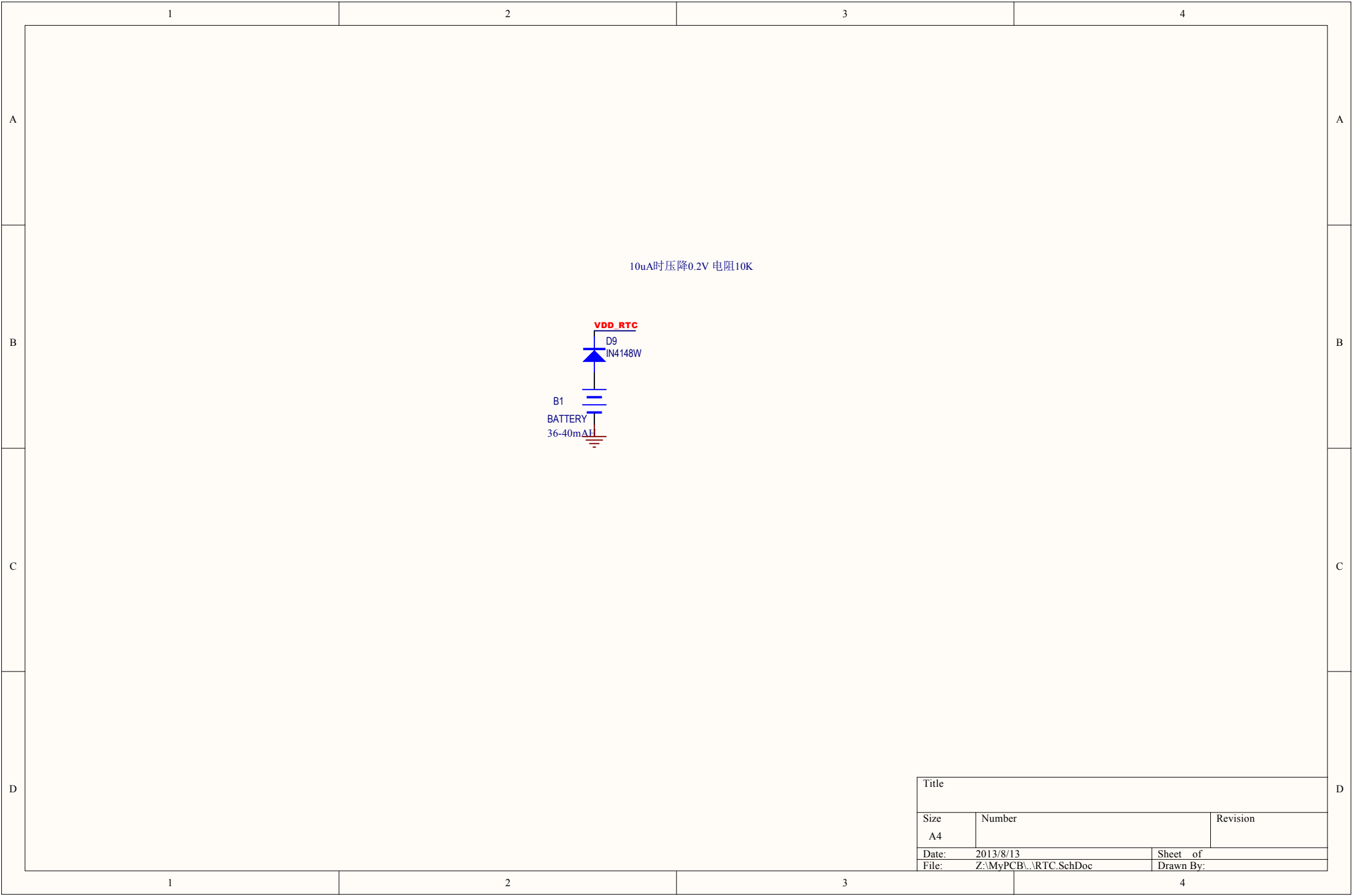


### 3.修改485为串口1



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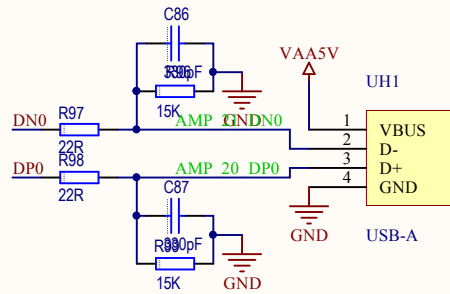




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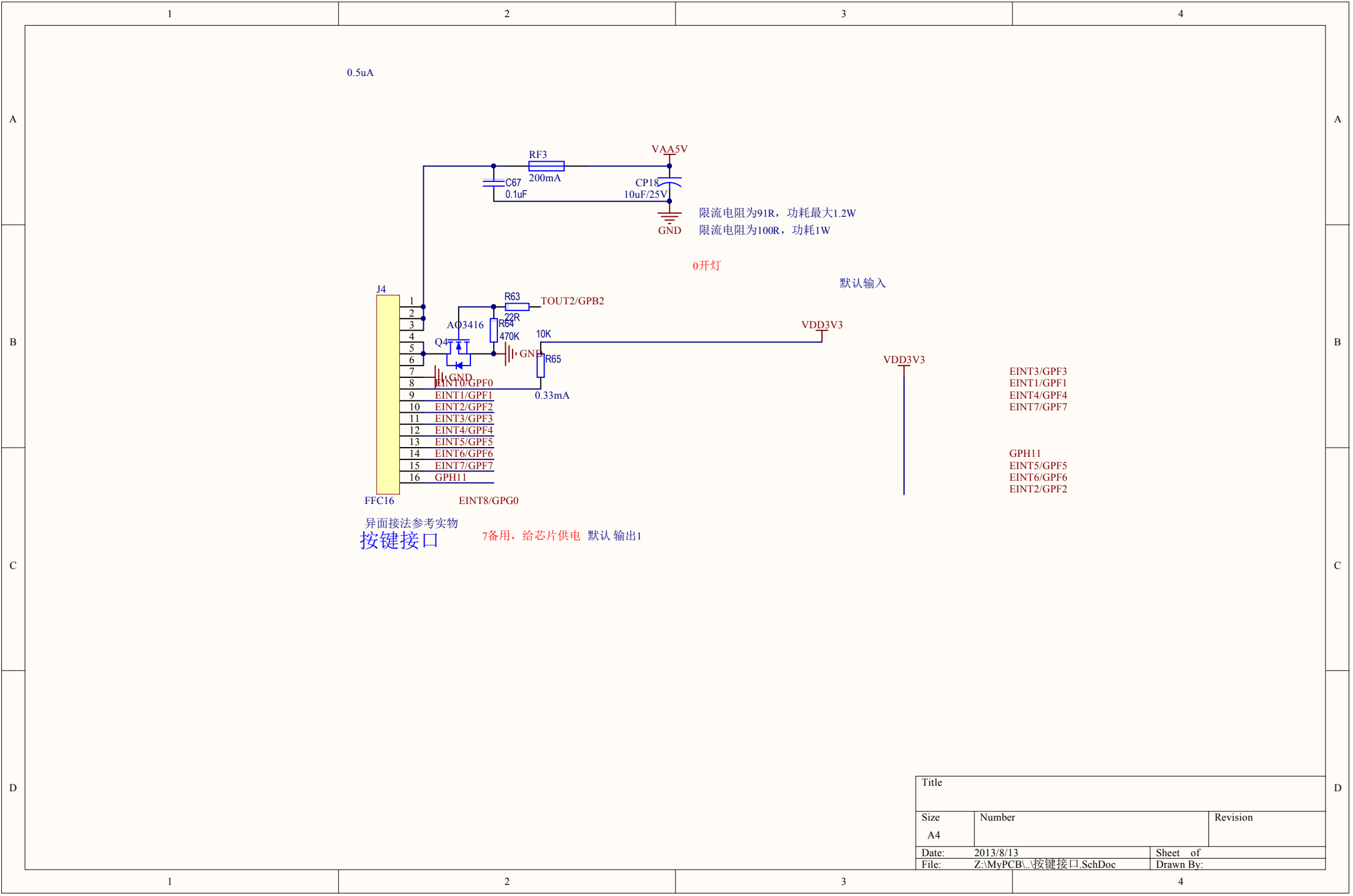


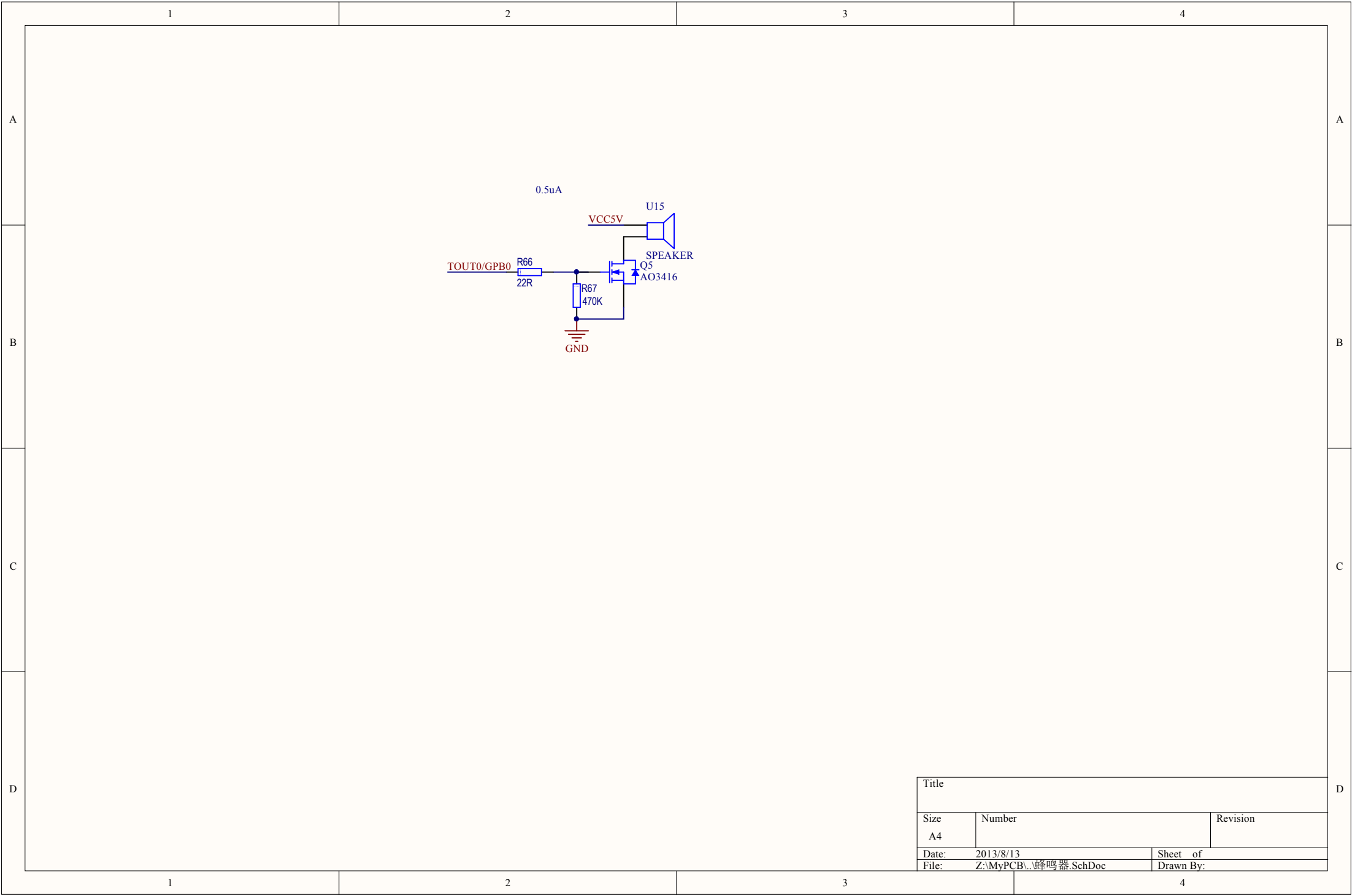




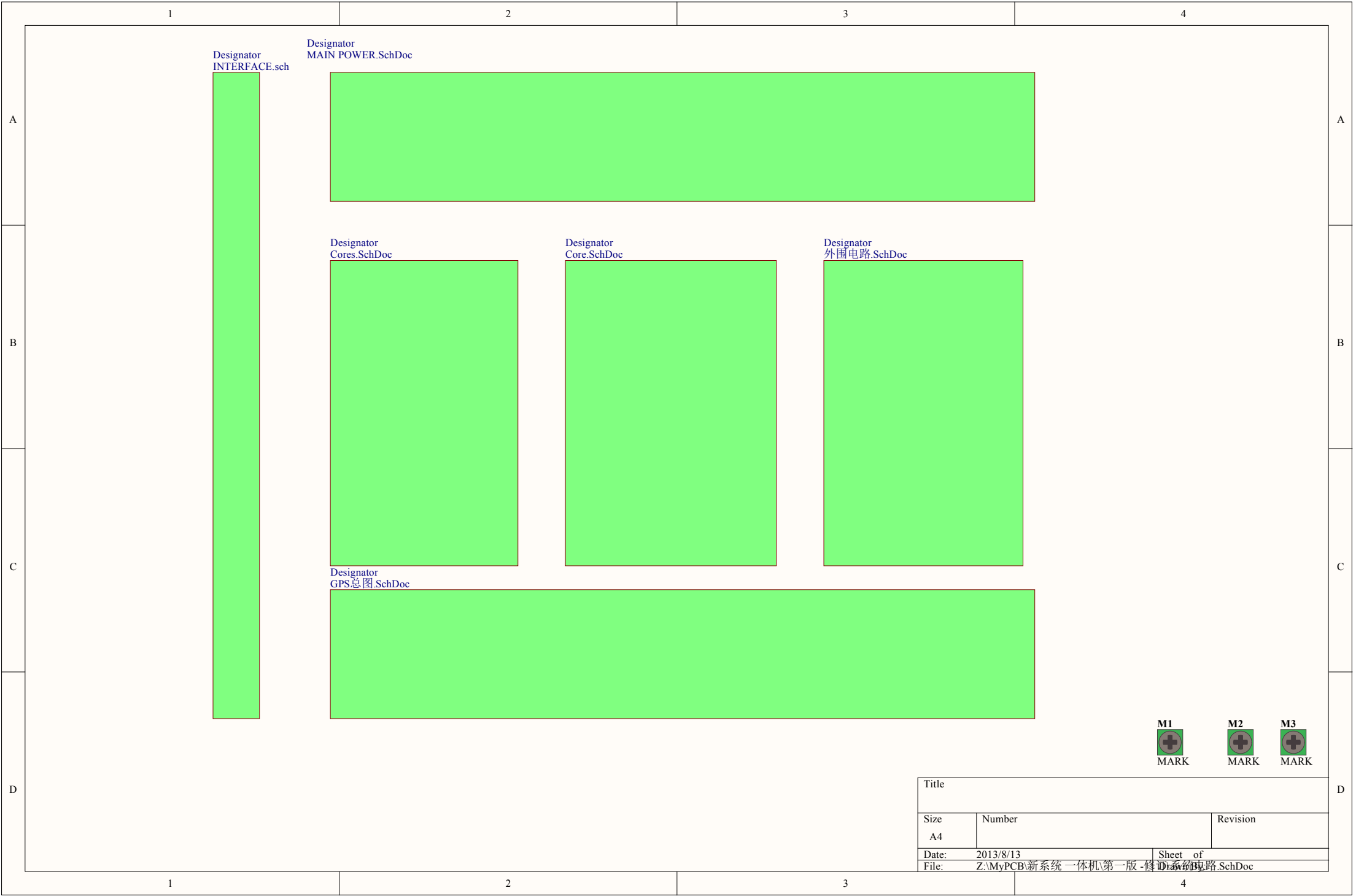
**USB Host**

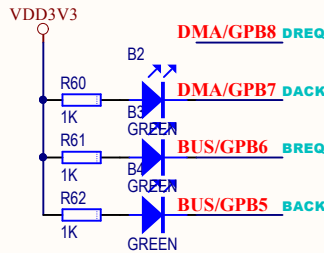
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B				B												
C				C												
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