



Koruza-CM

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Version Revision:

v0.2 - 09.03.2017.

v0.3 - 30.03.2017.

v0.3.1 - 05.05.2017.

DESIGN CONSIDERATIONS

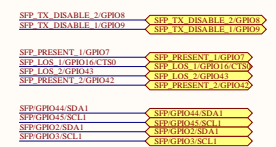
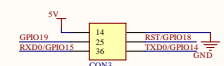
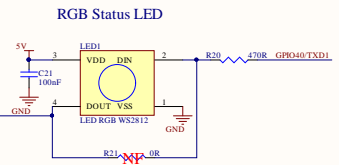
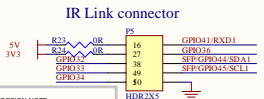
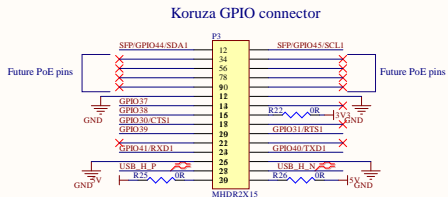
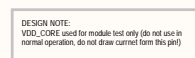
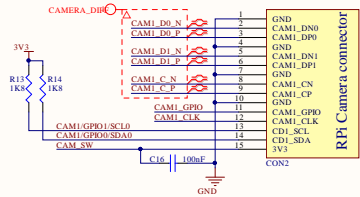
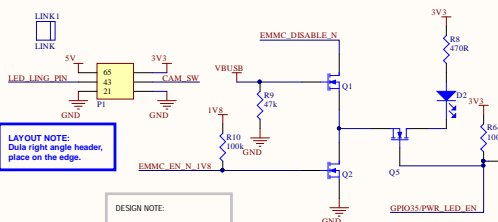
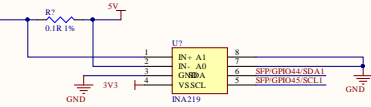
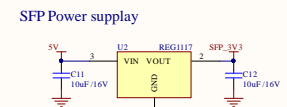
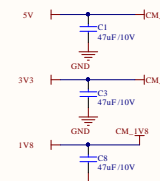
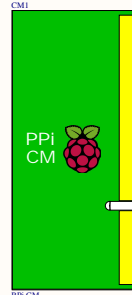
DESIGN NOTE:
Example text for informational
design notes.

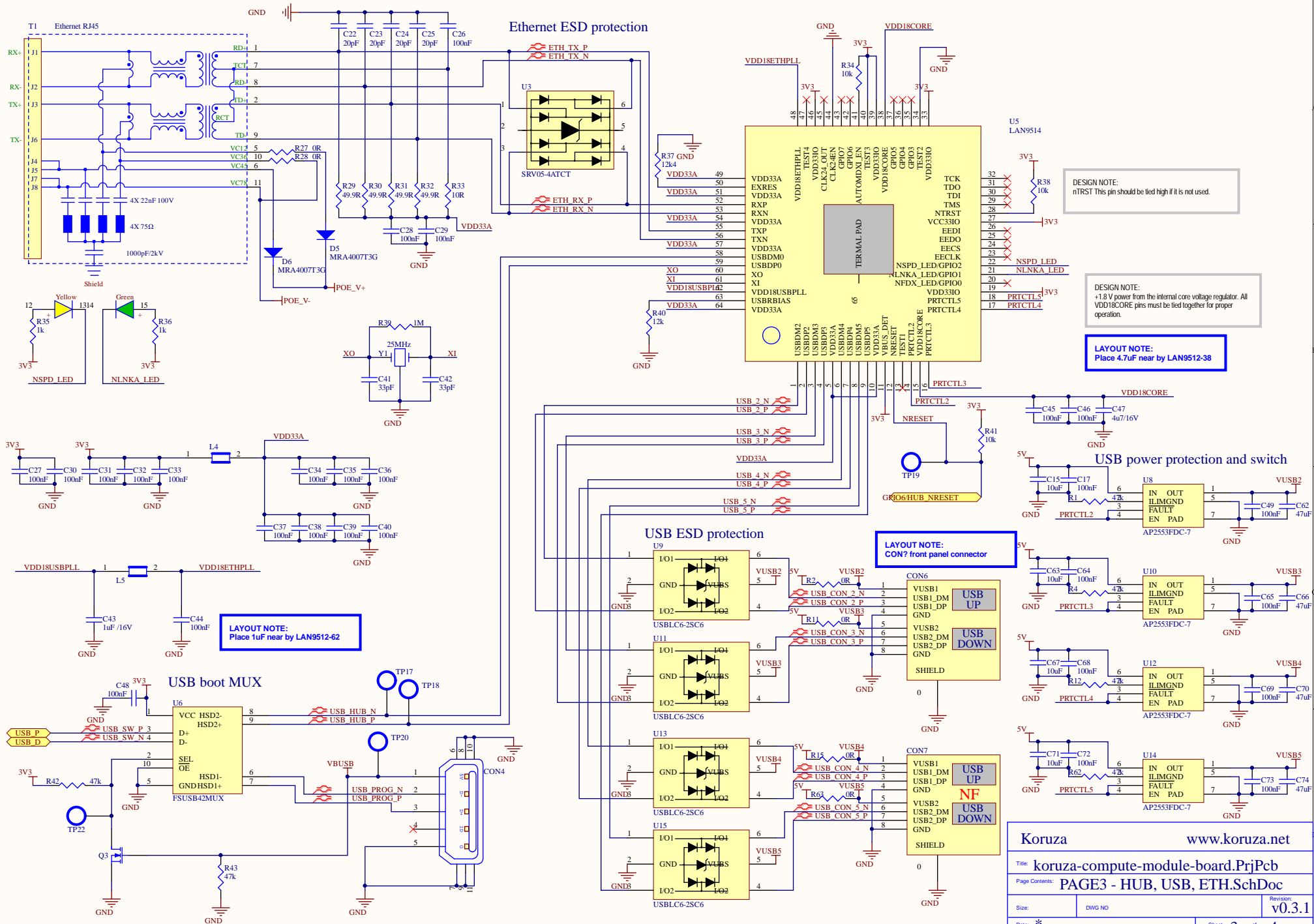
DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

Koruza		www.koruza.net	
Title:		koruza-compute-module-board.PrjPcb	
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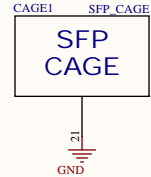
www.koruzanet



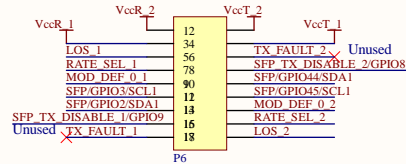


Rigid to Flex PCB connection

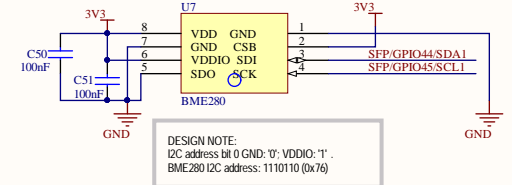
SFP Cage Rigid to flex connector



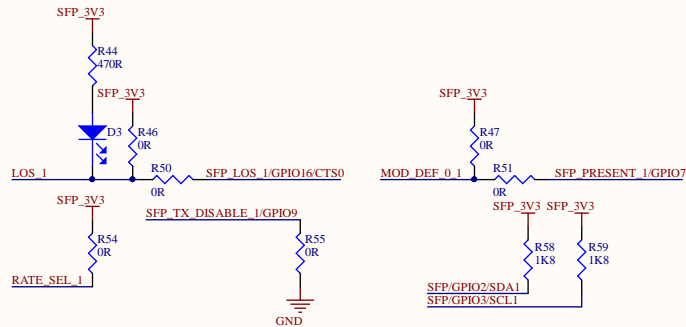
Koruza SFP rigid to flex GPIO connector



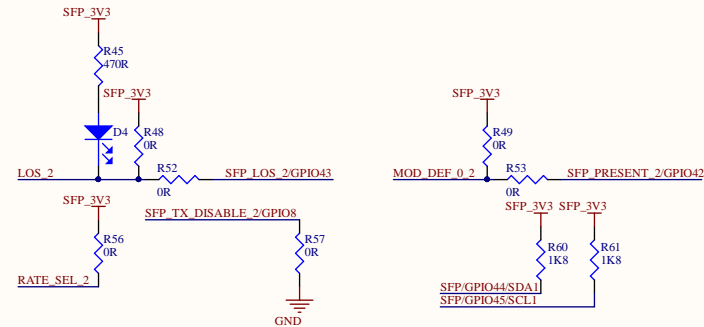
Environment sensor
Temperature, humidity, presure



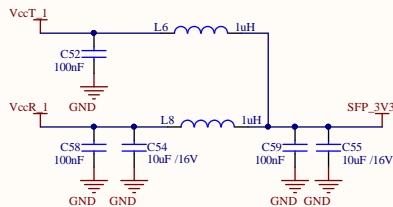
SFP1 GPIO config



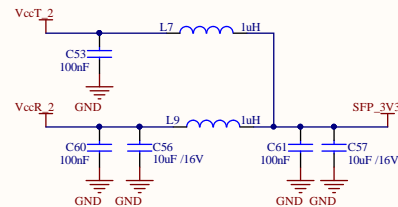
SFP2 GPIO config



SFP1 Power filtering network



SFP2 Power filtering network



DESIGN NOTE:
Note: Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin at 3.3V. When the recommended supply filtering circuit is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA, greater than the steady state value.

