

Koruza compute module board

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PAGE3 - HUB, USB, ETHERNET

REVISION HISTORY:

TODO:

- * RPi zero reader
- * GPIO headers
- * Koruza connectors
- * PoE power supplay - 24V to 9V
- * 5V power supplay - 9 V to 5V
- * JTAG?

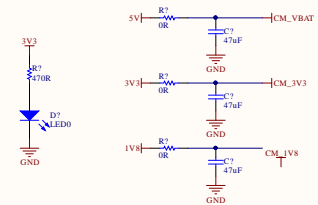
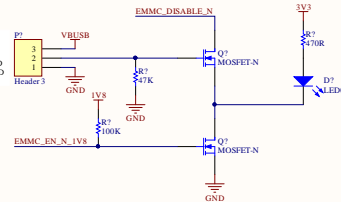
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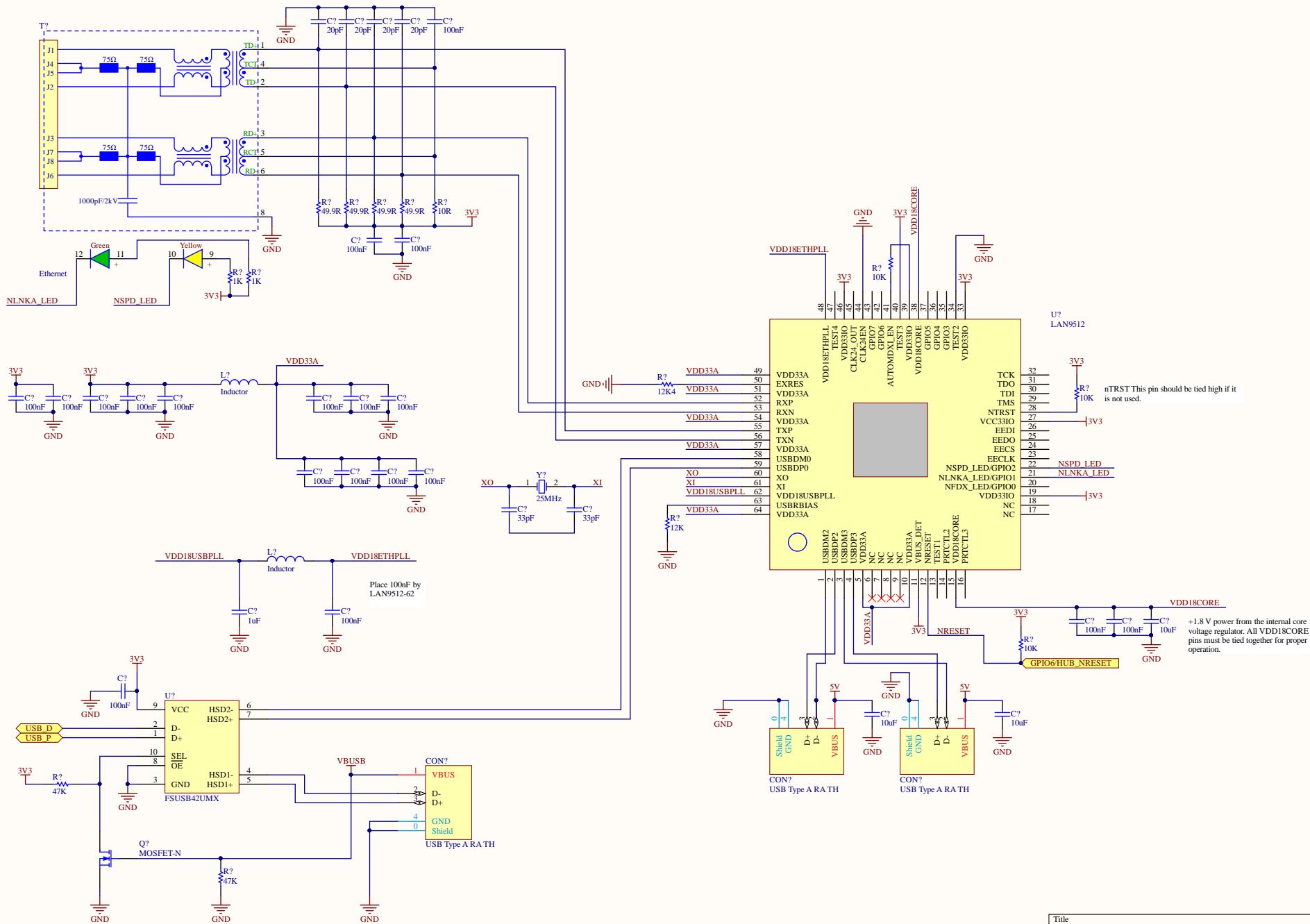
VDD_CORE used for module test only (do not use in normal operation, do not draw current from this pin!)



Jumper position:
3-2 = USB BOOT ENA
2-1 = USB BOOT DISA



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