



Koruza-CM

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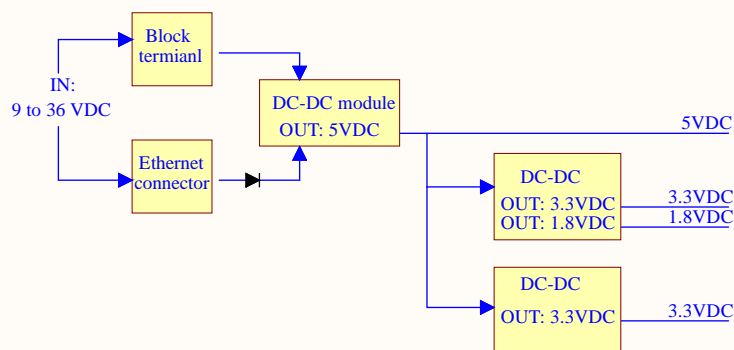
Version Revision:

v0.2 - 09.03.2017.

v0.3 - 30.03.2017.

v0.3.1 - 05.05.2017.

POWER DIAGRAM



DESIGN CONSIDERATIONS

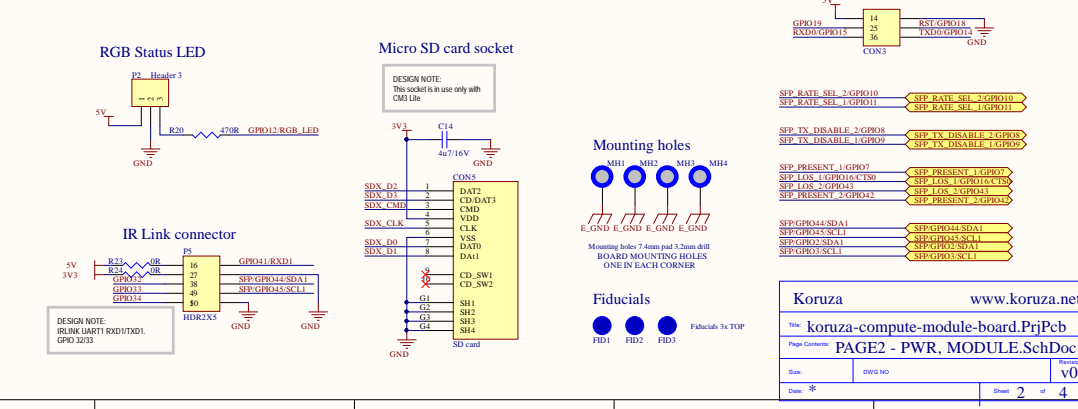
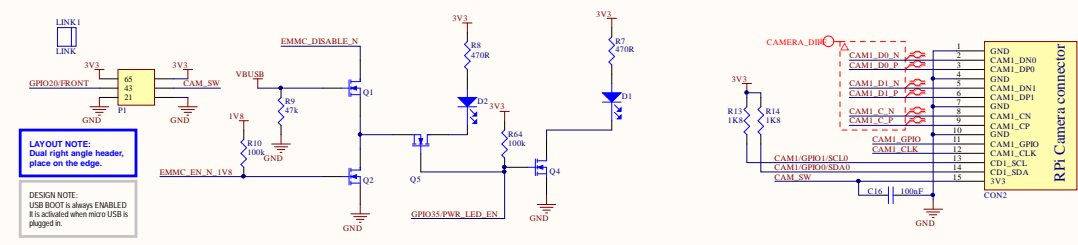
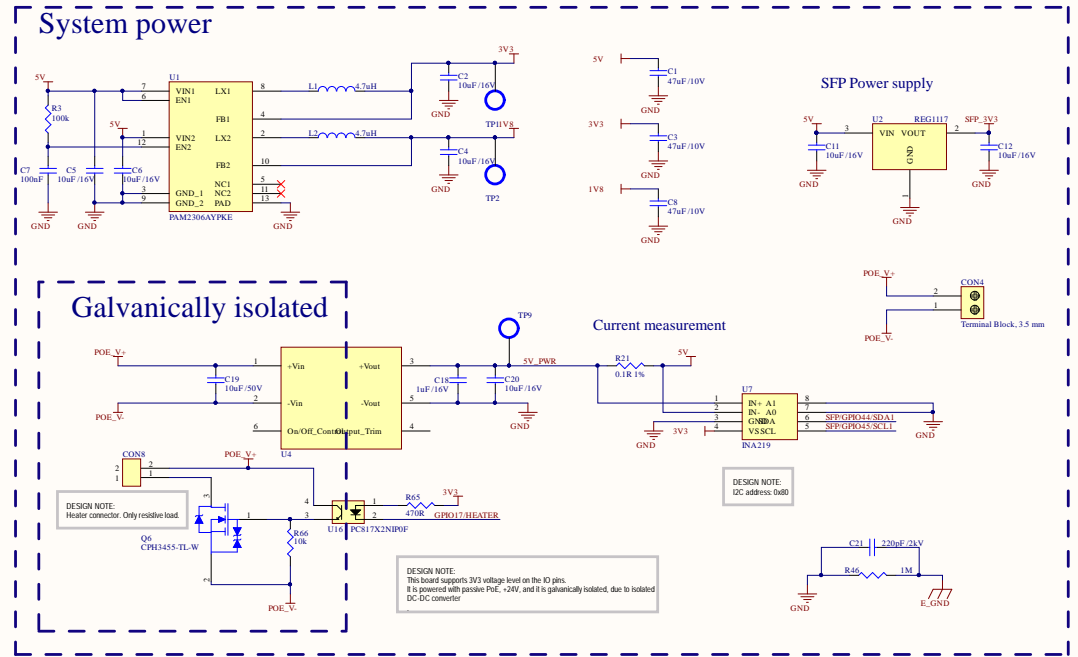
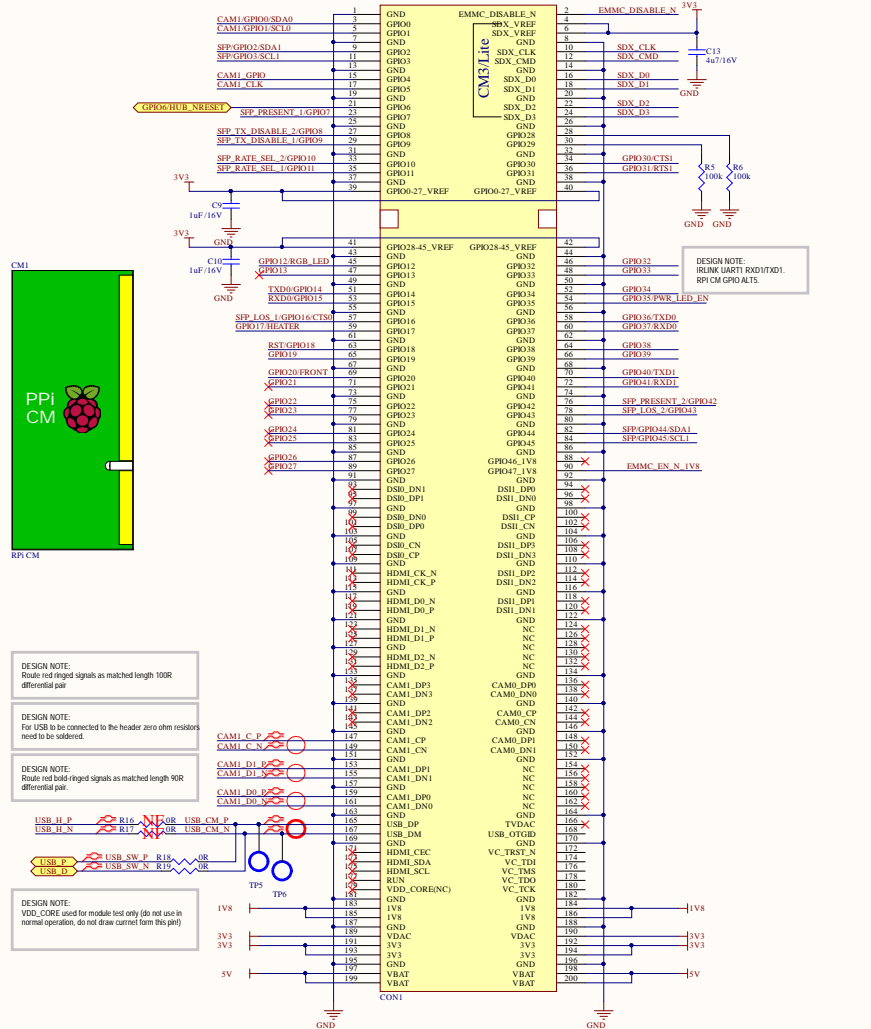
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

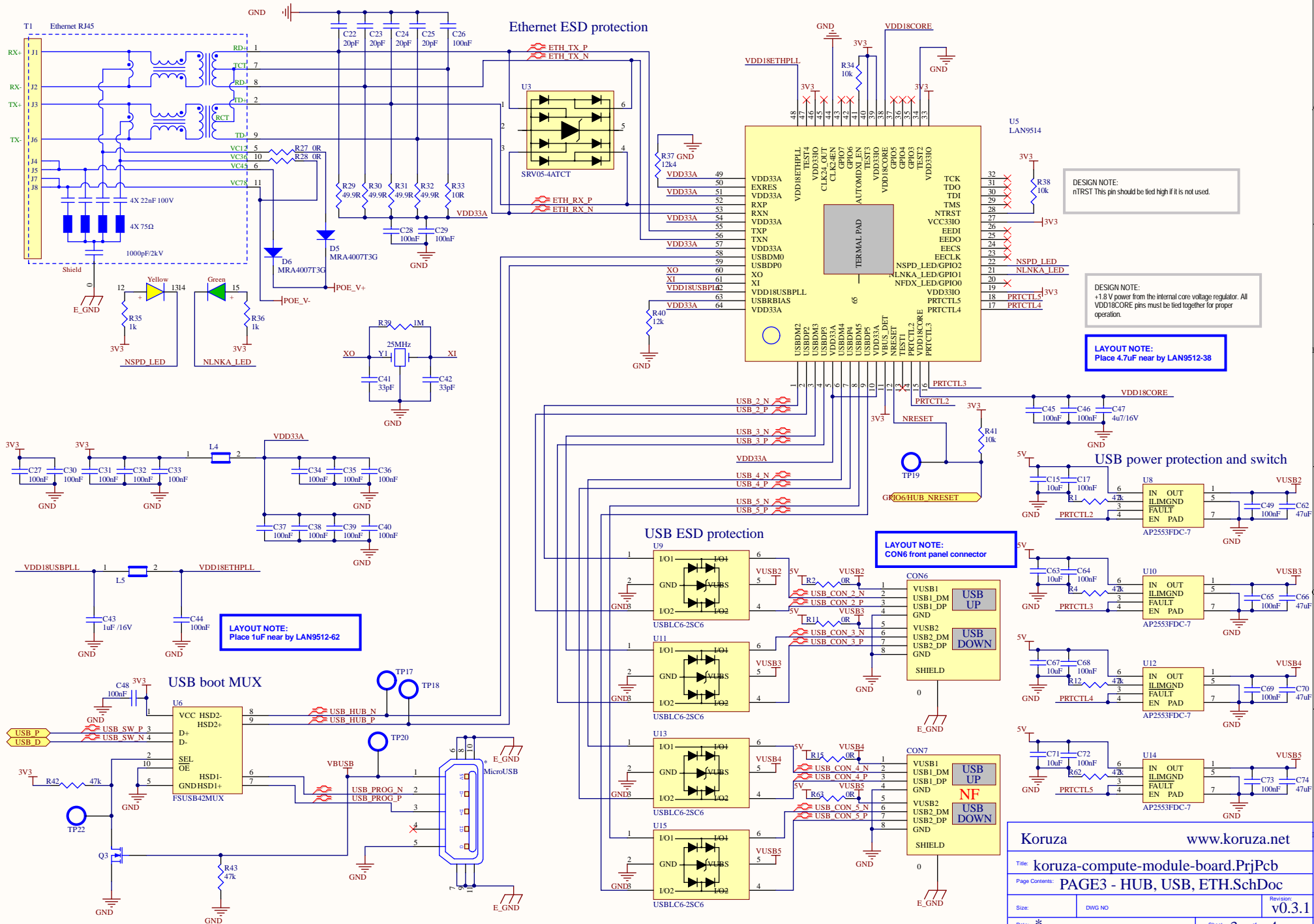
LAYOUT NOTE:
Example text for critical
layout guidelines.

Koruza		www.koruza.net	
Title: koruza-compute-module-board.PrjPcb			
Page Contents: PAGE1 - CONTENTS.SchDoc			
Size:	DWG NO		Revision: v0.3.1
Date: *	Sheet 1 of 4		

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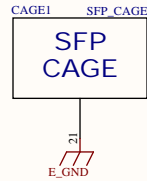


	Pull	ALT0	ALT1	ALT2	ALT3	ALT4	ALT5
GPIO30	Low	<reserved>	SA3	PCM_DIN	CT15		CT15
GPIO31	Low	<reserved>	SA2	PCM_DOUT	RTS0		RTS1
GPIO36	High	SPD_CE_N	S00	TXD0	<reserved>		
GPIO37	Low	SPD_MISO	S01	RXD0	<reserved>		
GPIO38	Low	SPD_MOSI	S02	RTS0	<reserved>		
GPIO39	Low	SPD_SCLK	S03	CTS0	<reserved>		
GPIO40	Low	PWM0	S04		<reserved>	SP12_MISO	TXD1
GPIO41	Low	PWM1	S05	<reserved>	<reserved>	SP12_MOSI	RXD1
GPIO44	-	GPIOCLK1	SDA0	SDA1	<reserved>	SP12_CE1_N	
GPIO45	-	PWM1	S06	SCL1	<reserved>	SP12_CE2_N	

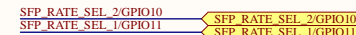
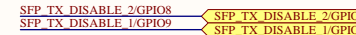
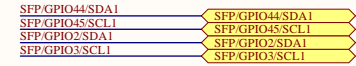
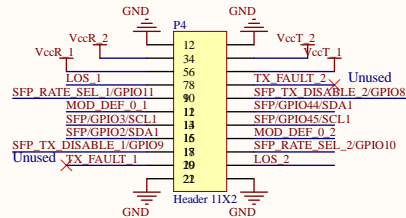


Rigid to Flex PCB connection

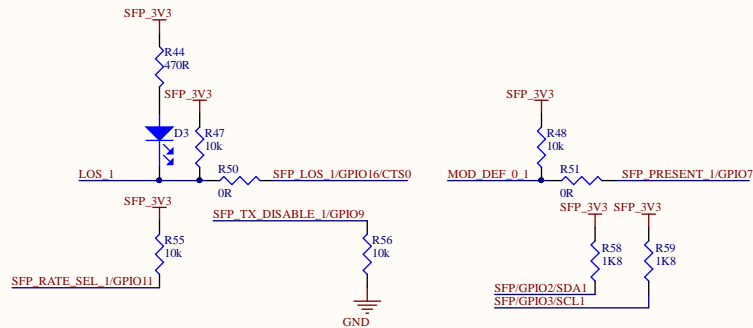
SFP Cage Rigid to flex connector



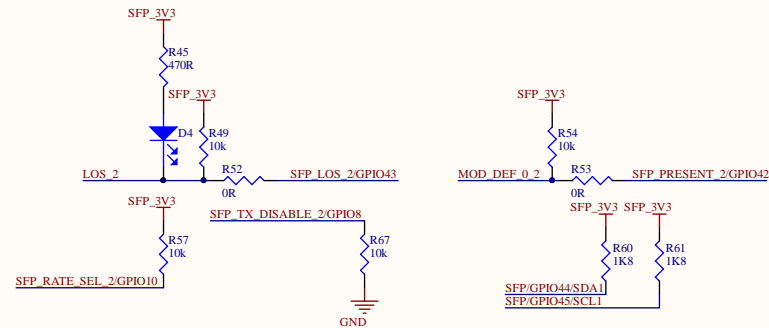
Koruza SFP rigid to flex GPIO connector



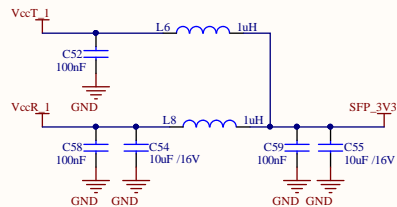
SFP1 GPIO config



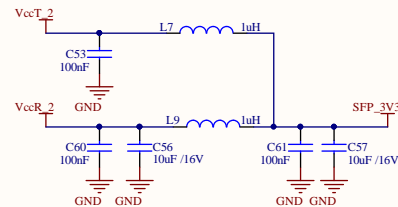
SFP2 GPIO config



SFP1 Power filtering network



SFP2 Power filtering network



DESIGN NOTE:
Note: Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin at 3.3V. When the recommended supply filtering circuit is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value.

