

野火_STM32F103ZET6核心板_原理图_V1.0

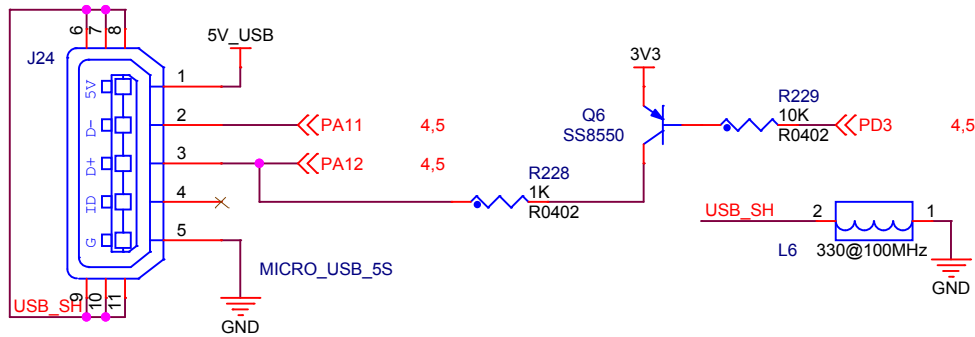
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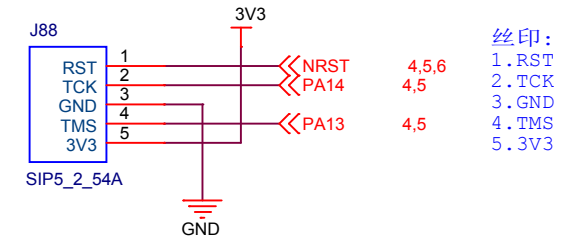
历史版本

版本号	日期	设计	描述
V1.0	2019-05-06	cancore	初始版本

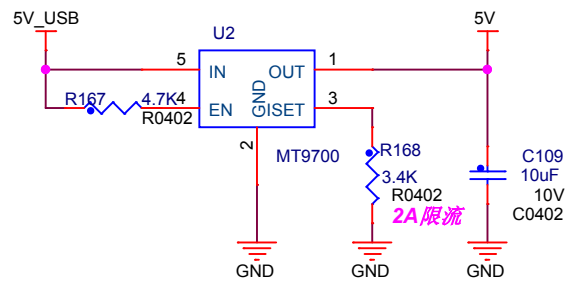
USB Device



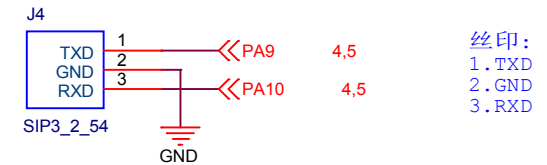
SWD接口



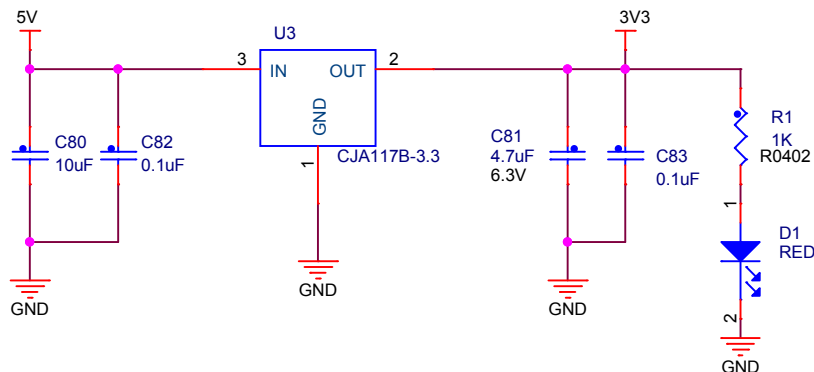
5V输出过流保护



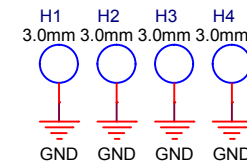
调试串口



LDO 3.3V



定位孔

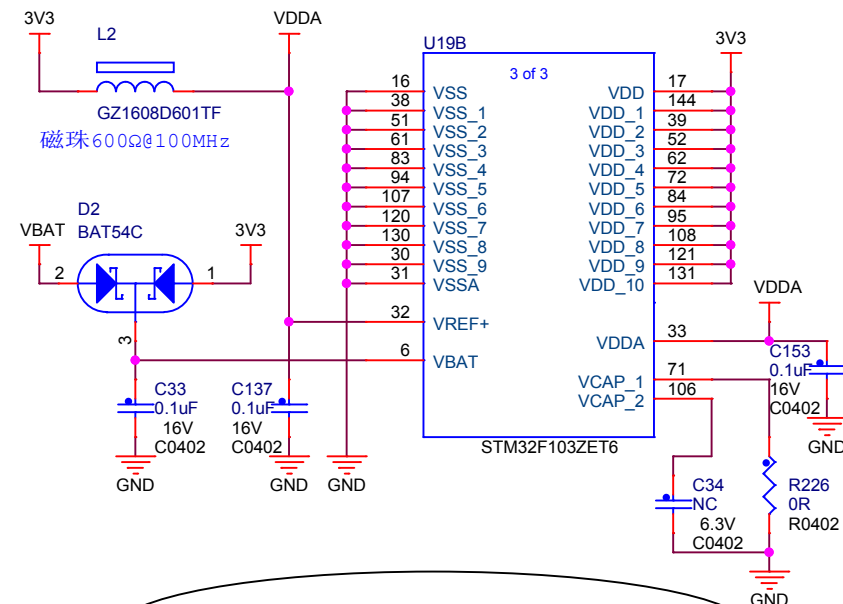
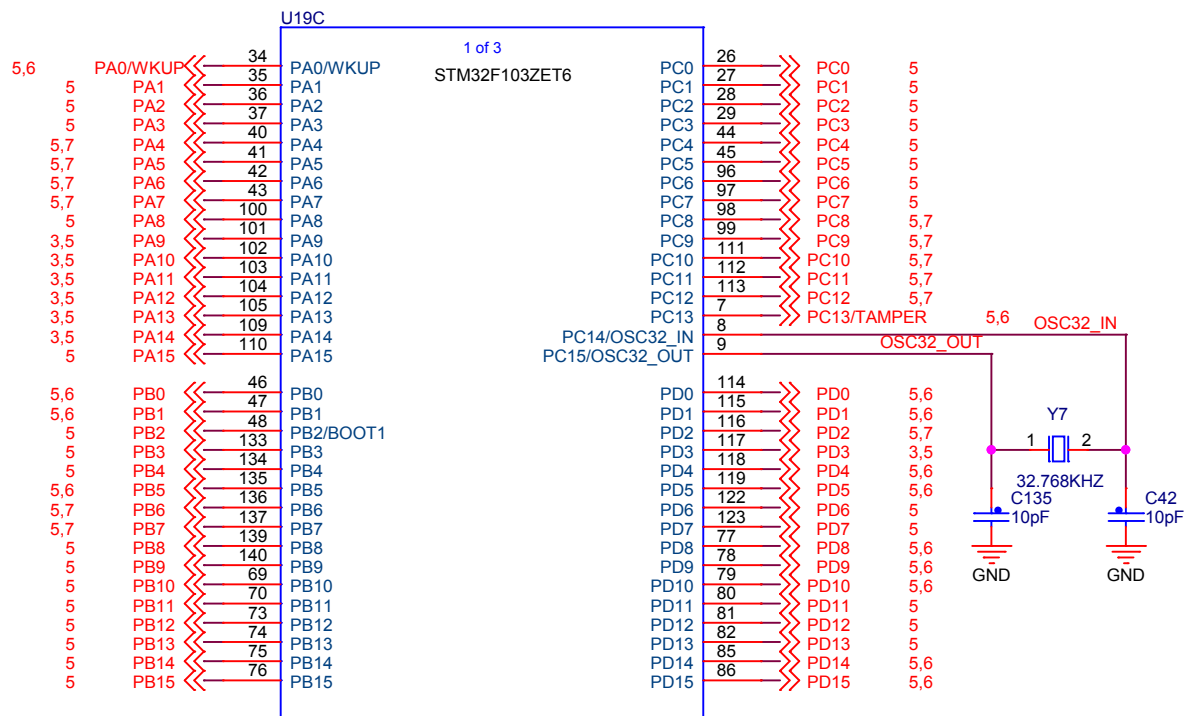


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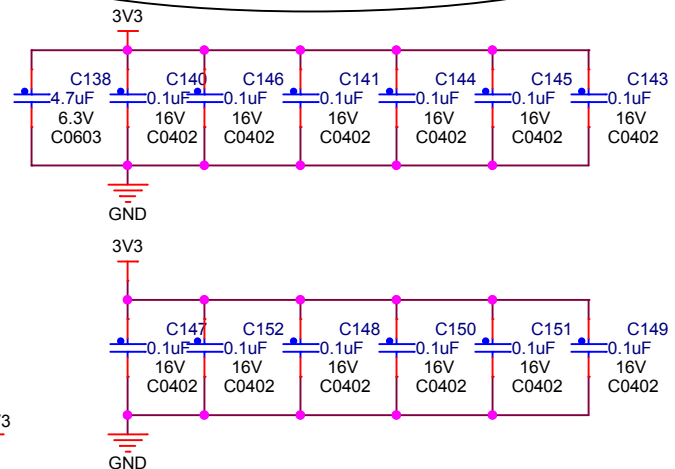
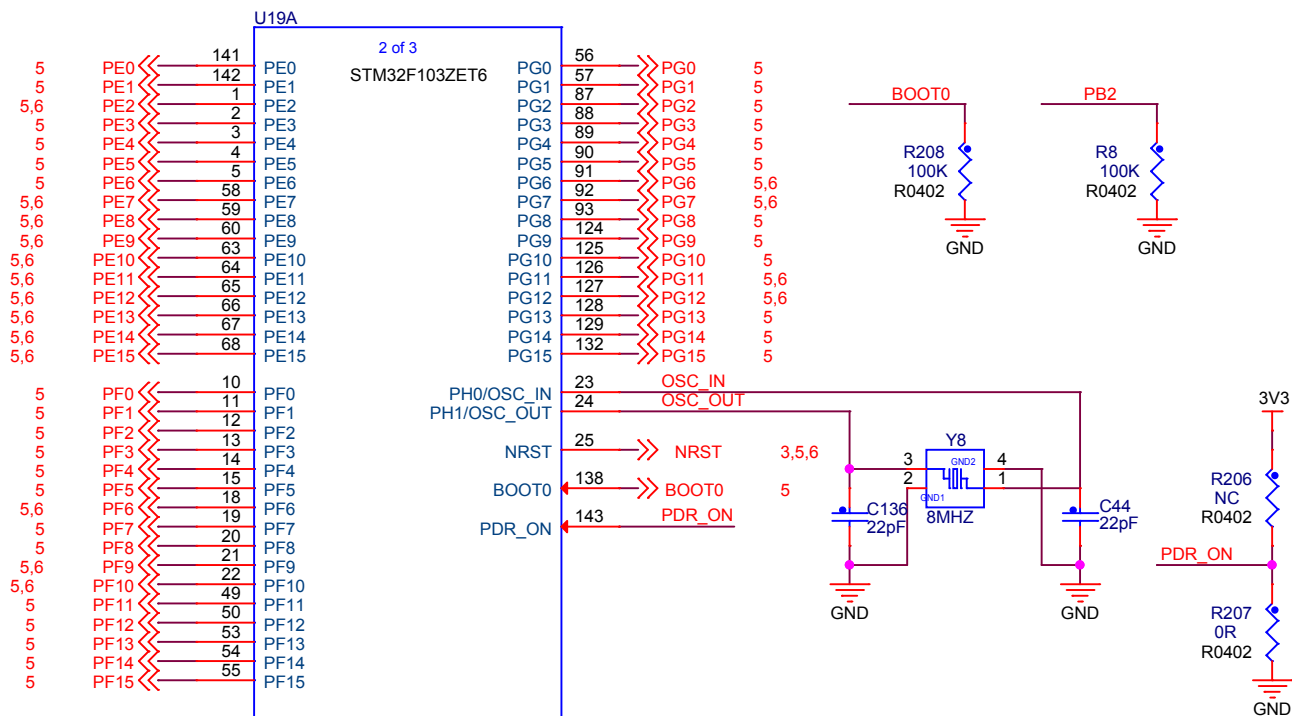
Title
野火_STM32F103ZET6核心板_原理图

Size A4	Document Number 电源供电	Rev V1.0
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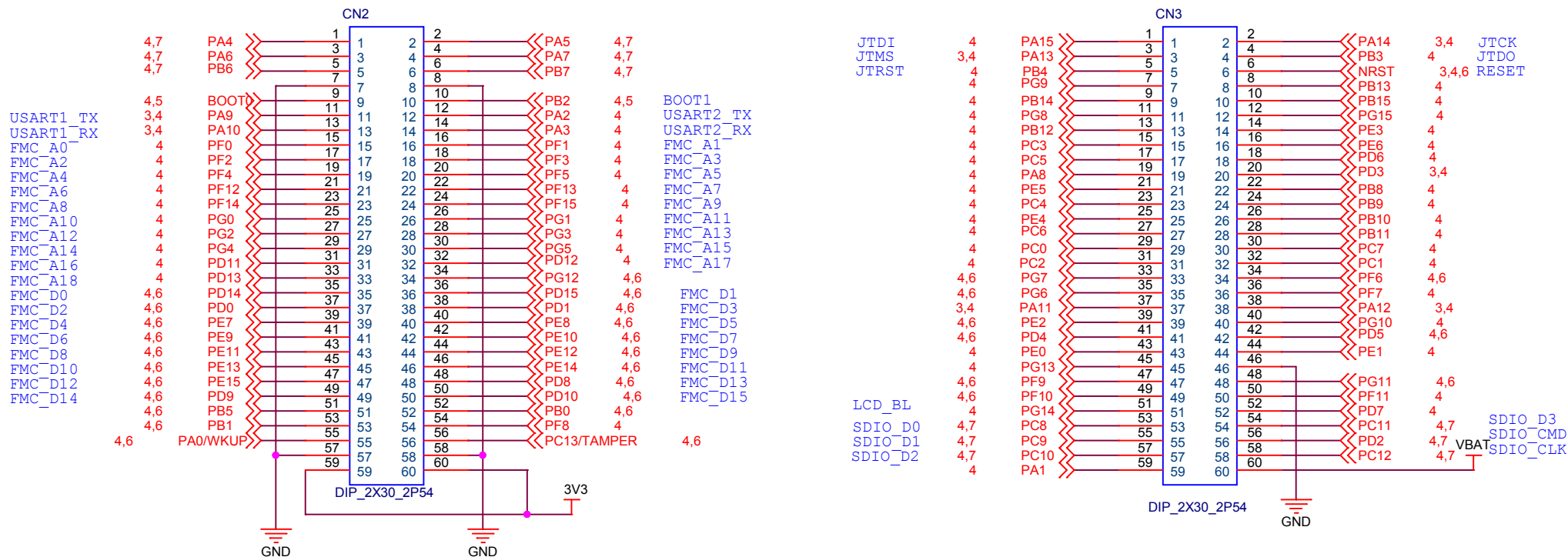


所有退耦电容应尽量靠近MCU电源引脚放置

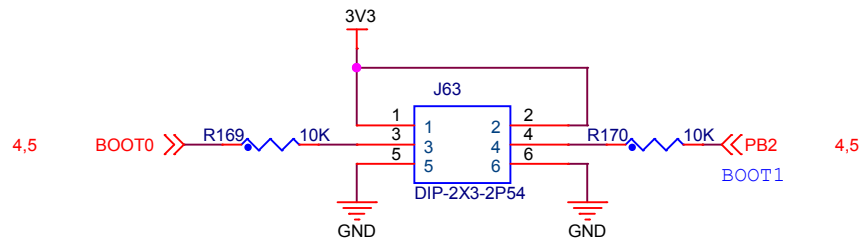


引出IO口

引出IO 111个，包含 5个JTAG IO



设置启动模式

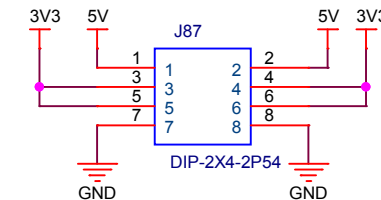


F103 BOOT设置

BOOT0	BOOT1	启动方式
0	X	用户闪存
1	0	系统存储器/ISP
1	1	内嵌SRAM

默认配置是用户闪存（即内部Flash）

引出电源



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Title

野火_STM32F103ZET6核心板_原理图

Size
A4

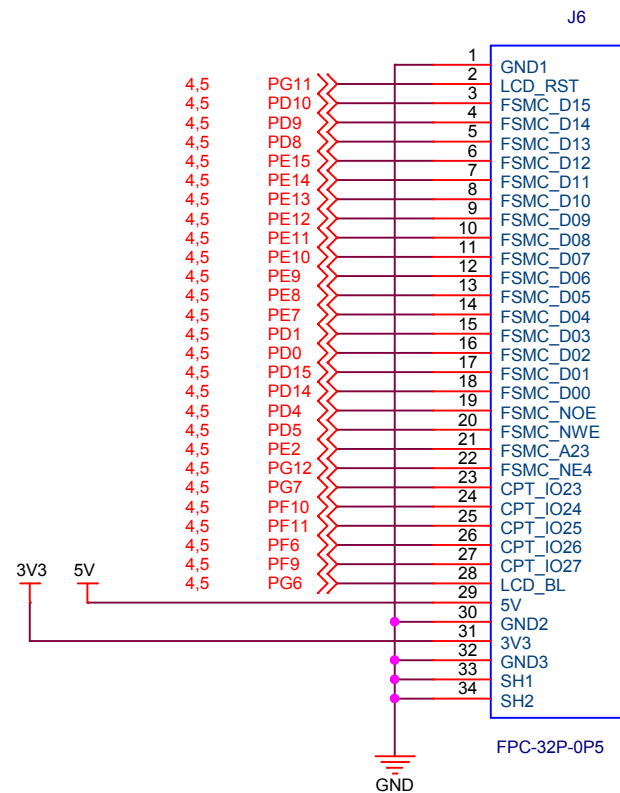
Document Number
引出IO/BOOT

Rev
V1.0

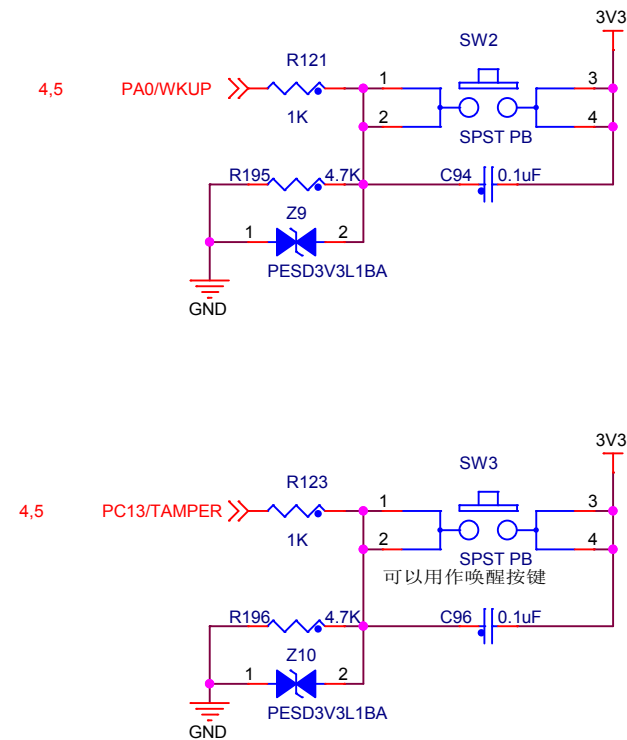
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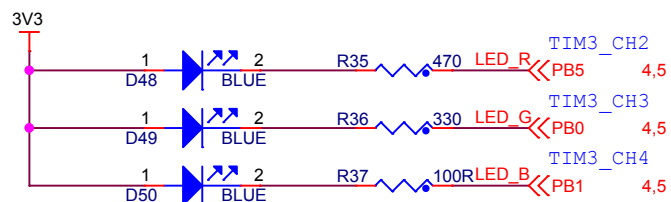
液晶接口-FSMC-8080模式



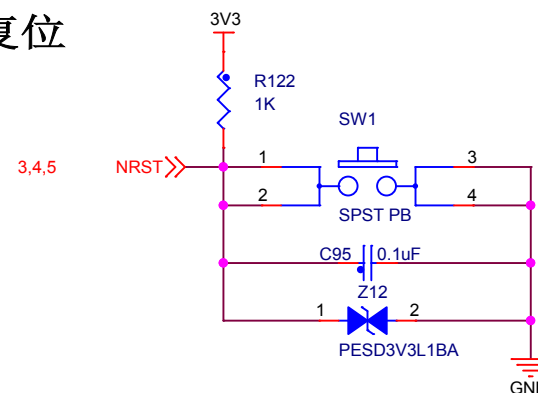
按键



LED



复位



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Size A4	Document Number LCD/CSI	Rev V1.0
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The diagram shows the W25Q64 memory chip (U24) connected to the microcontroller's PA4 and PA5 pins. The chip's VCC (pin 8) is connected to the 3V3 supply, and its GND (pin 4) is connected to ground. A 10K resistor (R223) is connected between the chip's CE (pin 1) and the 3V3 supply. A 0.1uF capacitor (C158) is connected between the chip's VCC (pin 8) and ground. The chip's SCK (pin 6) is connected to the microcontroller's PA5 pin. The chip's SI_IO0 (pin 5), SO_IO1 (pin 2), WP_IO2 (pin 3), and HOLD/RESET_IO3 (pin 7) pins are connected to the microcontroller's PA7 and PA6 pins.

The schematic diagram illustrates the electrical connections for the TF card slot. A 3V3 power supply is connected to a series of 10K resistors (R183, R184, R185, R186, R187) which are connected to the PC pins (PC10, PC11, PD2, PC12, PC8, PC9). The TF card slot (J28) is connected to the microcontroller pins (DAT2, CD/DAT3, CMD, VDD, CLK, VSS, DAT0, DAT1). The card's pins (9-15) are connected to the microcontroller's pins (9-15).