

野火_F767挑战者_核心板_原理图_V2.1

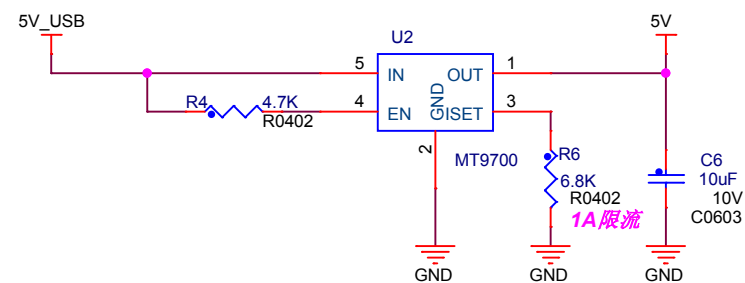
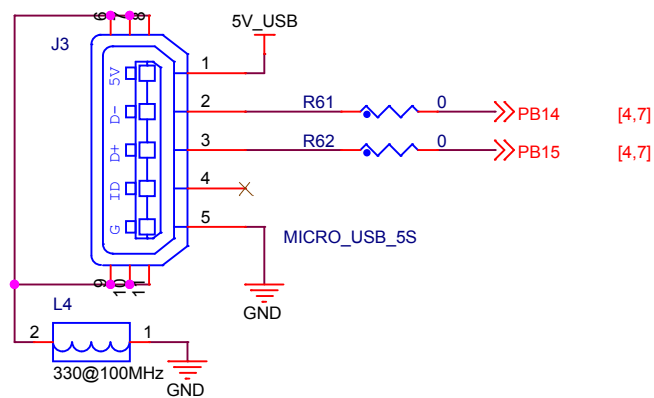
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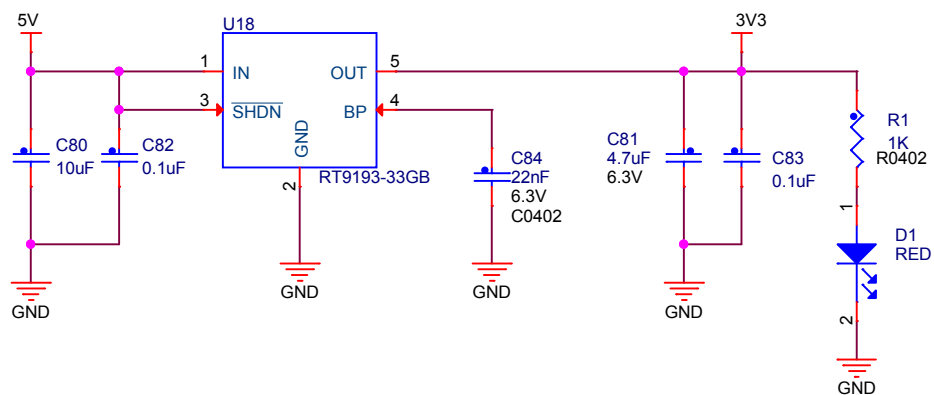
历史版本

版本号	日期	设计	描述
V2.0	2019-03-12	cancore	初始版本
V2.1	2019-04-20	cancore	SDRAM的片选增加上拉

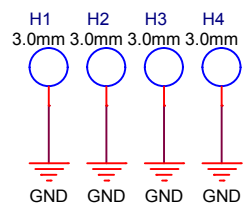
5v供电过流保护



5V转3.3V

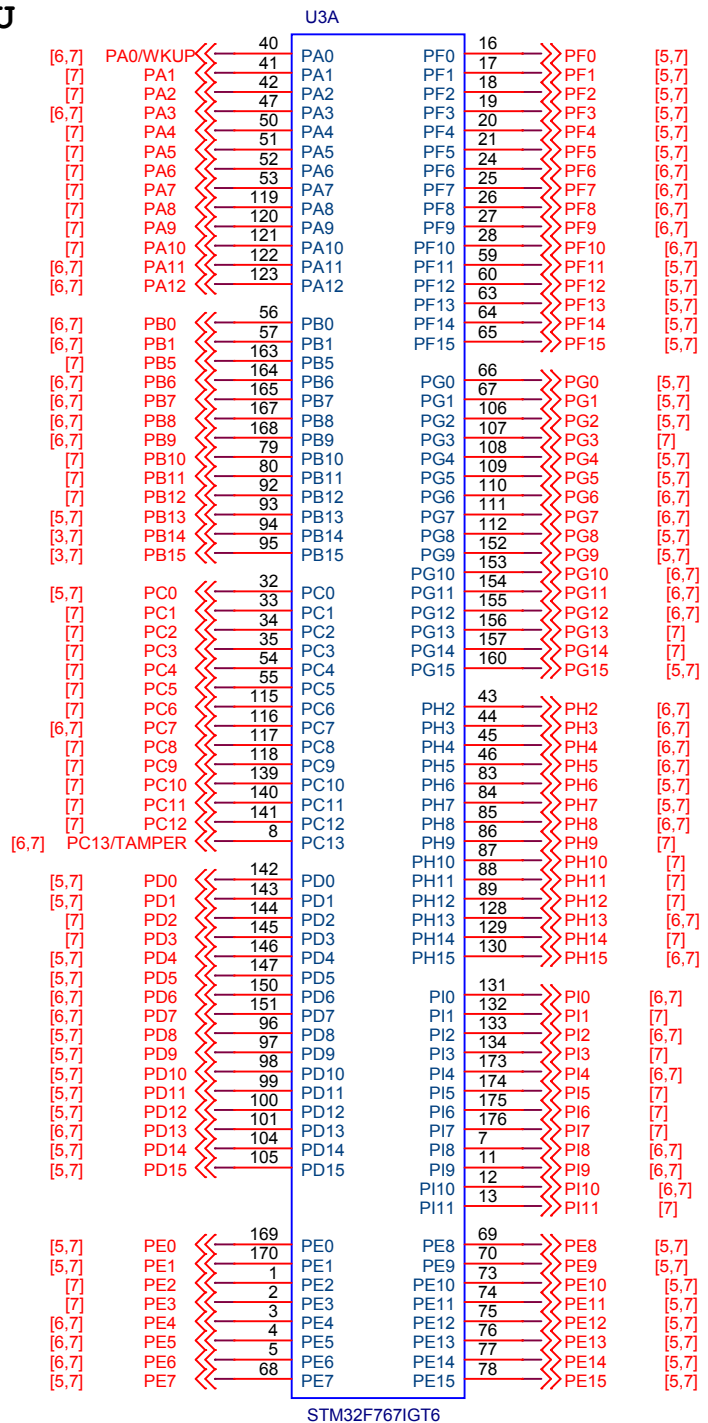


定位孔

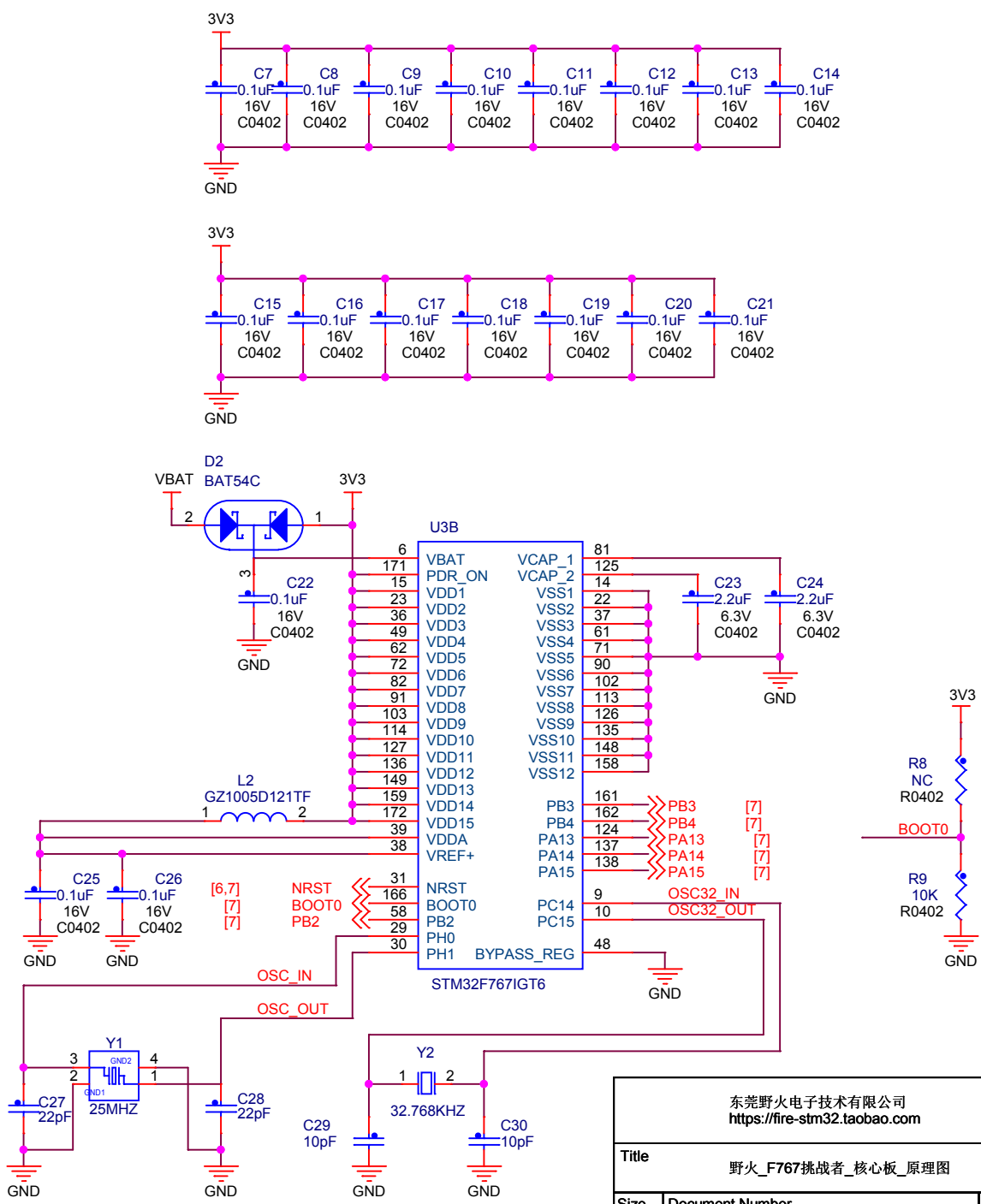


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MCU

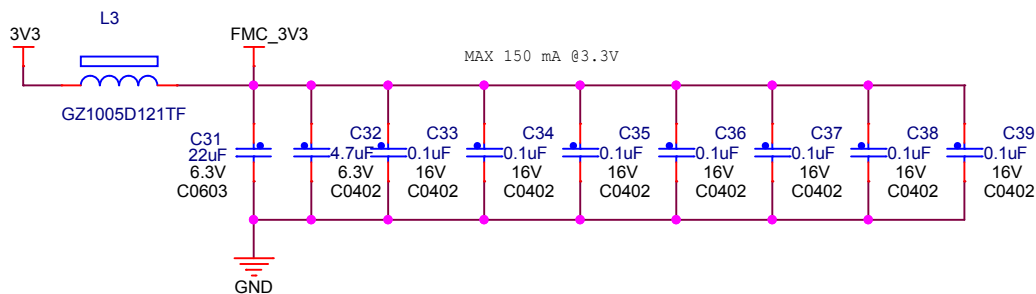
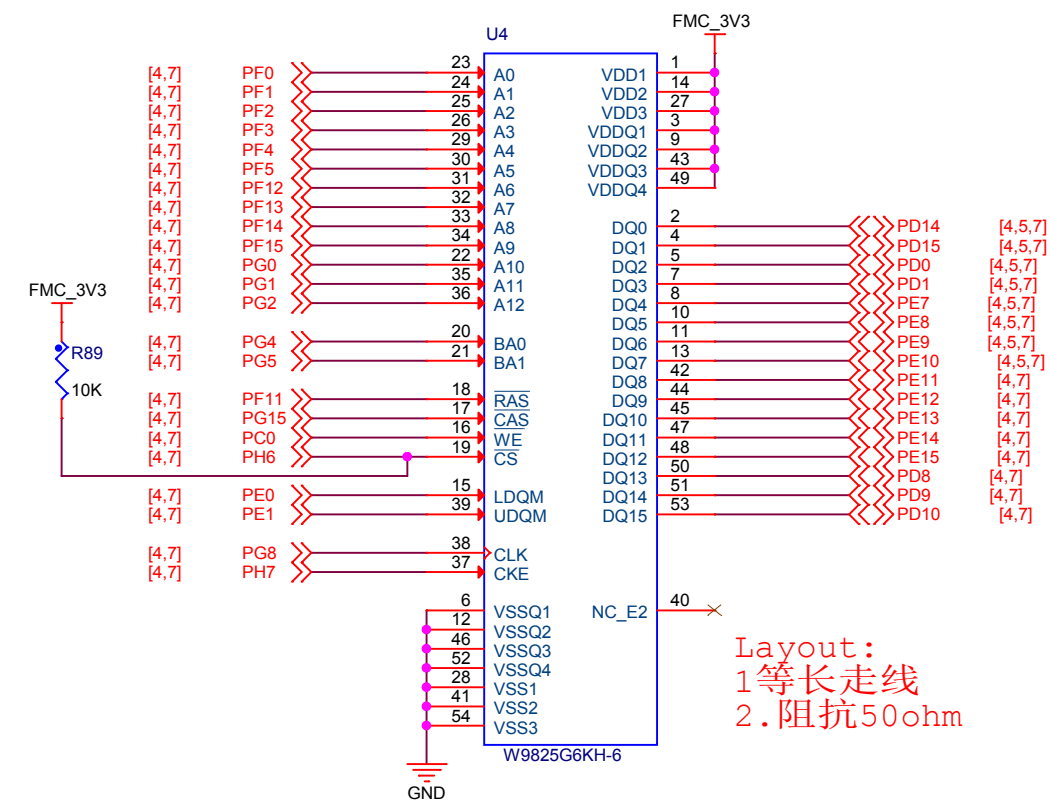


STM32F767IGT6

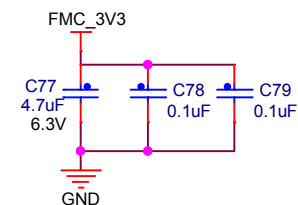
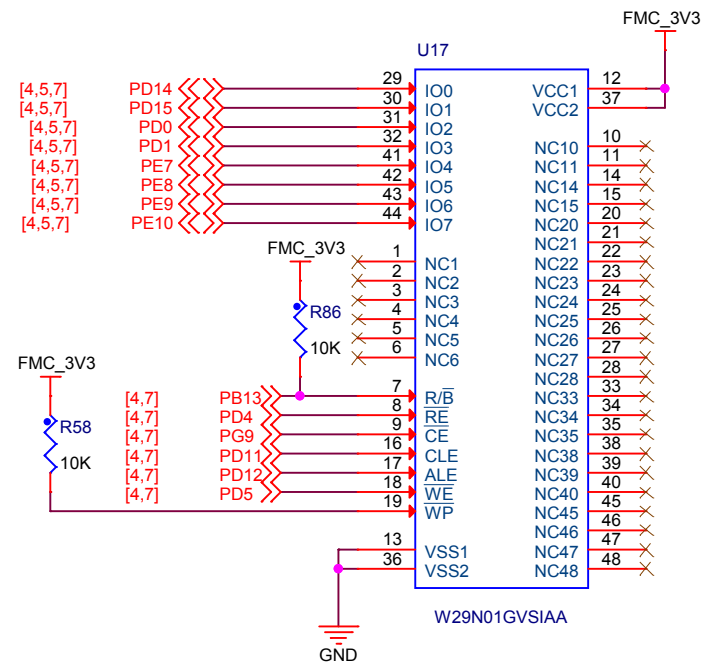


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SDRAM



NAND FLASH



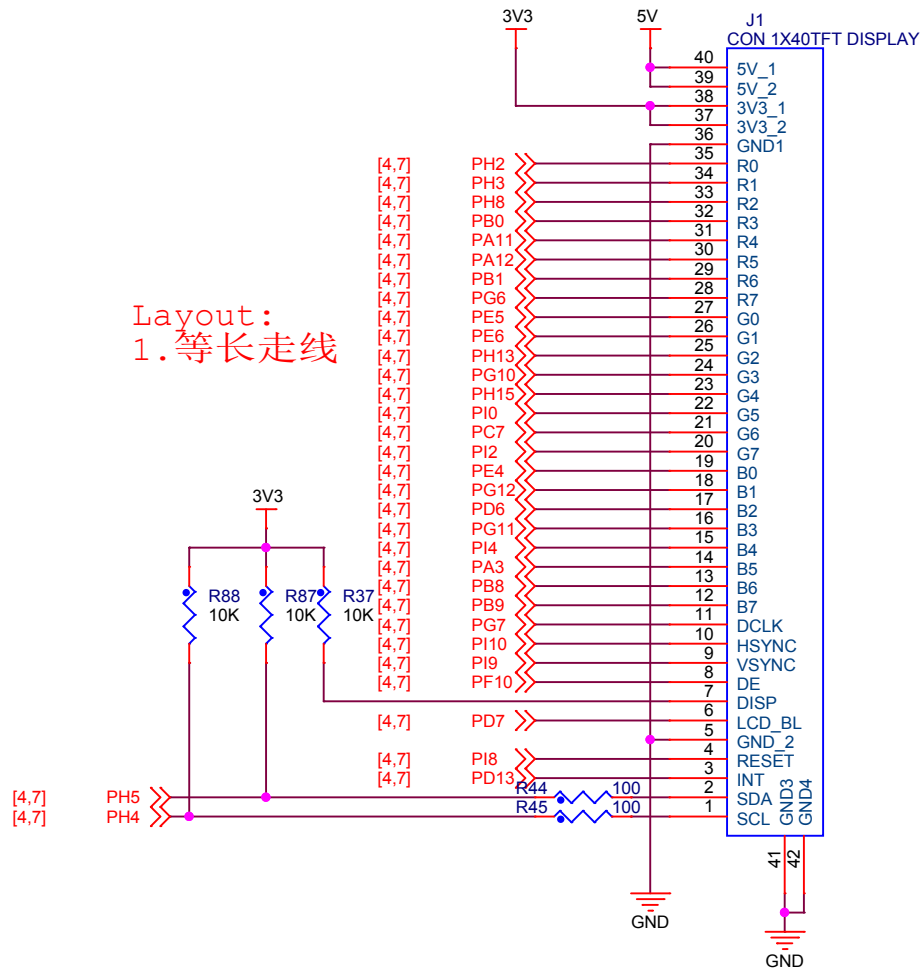
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<https://fire-stm32.taobao.com>

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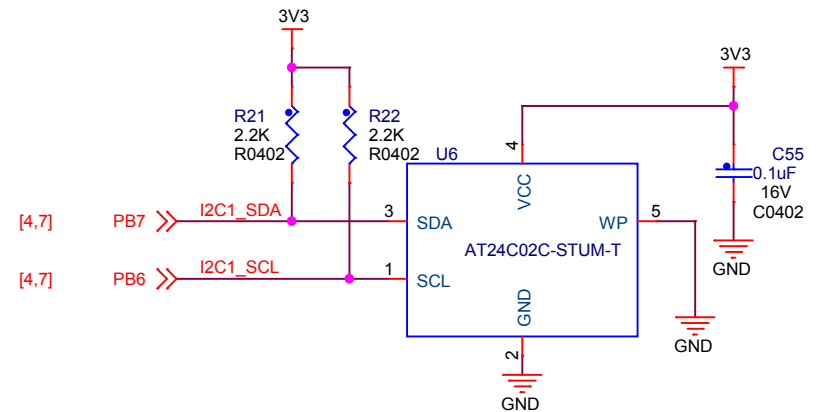
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LCD RGB888

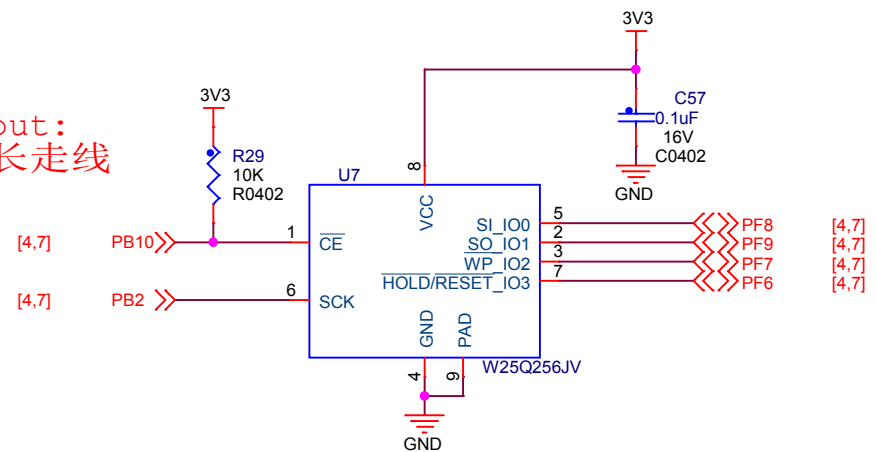


EEPROM

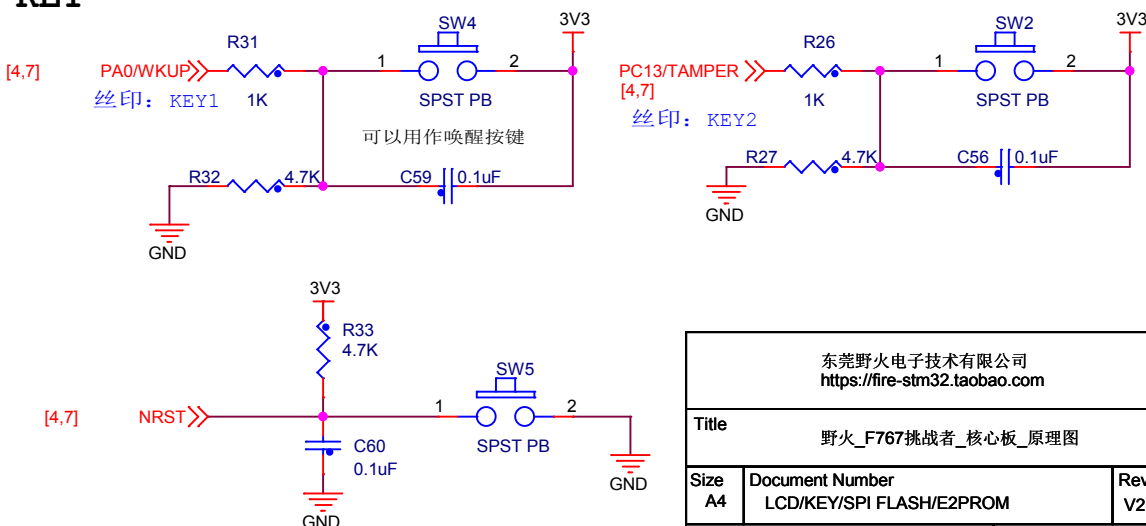


SPI FLASH

Layout: 1 等长走线

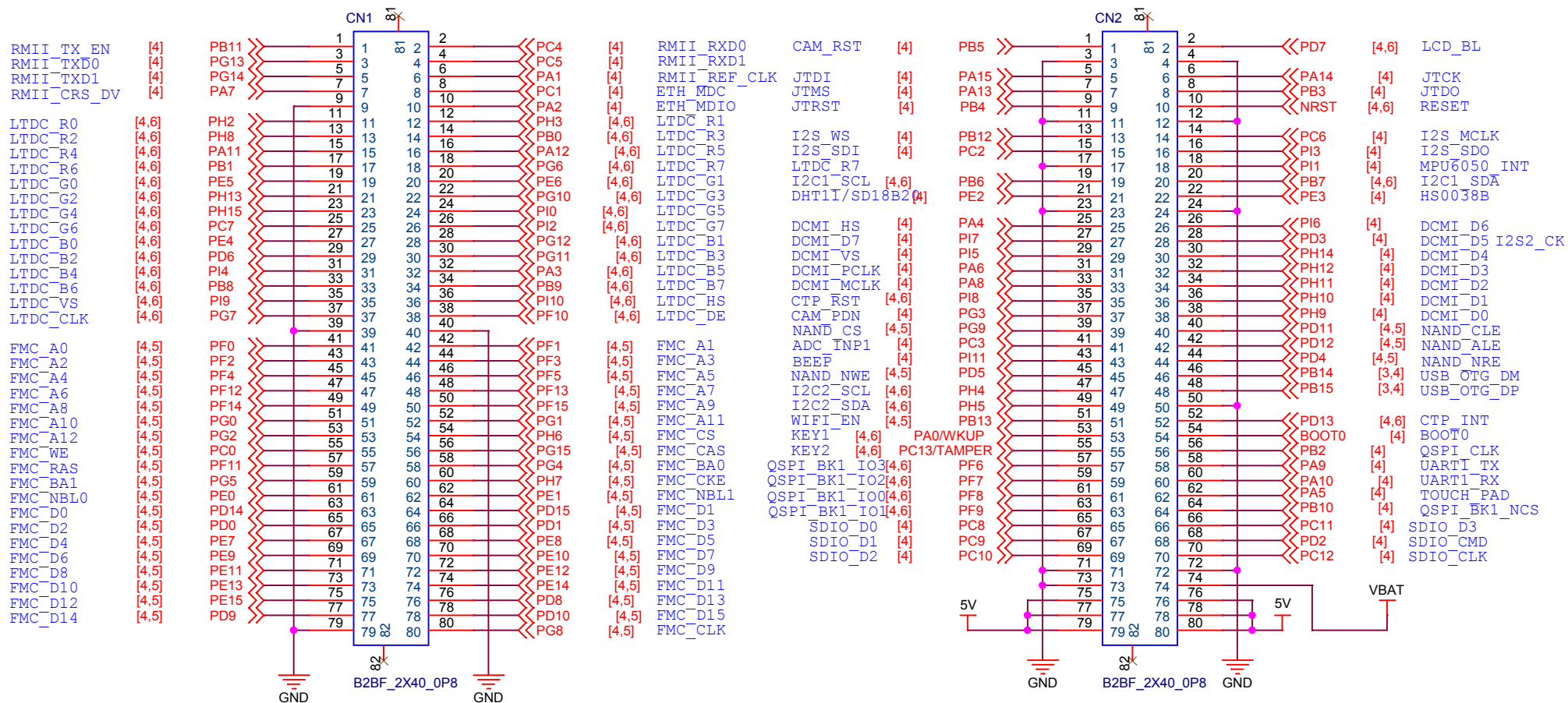


KEY

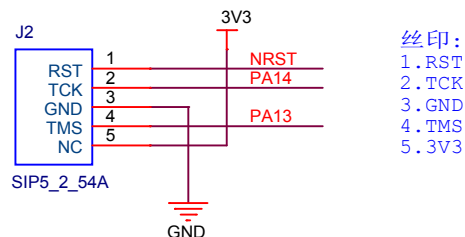


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连接器



JTAG



串口

