FRDM-MCXA153

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REVISION HISTORY

REVISION	DESCRIPTION OF CHANGE	DATE	APPROVED
X1	Draft	21-Mar-2023	Jonson Chen
A	Pilot	8-Jun-2023	Jonson Chen
A1	Production	28-Aug-2023	Jonson Chen

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ICAP Classification: CP: IUO: X PUBI:

Designer: Jonson Chen	Drawing Title: FRDM-MCXA153	
Drawn by: Leilei Du	Page Title: COVER	
Approved: Jonson Chen	Size C / Document Number SCH-90829, PDF: SPF-90829	Rev A1
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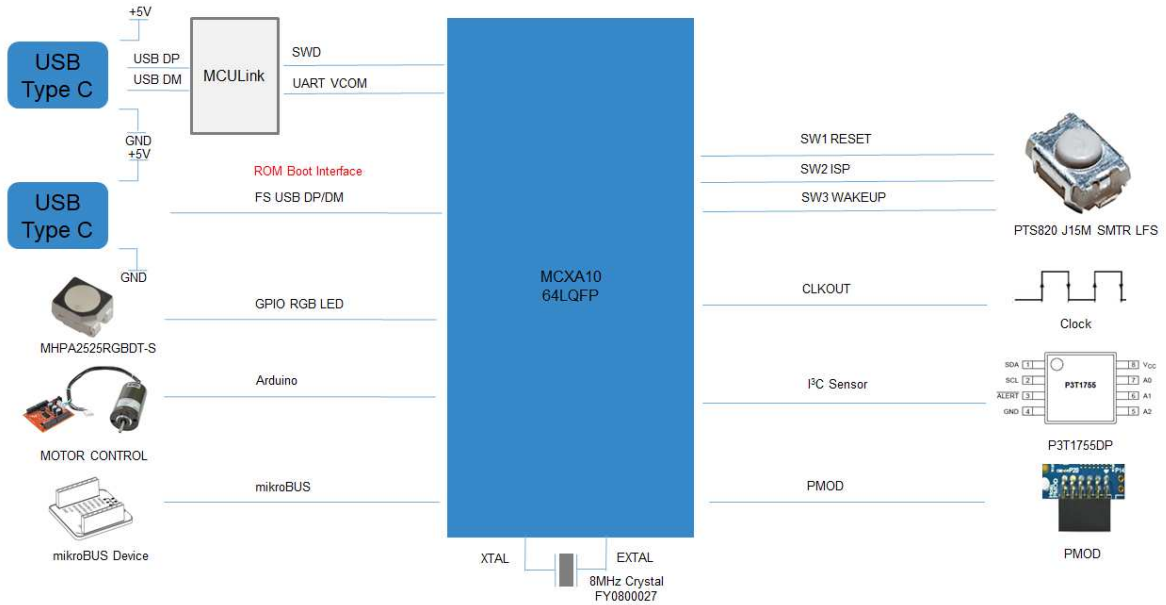
FRDM-MCXA153 BLOCK DIAGRAM



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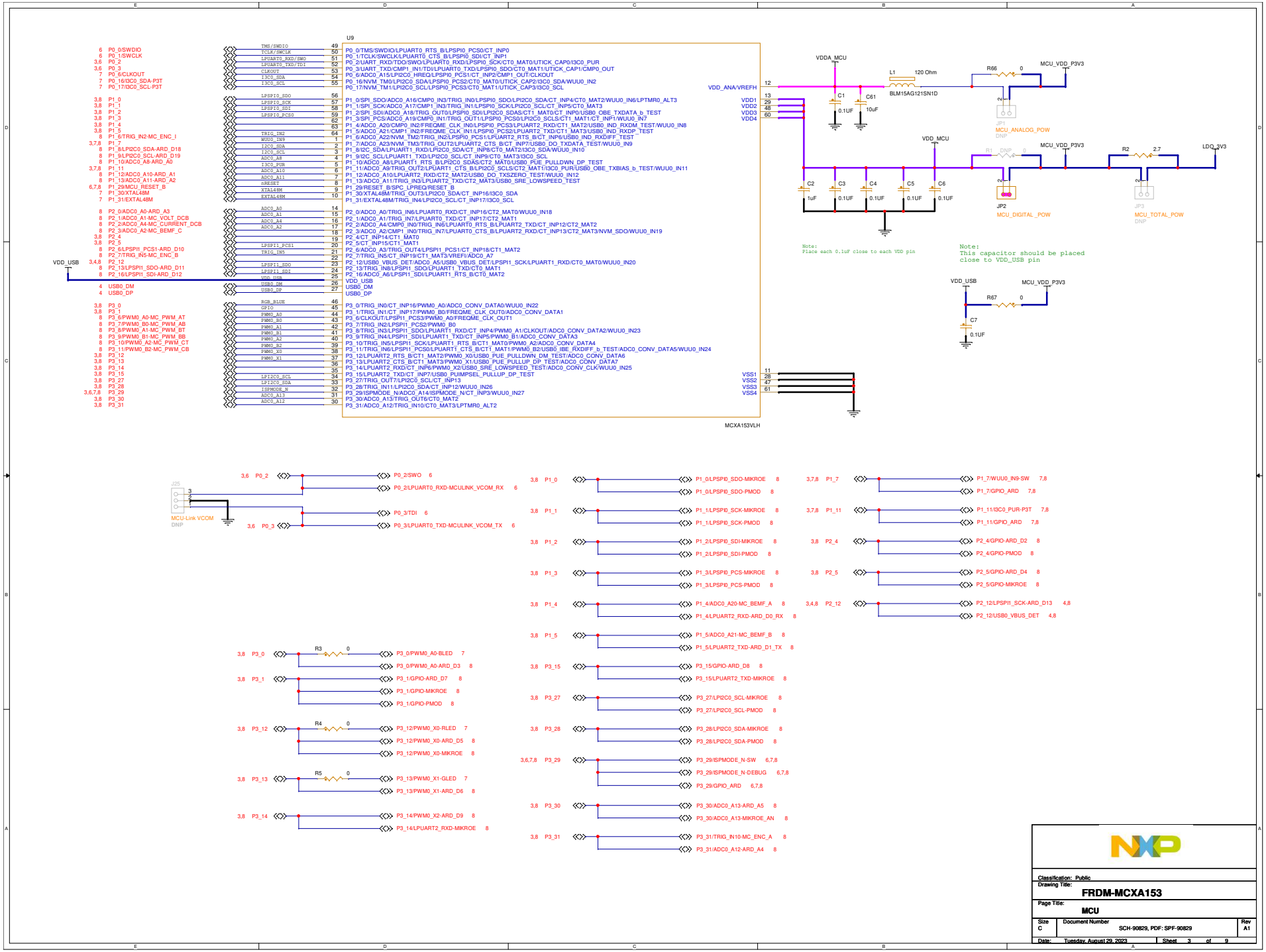

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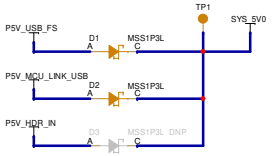
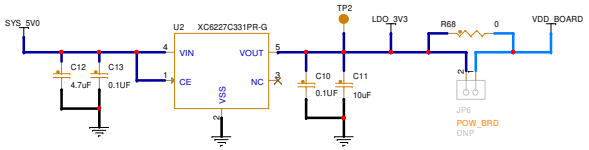
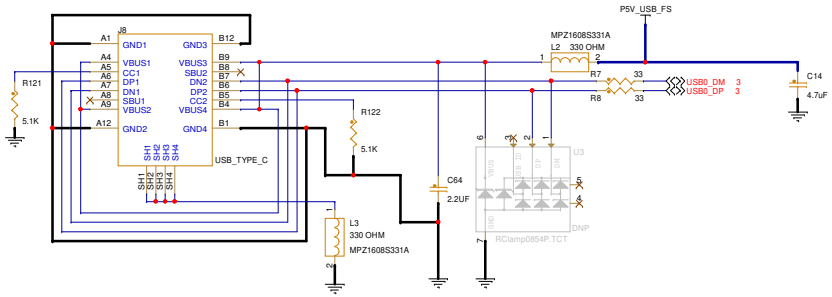
POWER

SYS_5V0

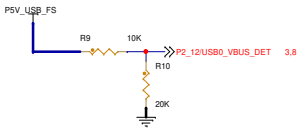




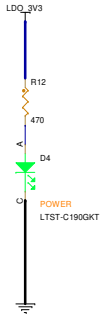
USB FULL SPEED
USB Type C



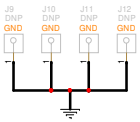
Note:
90Ohms differential impedance
Differential signal routing



POWER LED

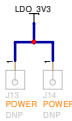


GND Test Points



Note:
Four ground test points are placed
on the four sides of the board to
facilitate testing

Power Supply Test Points



Note:
Two power supply test points are placed on
the left and right sides of the board to facilitate
testing or provide power for other boards

Mounting Holes

Note:
The mechanical mounting holes are
placed on the four vertices of the board

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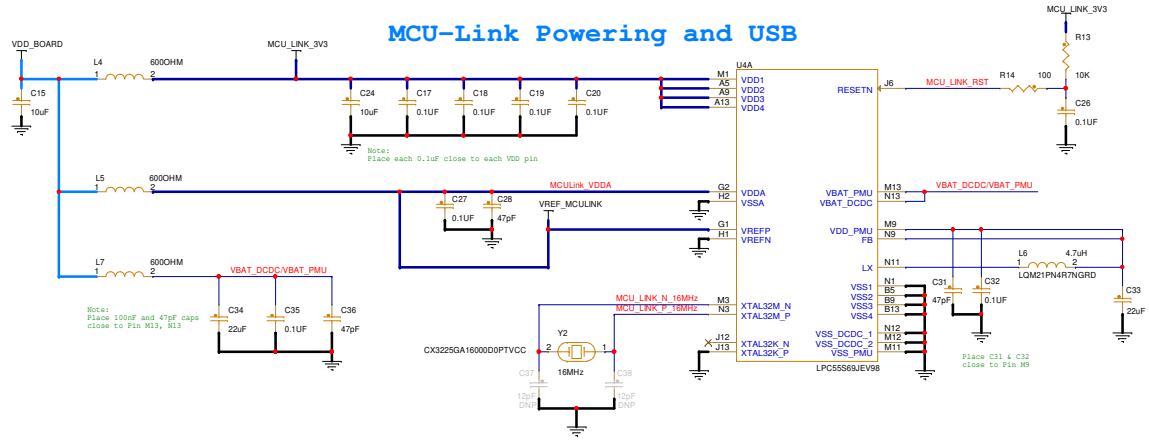
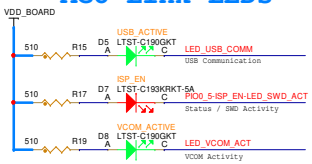
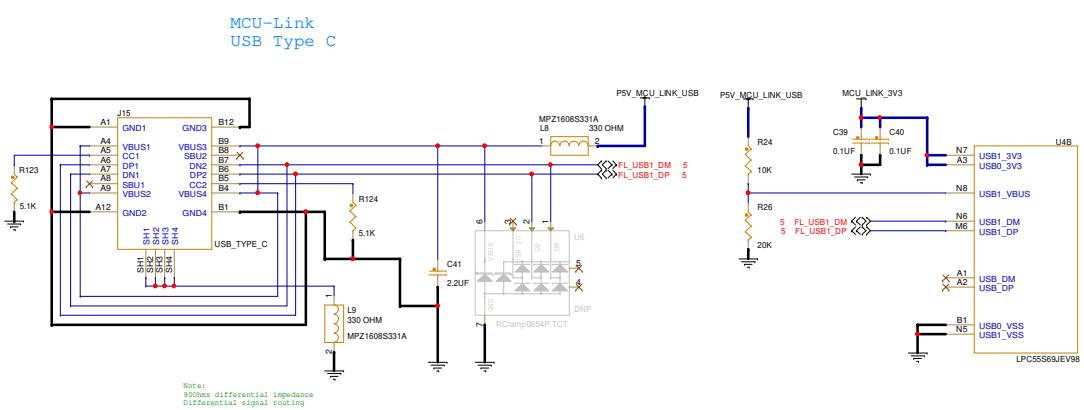
MCU-Link Powering and USB

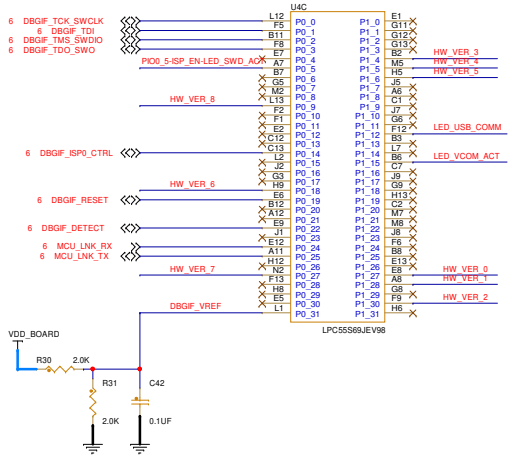


MCU-Link LEDs

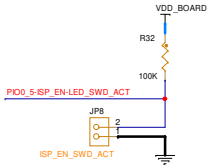


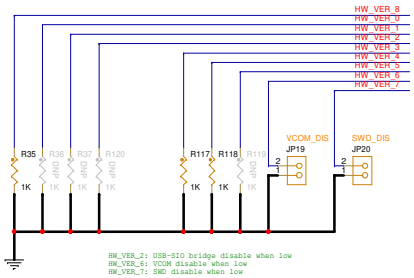
MCU-Link USB Type C





MCU-Link Mode





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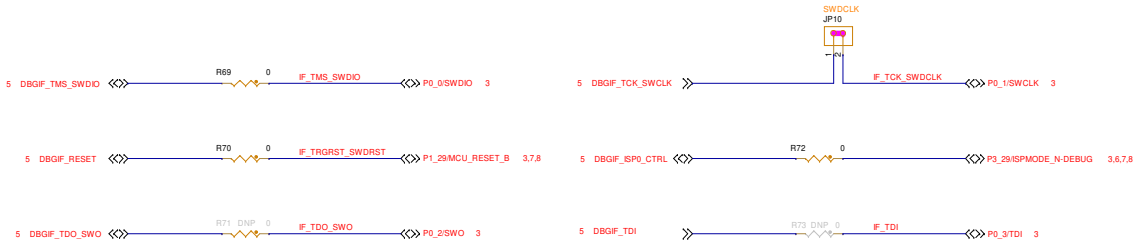
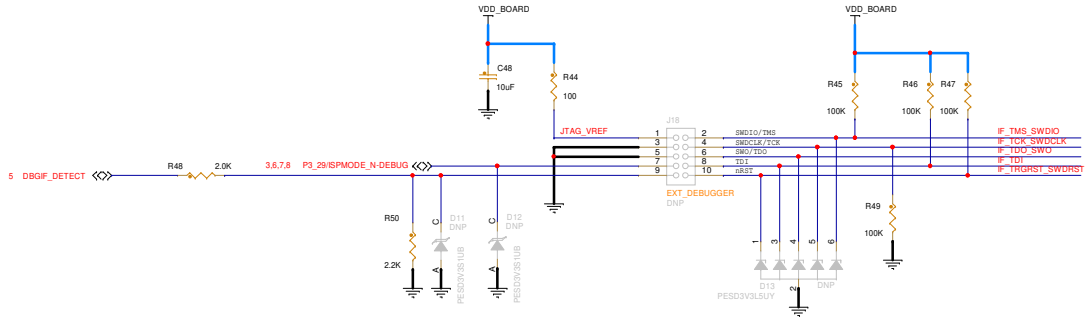
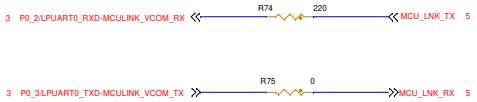
MCU-Link Debug Interface



External Debugger



MCU VCOM



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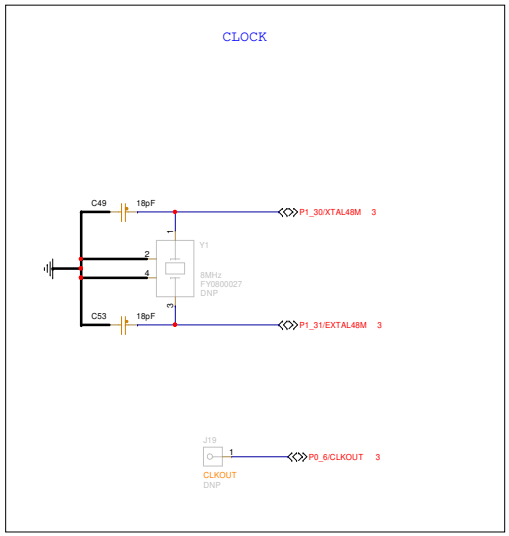

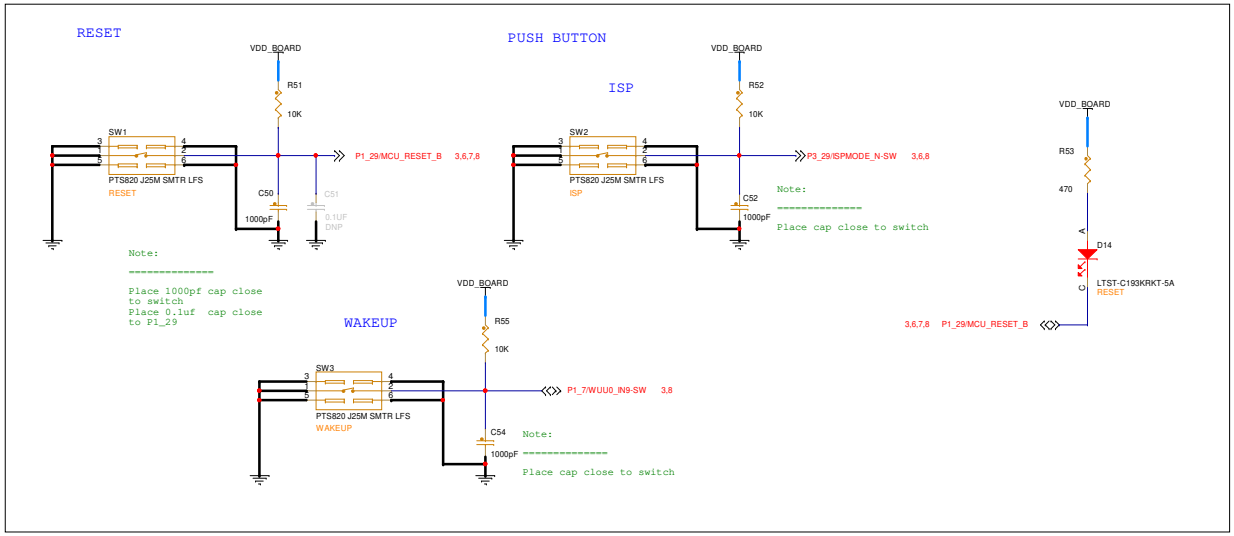

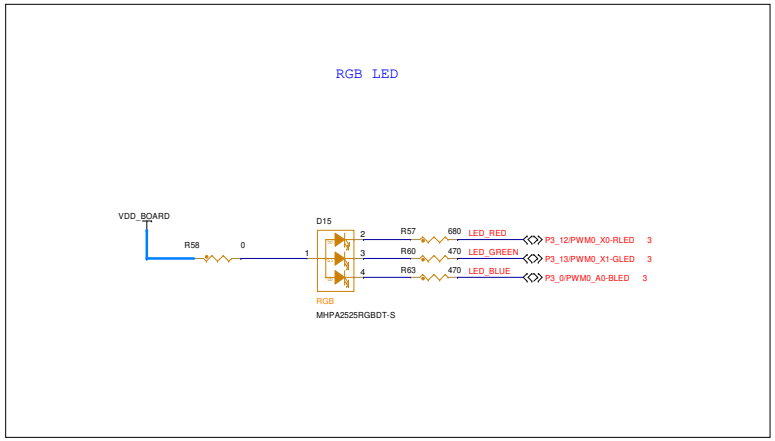

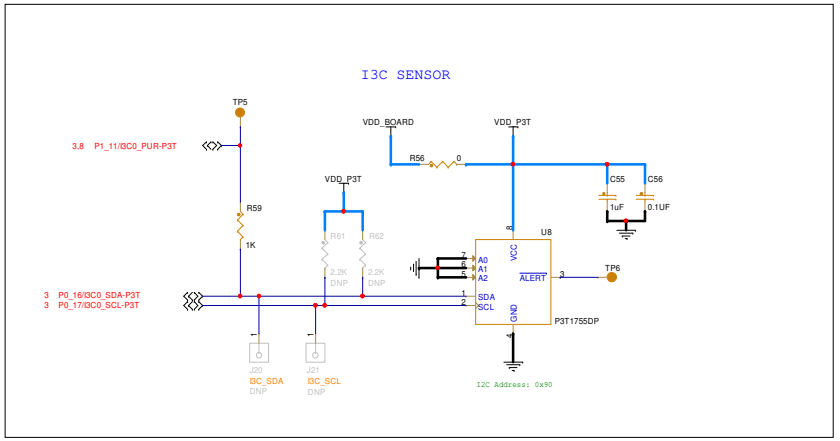

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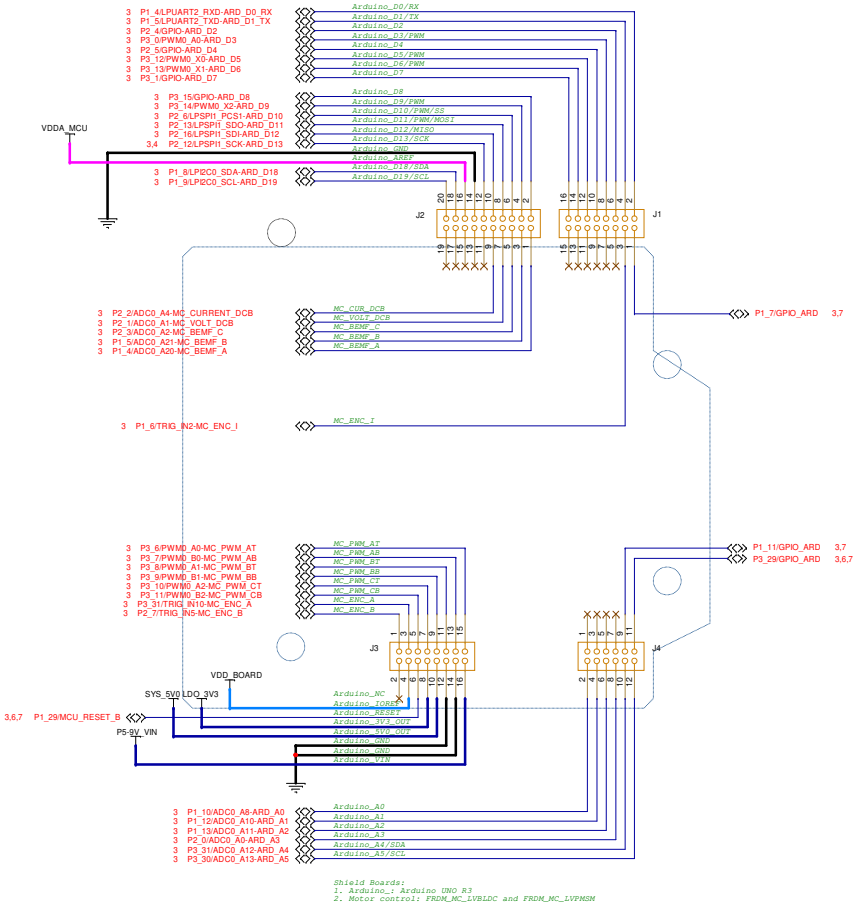
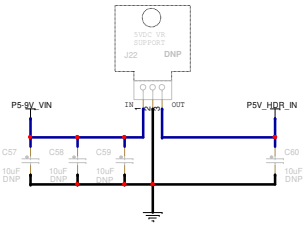
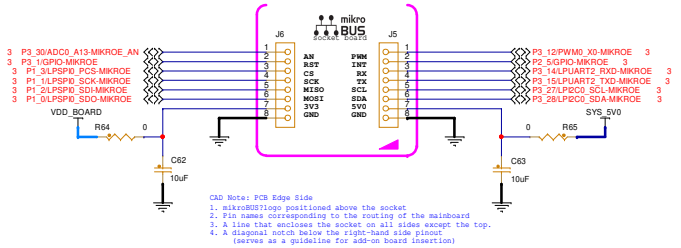
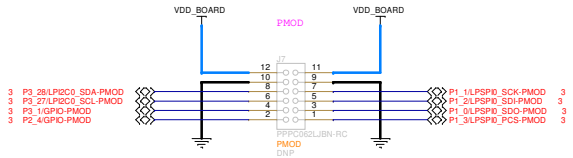
ARDUINO SHIELD COMPATIBLE HEADERS









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REF DES	*JUMPER*	*PAGE NAME*
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JP1,JP3	OPEN	03 MCU
JP6	OPEN	04 POWER
JP8,JP19,JP20	OPEN	05 MCU_LINK_USB
JP10	1-2	06 MCU_LINK_DEBUG

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D3,J9,J10,J11,J12,J13,J14, JP6,U3	DNP	04 POWER
C37,C38,R36,R37,R119,R120,U6	DNP	05 MCU_LINK_USB
D11,D12,D13,J18,R71,R73	DNP	06 MCU_LINK_DEBUG
C51,J19,J20,J21,R61,R62,Y1	DNP	07 CLOCK_RESET_HMI
C57,C58,C59,C60,J7,J22	DNP	08 HEADERS

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