Power

File: Power.kicad_sch

- FTDI_USB_D+ — USB_D+
- FTDI_USB_D- — USB_D-
- RADAR_$\overline{RST}$_1 — RADAR_$\overline{RST}$_1
- REG_MODE_SELECT — REG_MODE_SELECT

FTDI

File: FTDI.kicad_sch

- FTDI_USB_D+ — USB_D+
- FTDI_USB_D- — USB_D-
- RADAR_$\overline{RST}$_2 — RADAR_$\overline{RST}$_2
- USER_LED_WS_GPIO_0 — USER_LED_SW_GPIO_0
- FTDI_HOST_INT_1 — FTDI_HOST_INT_1
- FTDI_SOP0 — FTDI_SOP0
- FTDI_SOP1 — FTDI_SOP1
- FTDI_SPI_CLK — FTDI_SPI_CLK
- FTDI_SPI_MISO — FTDI_SPI_MOSI
- FTDI_SPI_MOSI — FTDI_SPI_MISO
- FTDI_SPI_CS — FTDI_SPI_CS

COM busses

File: COM_busses.kicad_sch

- CAN_PHY_TX — CAN_TX
- CAN_PHY_RX — CAN_RX
- CAN_STB_CTRL — CAN_STB_CTRL
- LIN_PHY_RX — LIN_PHY_RX
- PMIC_CLKOUT_LIN_PHY_TX_SOP1 — PMIC_CLKOUT_LIN_PHY_TX_SOP1
- LIN_PHY_EN_CTRL — LIN_PHY_EN_CTRL
- RS232_TX — RS232_TX
- RS232_RX — RS232_RX
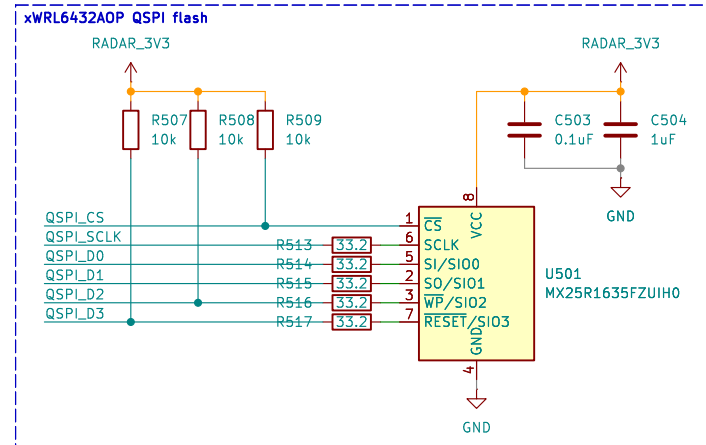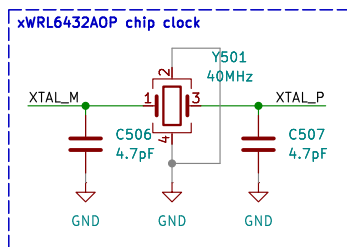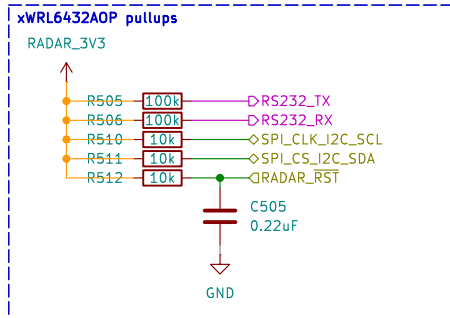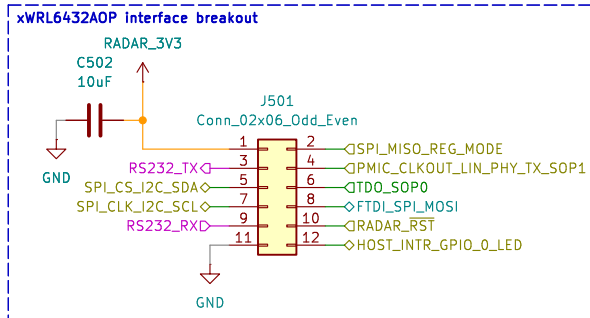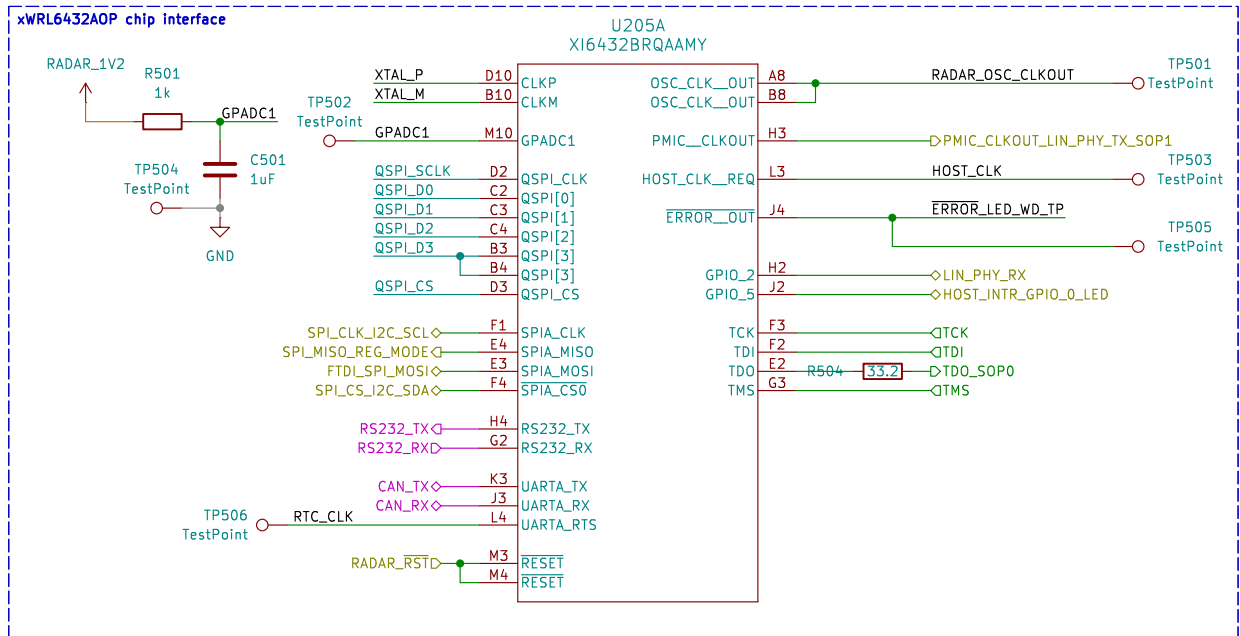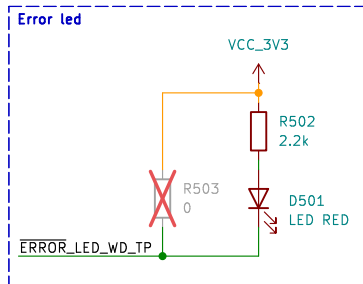
xWRL6432AOP interface

File: xWRL6432AOP_interface.kicad_sch
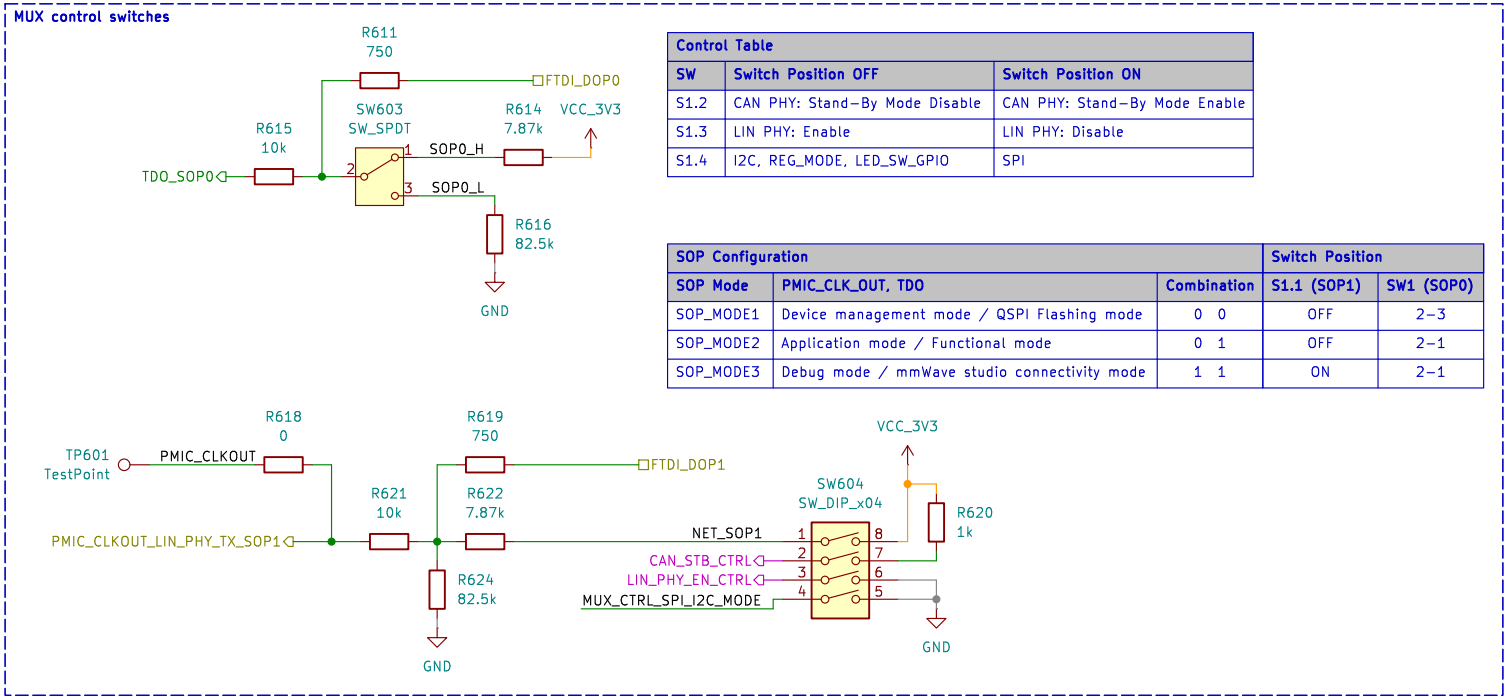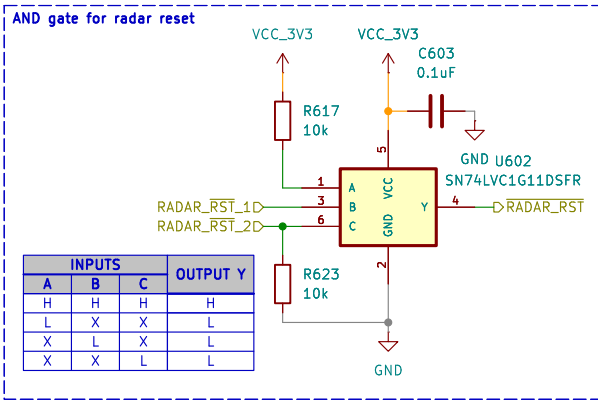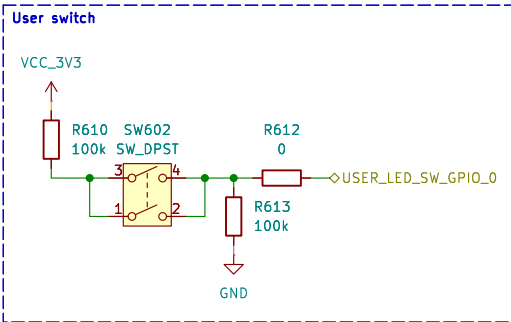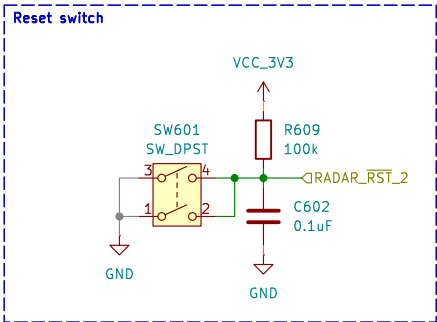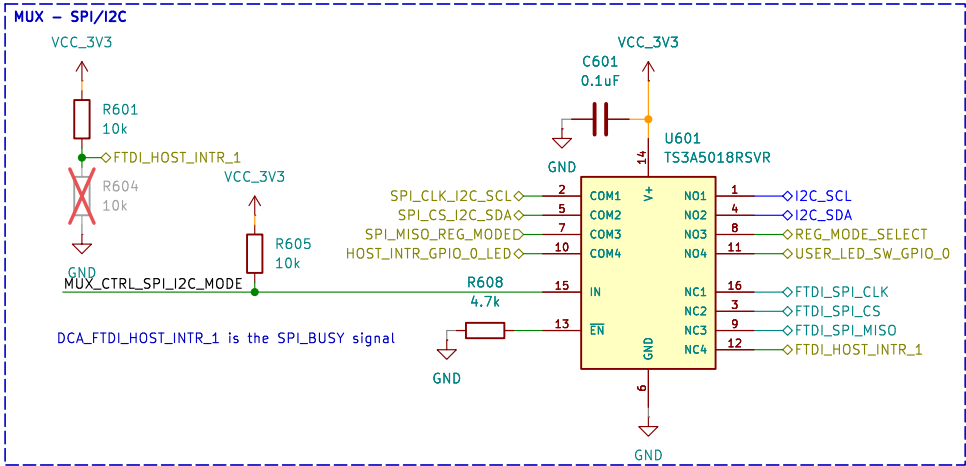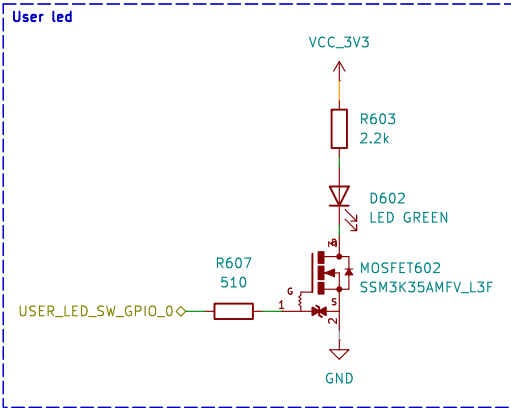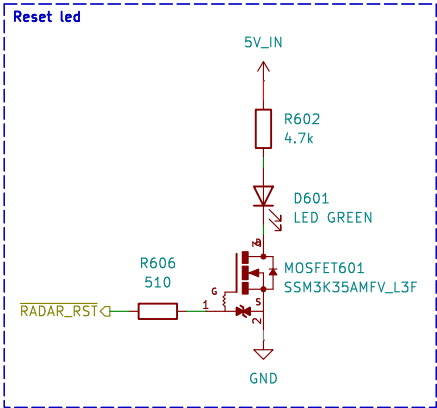
- TCK — TCK
- TDI — TDI
- TDO_SOP0 — TDO_SOP0
- TMS — TMS
- PMIC_CLKOUT_LIN_PHY_TX_SOP1 — PMIC_CLKOUT_LIN_PHY_TX_SOP1
- LIN_PHY_RX — LIN_PHY_RX
- SPI_CLK_I2C_SCL — SPI_CLK_I2C_SCL
- SPI_CS_I2C_SDA — SPI_CS_I2C_SDA
- CAN_TX — CAN_TX
- CAN_RX — CAN_RX
- RS232_TX — RS232_TX
- RS232_RX — RS232_RX
- FTDI_SPI_MOSI — FTDI__SPI_MOSI
- SPI_MISO_REG_MODE — SPI_MISO_REG_MODE
- HOST_INTR_GPIO_0_LED — HOST_INTR_GPIO_0_LED
- RADAR_$\overline{RST}$ — RADAR_RST

Muxes

File: Muxes.kicad_sch

- FTDI_HOST_INT_1 — FTDI_HOST_INTR_1
- FTDI_SPI_CLK — FTDI_SPI_CLK
- FTDI_SPI_MISO — FTDI_SPI_MISO
- FTDI_SPI_CS — FTDI_SPI_CS
- FTDI_SOP0 — FTDI_DOP0
- FTDI_SOP1 — FTDI_DOP1
- TDO_SOP0 — TDO_SOP0
- HOST_INTR_GPIO_0_LED — HOST_INTR_GPIO_0_LED
- USER_LED_SW_GPIO_0 — USER_LED_SW_GPIO_0
- SPI_CLK_I2C_SCL — SPI_CLK_I2C_SCL
- SPI_CS_I2C_SDA — SPI_CS_I2C_SDA
- SPI_MISO_REG_MODE — SPI_MISO_REG_MODE
- I2C_SCL — I2C_SCL
- I2C_SDA — I2C_SDA
- PMIC_CLKOUT_LIN_PHY_TX_SOP1 — PMIC_CLKOUT_LIN_PHY_TX_SOP1
- LIN_PHY_EN_CTRL — LIN_PHY_EN_CTRL
- CAN_STB_CTRL — CAN_STB_CTRL
- REG_MODE_SELECT — REG_MODE_SELECT
- RADAR_$\overline{RST}$_2 — RADAR_$\overline{RST}$_2
- RADAR_$\overline{RST}$_1 — RADAR_$\overline{RST}$_1
- RADAR_$\overline{RST}$ — RADAR_RST

Mounting holes

- H101 — GND
- H102 — GND
- H103 — GND
- H104 — GND

Table of contents

I2C & SPI header for FTDI interface

J101 Conn_01x03_Socket
- 1 — I2C_SCL
- 2 — I2C_SDA
- 3 — GND

VCC_3V3
J102 Conn_01x07_Socket
- 1
- 2 — FTDI_HOST_INT_1
- 3 — FTDI_SPI_CLK
- 4 — FTDI_SPI_CS
- 5 — FTDI_SPI_MISO
- 6 — FTDI_SPI_MOSI
- 7 — GND

Block diagram

- Reset switch — nRESET
- SOP switch (2x) — SOPs
- QSPI flash MX25R1635 FZUIHD — QSPI
- xWRL6432AoP
- 40MHz XTAL
- FT4232HQ FTDI — SPI/SOP /RST/INT
- USB 5V
- DC JACK 5V
- LDO TLV75533PDRV — 3V3
- Load switch TPS2116DRLR — 5V
- DC DC converter TPS628502DRLR — 1V8 / 5V
- DC DC converter TPS628502DRLR — 1V2 / 5V
- User LED — User Switch — GPIO
- CAN FD Interface TCAN1024HGVDQ1 — CAN FD — CAN header
- LIN PHY Interface TLIN1039DDFRQ1 — LIN — LIN header
- UART-to-RS232 TRS3221EIDBR — UART — RS232 header

12-pin header:
- I2C header — GND, I2C
- SPI header — GND, SPI/INT, 3V3
- 3V3, GND, SOP, UART, SPI/I2C, RESET

JTAG interface

VCC_3V3
J103 Conn_01x06_Socket
- 1
- 2 — TCK
- 3 — TDI
- 4 — TDO_SOP0
- 5 — TMS
- 6 — GND

USB C connector
J202 USB_C_Receptacle_USB2.0_16P
C201 10uF
FB202 30@100MHz 2.2A
D201 PMEG3020ER
C202 0.47uF
C203 0.22uF
C204 10uF
C205 10uF
C206 4.7uF
USB_5V
5V_IN_USB
GND
R203 5.1k
R204 5.1k
R207 330k
U202 USBLC6-2P6
FTDI_USB_D+
FTDI_USB_D-
USB_5V

DC jack
J201 Barrel_Jack_Switch
5V 3A
F201 32V 5A
DC_IN
D203 5.8v
C216 47uF
C217 47uF
FB203 30@100MHz 4A
D202 PMEG3020ER
FB201 30@100MHz 4A
5V_REG
GND

DC-DC regulators
5V_IN
C207 10uF
C208 0.47uF
C209 470pF
R201 100k
MODE
U201 TPS628502DRLR
VIN / EN / MODE/SYNC / COMP/FSET / FB / PG / SW
R205 18k
COMP2
L201 470nH
R202 68k
C210 10pF
C211 10uF
C212 10uF
C213 22uF
C214 22uF
L202 100uH
C215 22uF
R206 68k
REG_OUT_1V2
REG_1V2
1V2_FB
REG_PG
SW2

Design note:
For fixed regulator mount:
1. U201 - TPS685020ADRLR
2. R202 - 0ohm
3. R206 - DNP
4. C212 - DNP

REG_OUT_1V2
REG_RF_1V2
L203 100uH
C218 22uF

5V_IN
C219 10uF
C220 0.47uF
C221 470pF
R211 100k
MODE
U203 TPS628502DRLR
VIN / EN / MODE/SYNC / COMP/FSET / FB / PG / SW
R213 18k
COMP3
L204 470nH
R212 80.6k
C222 10pF
C223 10uF
C224 10uF
C225 22uF
C226 22uF
L205 100uH
C227 47uF
R214 40.2k
REG_OUT_1V8
REG_1V8
1V8_FB
REG_PG
SW3

Design note:
For fixed regulator mount:
1. U203 - TPS685020MDRLR
2. R211 - 0ohm
3. CR214 - DNP
4. C226 - DNP

Load switch
FTDI_VBUS
FB204 30@100MHz 2.2A
D206 PMEG3020ER
5V_IN_USB
5V_REG
5V_IN_USB
C228 1uF
C229 0.1uF
C230 1uF
C231 0.1uF
R215 10k
SW_PR1
R216 100k
5V_REG
U204 TPS21116DRL
VIN1 / PR1 / MODE / VIN2 / VOUT / ST / GND
TP201 TestPoint
5V_IN_USB
R217 10k
C232 0.1uF
C233 1uF
C234 10uF
5V_IN
SW_ST
TP202 TestPoint

Mode: Priority Mode:
If VPR1 is high (VPR1 > VREF), then Vout = Vin1
If VPR1 is low (VPR1 < VREF), then Vout = Vin2

Indicator leds
5V_IN
R208 4.7k
D204 LED GREEN
VCC_3V3
R209 2.2k
D205 LED YELLOW
R210 510
PGOOD
Q201 BSS123
GND

Mode control
R218 0
MODE
REG_MODE_SELECTD
R219 100k
MOSFET201 SSM3K35AMFV_L3F
GND

3V3 LDO
5V_IN
U206 TLV75533PDRV
IN / EN / OUT / GND
VCC_3V3
VCC_3V3_EN
C246 2.2uF
C245 2.2uF
R221 100k
R222 0
REG_PG
RADAR_RST_1
TP205 TestPoint
VCC_3V3
R223 0
PGOOD
R224 100k
TP206 TestPoint
REG_1V8
R225 10k
VCC_3V3_EN

xWRL6432AOP power
RADAR_1V2
RADAR_SRAM_1V2
R220 0
TP203 TestPoint
U205B XI6432BRQAAMY
RADAR_1V2
RADAR_SRAM_1V2
RADAR_1V_RF_OUT
RADAR_1P2_RF
RADAR_1V8
RADAR_3V3
RADAR_1V4_APLL
RADAR_1V4_SYNTH
VDD / VDD_SRAM
VOUT_14APLL / VOUT_14SYNTH
VDDA_10RF (C7, K7, K8, K9, K10, K11)
VDDA_12RF (L7, L8, L9, L10)
VDDA_18BB (M7, M8, M9, N6, N7, N8, N9, N10)
VDDA_18VCO (F9, F10)
VIN_18PM (H7, H8)
VIOIN (C1, J1)
VIOIN_18 (B1, L1, L2)
VIOIN_18CLK (D7)
VPP (A2, B2)
VNWA (M2, N2)
VBGAP (H10)
VSS / VSSA (A1, A3, A4, A5, E1, M1, N1, N3, N5, A6, A7, A9, A10, B7, B9, C8, C9, C10, D8, D9, E7, E8, E9, E10, F8, G8, G9, G10, H9, J7, J8, J9, J10)
TP208 TestPoint
VPP_1P7
C263 0.1uF
RADAR_SRAM_1V2
RADAR_VBGAP
GND

xWRL6432AOP decoupling

BB SUPPLY
RADAR_1V8
TP204 TestPoint
C235 0.22uF
C236 0.22uF
C237 10uF

VCLK SUPPLY
RADAR_1V8
C238 0.22uF
C239 10uF

VCO_LDO SUPPLY
RADAR_1V8
C240 0.22uF

PM SUPPLY
RADAR_1V8
C241 0.22uF

1PBV_IO SUPPLY
RADAR_1V8
C242 0.22uF
C243 0.22uF
C244 0.22uF

VBGAP SUPPLY
RADAR_VBGAP
C247 0.047uF

VOUT_PLL SUPPLY
RADAR_1V4_APLL
C248 1uF

VOUT_SYNTH SUPPLY
RADAR_1V4_SYNTH
C249 1uF

3V3_IO SUPPLY
RADAR_3V3
C250 2.2uF
C251 0.22uF
C252 0.22uF

Design note:
Decoupling capacitors followed as
per xWRL6432 FCCSP EVM

VNWA SUPPLY
RADAR_SRAM_1V2
C253 0.22uF

SRAM SUPPLY
RADAR_SRAM_1V2
C254 0.22uF

1V_RF_OUT SUPPLY
RADAR_1V_RF_OUT
C255 2.2uF
C256 0.22uF
C257 10uF

1V2_DIG SUPPLY
RADAR_1P2_RF
C258 4.7uF
C259 0.22uF
C260 0.22uF
C261 10nF
C262 10nF
TP207 TestPoint

FTDI – Power filtering

VCC_3V3 → FB301 600@100MHz 0.3A → FTDI_3V3

FTDI – Decoupling

FTDI_VCORE
- C301 4.7uF
- C302 0.1uF
- GND

FTDI_3V3 → FB302 600@100MHz 0.3A → VPHY_FTDI
- C303 4.7uF
- C304 0.1uF
- GND

FTDI_VCORE
- C306 0.1uF
- C307 0.1uF
- GND

FTDI_3V3 → FB303 600@100MHz 0.3A → VPLL_FTDI
- C308 4.7uF
- C309 0.1uF
- GND

FTDI_3V3
- C310 1uF
- C311 0.1uF
- GND

FTDI_3V3
- C312 1uF
- C313 0.1uF
- C314 0.1uF
- GND

FTDI – Clock

XTAL_M — Y301 12MHz — XTAL_P
- C315 8pF — GND
- GND
- C316 8pF — GND

FTDI – USB to SPI converter

VPLL_FTDI
VPHY_FTDI
FTDI_VCORE
FTDI_3V3
VCC_3V3

U301
FT4232HQ

FTDI_3V3 → 50 VREGIN
FTDI_VCORE → 49 VREGOUT

Pins: 4 VPHY, 9 VPLL, 12 VCORE, 37 VCORE, 64 VCORE, 20 VCCIO, 31 VCCIO, 42 VCCIO, 56 VCCIO

ADBUS0 16 SPI_CLK — R302 33.2 — FTDI_SPI_CLK
ADBUS1 17 SPI_MOSI — R303 33.2 — FTDI_SPI_MOSI
ADBUS2 18 SPI_MISO — R304 0 — FTDI_SPI_MISO
ADBUS3 19 SPI_CS — R305 33.2 — FTDI_SPI_CS
ADBUS4 21
ADBUS5 22
ADBUS6 23
ADBUS7 24

R301 100k

FTDI_USB_D+ → 7 DM
FTDI_USB_D- → 8 DP

BDBUS0 26
BDBUS1 27
BDBUS2 28
BDBUS3 29
BDBUS4 30
BDBUS5 32 HOSTIRQ_FTDI_IN — R307 0 — FTDI_HOST_INT_1
BDBUS6 33
BDBUS7 34 HOSTIRQ_FTDI_OUT — R312 0

REF_FTDI → 10k R306 → 6 REF
RST_FTDI → 1k R310 → 14 RESET
GND

CDBUS0 38
CDBUS1 39
CDBUS2 40
CDBUS3 41
CDBUS4 43
CDBUS5 44
CDBUS6 45 RST_FTDI — R314 0 — RADAR_RST_2
CDBUS7 46

EECS → 63 EECS
EECLK → 61 EEDATA
EEDATA → 62 EECLK

DDBUS0 48
DDBUS1 52
DDBUS2 53
DDBUS3 54 SOP1_FTDI — R315 0 — FTDI_SOP1
DDBUS4 55 SOP0_FTDI — R316 0 — FTDI_SOP0
DDBUS5 57
DDBUS6 58
DDBUS7 59 USR_LED_SW — R317 0 — USER_LED_WS_GPIO_0

XTAL_M → 2 OSCI
XTAL_P → 3 OSCO

PWREN 60
SUSPEND 36 — R318 33.2 — FTDI_SUSPEND

13 TEST
AGND 10
GND: 1, 5, 11, 15, 25, 35, 47, 51, 65 EXP
GND

R319 2.2k
D301 LED YELLOW
GND

FTDI – EEPROM

FTDI_3V3
- C305 0.1uF
- GND
- R311 10k

U302 93LCxxB
- 1 CS
- 8 VCC
- 5 GND
- 2 SCLK
- 3 DI
- 4 DO

EECS → CS
FTDI_3V3
R308 10k
R309 10k
R313 2.2k
EECLK
EEDATA
GND

Designer: EV
Sheet: /FTDI/
File: FTDI.kicad_sch
Title: FTDI
Size: A3   Date: 2025-04-20   Rev: V1.0
KiCad E.D.A. 9.0.1   Id: 3/6

CAN bus transceiver

5V_IN

R401
0

C401
0.1uF

GND

C402 VCC_3V3
0.1uF

GND

U401
TCAN1042HGVDRQ1

GND

5	VIO	
	VCC	3

R402
61.9

C403
4.7uF

CAN_H	1		
	2		J401
CAN_L	3		Conn_01x03_Socket

GND

CAN_PHY_TXD R403 0 1 TXD   CANH 7
CAN_PHY_RX   R404 0 4 RXD   CANL 6

CAN_STB_CTRL R406 0 8 STB   GND 2

R407
10k

R405
61.9

GND

GND

GND

LIN bus transceiver

VCC_3V3

5V_IN

D401   R408
PMEG3020ER   1k

5V_IN

C404
0.1uF

U402
TLIN1039DDFRQ1

	7	VSUP	
		LIN	6

R409   R410
10k   10k

GND

LIN	1			J402
	2			Conn_01x03_Socket
	3			

LIN_PHY_RX R411 0 1 RXD
PMIC_CLKOUT_LIN_PHY_TX_SOP1 R412 0 4 TXD
LIN_PHY_EN_CTRL R413 0 2 EN   GND 5

C405
220pF

GND   GND

GND

RS232 transceiver

U403
TRS3221EIDBR

RS232_TXD 11 DIN   DOUT 13
RS232_RX   8   RIN   ROUT 9

VCC_3V3 12 FORCEON
         16 FORCEOFF

GND 1 EN   INVALID 10

C406   2 C1+   C2+ 5
0.1uF
VCC_3V3 4 C1-   C2- 6

0.1uF
C408 3 V+   V- 7   C409

0.1uF
15 VCC   GND 14

C410
0.1uF

GND

GND

TX	1	
	2	J403
RX	3	Conn_01x03_Socket

D402
NUP2105L

GND

C407
0.1uF

GND

Error led

VCC_3V3

R502
2.2k

R503
0

ERROR_LED_WD_TP

D501
LED RED

xWRL6432AOP chip interface

U205A
XI6432BRQAAMY

RADAR_1V2

R501
1k

GPADC1

Pin	Signal
D10	CLKP — XTAL_P
B10	CLKM — XTAL_M
M10	GPADC1 — GPADC1
D2	QSPI_CLK — QSPI_SCLK
C2	QSPI[0] — QSPI_D0
C3	QSPI[1] — QSPI_D1
C4	QSPI[2] — QSPI_D2
B3	QSPI[3] — QSPI_D3
B4	QSPI[3]
D3	QSPI_CS — QSPI_CS
F1	SPIA_CLK — SPI_CLK_I2C_SCL
E4	SPIA_MISO — SPI_MISO_REG_MODE
E3	SPIA_MOSI — FTDI_SPI_MOSI
F4	SPIA_CS0 — SPI_CS_I2C_SDA
H4	RS232_TX — RS232_TX
G2	RS232_RX — RS232_RX
K3	UARTA_TX — CAN_TX
J3	UARTA_RX — CAN_RX
L4	UARTA_RTS — RTC_CLK
M3	RESET — RADAR_RST
M4	RESET

Pin	Signal
A8	OSC_CLK_OUT — RADAR_OSC_CLKOUT
B8	OSC_CLK_OUT
H3	PMIC_CLKOUT — PMIC_CLKOUT_LIN_PHY_TX_SOP1
L3	HOST_CLK_REQ — HOST_CLK
J4	ERROR_OUT — ERROR_LED_WD_TP
H2	GPIO_2 — LIN_PHY_RX
J2	GPIO_5 — HOST_INTR_GPIO_0_LED
F3	TCK — TCK
F2	TDI — TDI
E2	TDO — R504 33.2 — TDO_SOP0
G3	TMS — TMS

TP502 TestPoint
TP504 TestPoint
C501 1uF
GND

TP501 TestPoint
TP503 TestPoint
TP505 TestPoint

TP506 TestPoint
RTC_CLK

xWRL6432AOP interface breakout

RADAR_3V3

C502
10uF

GND

J501
Conn_02x06_Odd_Even

	1	2	SPI_MISO_REG_MODE
RS232_TX	3	4	PMIC_CLKOUT_LIN_PHY_TX_SOP1
SPI_CS_I2C_SDA	5	6	TDO_SOP0
SPI_CLK_I2C_SCL	7	8	FTDI_SPI_MOSI
RS232_RX	9	10	RADAR_RST
	11	12	HOST_INTR_GPIO_0_LED

GND

xWRL6432AOP pullups

RADAR_3V3

	R505	100k	RS232_TX
	R506	100k	RS232_RX
	R510	10k	SPI_CLK_I2C_SCL
	R511	10k	SPI_CS_I2C_SDA
	R512	10k	RADAR_RST

C505
0.22uF

GND

xWRL6432AOP chip clock

Y501
40MHz

XTAL_M   XTAL_P

C506
4.7pF

C507
4.7pF

GND   GND   GND

xWRL6432AOP QSPI flash

RADAR_3V3

R507 10k
R508 10k
R509 10k

C503 0.1uF
C504 1uF

RADAR_3V3

GND

			Pin	U501
QSPI_CS			1	CS
QSPI_SCLK	R513	33.2	6	SCLK
QSPI_D0	R514	33.2	5	SI/SIO0
QSPI_D1	R515	33.2	2	SO/SIO1
QSPI_D2	R516	33.2	3	WP/SIO2
QSPI_D3	R517	33.2	7	RESET/SIO3

8 VCC
U501
MX25R1635FZUIH0
4 GND

GND

Reset led

5V_IN

R602
4.7k

D601
LED GREEN

R606
510

MOSFET601
SSM3K35AMFV_L3F

RADAR_RST

GND

User led

VCC_3V3

R603
2.2k

D602
LED GREEN

R607
510

MOSFET602
SSM3K35AMFV_L3F

USER_LED_SW_GPIO_0

GND

MUX - SPI/I2C

VCC_3V3

R601
10k

FTDI_HOST_INTR_1

R604
10k

VCC_3V3

GND

R605
10k

MUX_CTRL_SPI_I2C_MODE

DCA_FTDI_HOST_INTR_1 is the SPI_BUSY signal

C601
0.1uF

GND

VCC_3V3

U601
TS3A5018RSVR

SPI_CLK_I2C_SCL   COM1   V+   NO1   1   I2C_SCL
SPI_CS_I2C_SDA   COM2   5   NO2   4   I2C_SDA
SPI_MISO_REG_MODED   COM3   7   NO3   8   REG_MODE_SELECT
HOST_INTR_GPIO_0_LED   COM4   10   NO4   11   USER_LED_SW_GPIO_0

R608
4.7k

15   IN   NC1   16   FTDI_SPI_CLK
NC2   3   FTDI_SPI_CS
13   EN   NC3   9   FTDI_SPI_MISO
NC4   12   FTDI_HOST_INTR_1

GND

GND

MUX control switches

R611
750

FTDI_DOP0

SW603
SW_SPDT

R615
10k

TDO_SOP0

R614
7.87k

VCC_3V3

SOP0_H

SOP0_L

R616
82.5k

GND

Control Table

SW	Switch Position OFF	Switch Position ON
S1.2	CAN PHY: Stand-By Mode Disable	CAN PHY: Stand-By Mode Enable
S1.3	LIN PHY: Enable	LIN PHY: Disable
S1.4	I2C, REG_MODE, LED_SW_GPIO	SPI

SOP Configuration

SOP Mode	PMIC_CLK_OUT, TDO	Combination	Switch Position	
			S1.1 (SOP1)	SW1 (SOP0)
SOP_MODE1	Device management mode / QSPI Flashing mode	0 0	OFF	2-3
SOP_MODE2	Application mode / Functional mode	0 1	OFF	2-1
SOP_MODE3	Debug mode / mmWave studio connectivity mode	1 1	ON	2-1

R618
0

TP601
TestPoint

PMIC_CLKOUT

R619
750

FTDI_DOP1

R621
10k

R622
7.87k

PMIC_CLKOUT_LIN_PHY_TX_SOP1

NET_SOP1

R624
82.5k

GND

VCC_3V3

SW604
SW_DIP_x04

1   8
2   7   CAN_STB_CTRL
3   6   LIN_PHY_EN_CTRL
4   5   MUX_CTRL_SPI_I2C_MODE

R620
1k

GND

Reset switch

VCC_3V3

SW601
SW_DPST

R609
100k

3   4
1   2

RADAR_RST_2

C602
0.1uF

GND

GND

User switch

VCC_3V3

R610
100k

SW602
SW_DPST

R612
0

3   4
1   2

USER_LED_SW_GPIO_0

R613
100k

GND

GND

AND gate for radar reset

VCC_3V3   VCC_3V3

C603
0.1uF

GND   U602
SN74LVC1G11DSFR

R617
10k

RADAR_RST_1   1   A   VCC   5
RADAR_RST_2   3   B   Y   4   RADAR_RST
6   C
GND
2

R623
10k

GND

INPUTS			OUTPUT Y
A	B	C	
H	H	H	H
L	X	X	L
X	L	X	L
X	X	L	L