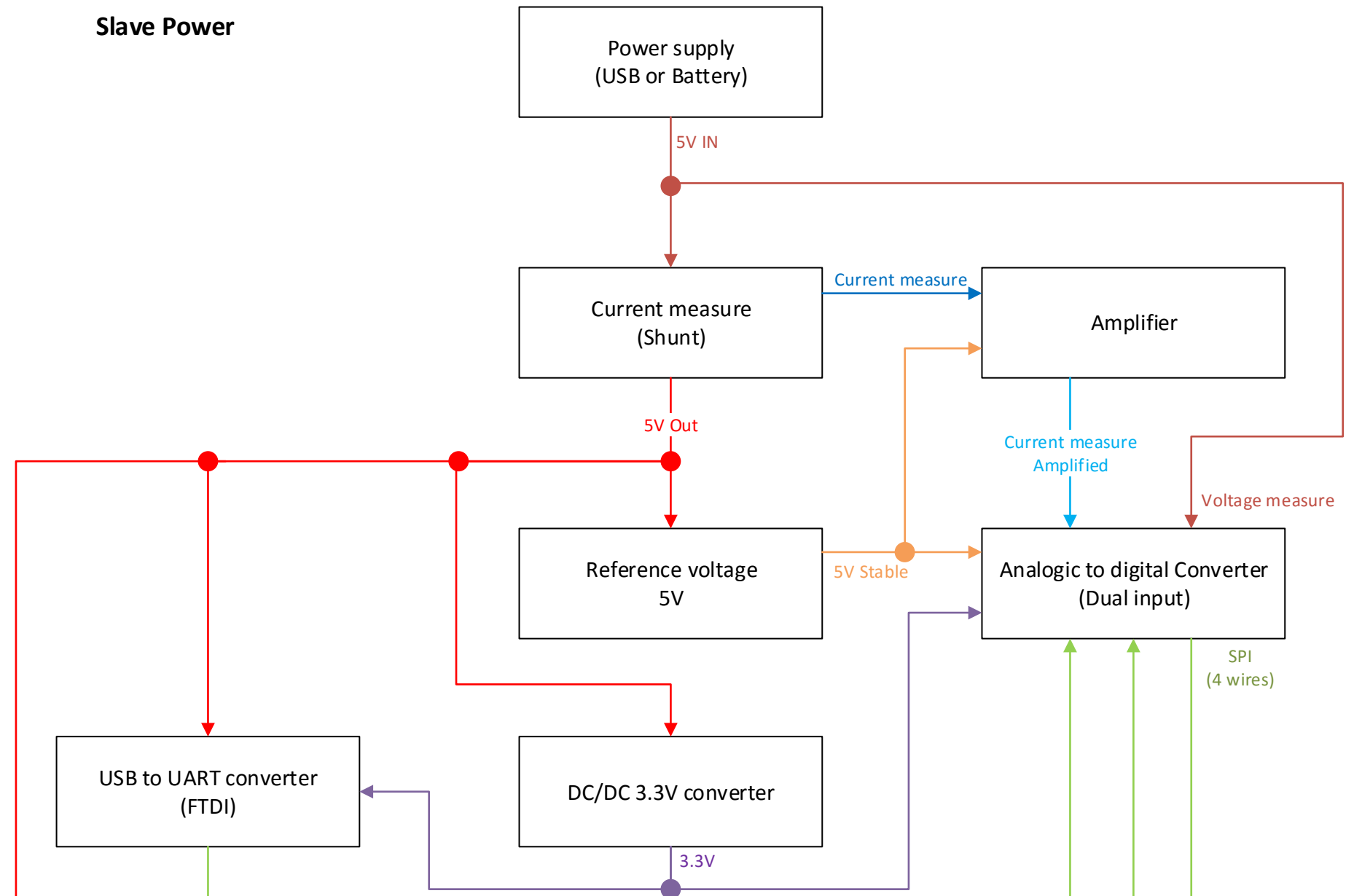


Slave Power



Master FPGA

Sync (1 wire)
Clock (1 wire)