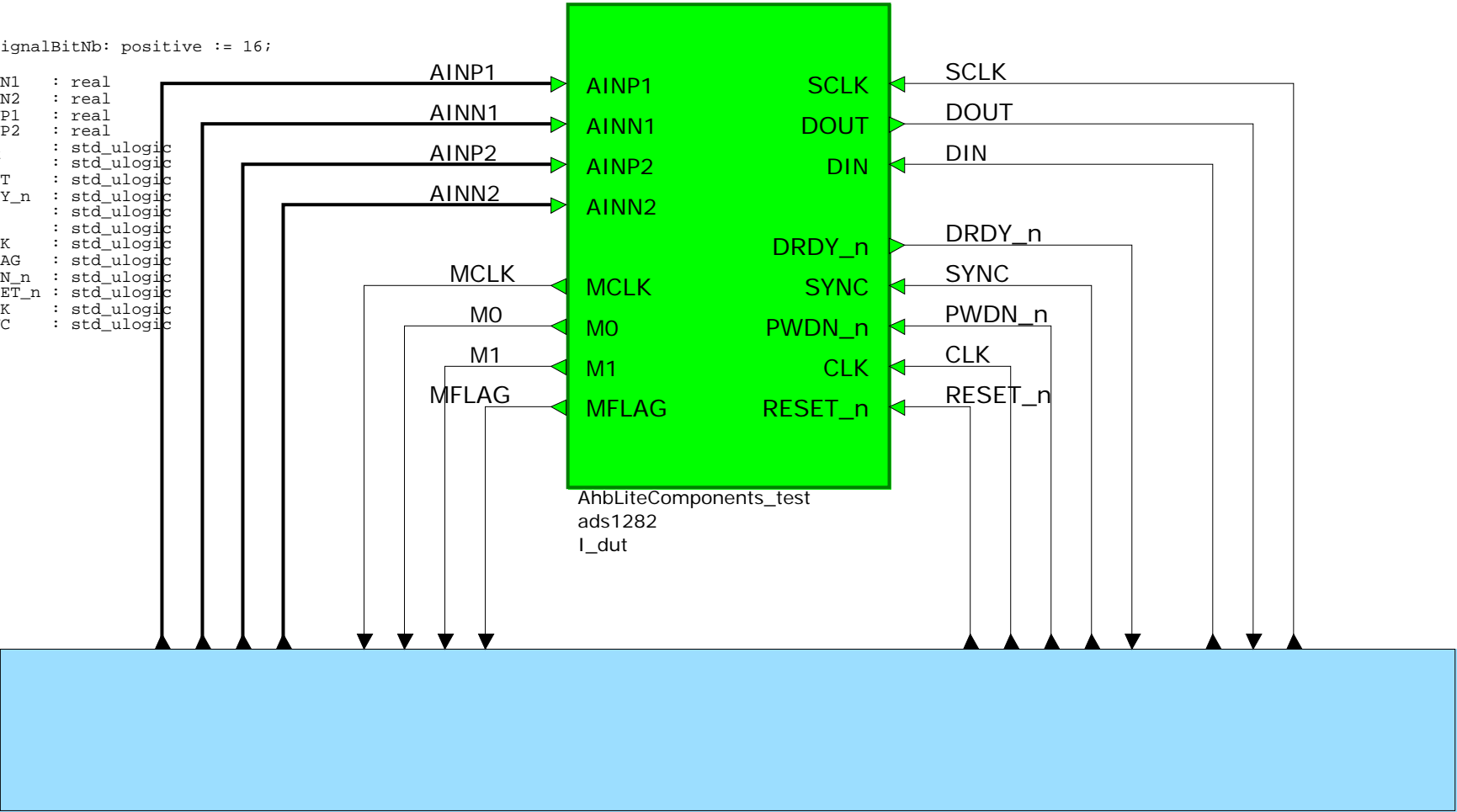**Package List**
LIBRARY ieee;
  USE ieee.std_logic_1164.all;
  USE ieee.numeric_std.ALL;

Declarations
Ports:
Pre User:
    constant signalBitNb: positive := 16;

Diagram Signals:
    SIGNAL AINN1   : real
    SIGNAL AINN2   : real
    SIGNAL AINP1   : real
    SIGNAL AINP2   : real
    SIGNAL CLK   : std_ulogic
    SIGNAL DIN   : std_ulogic
    SIGNAL DOUT   : std_ulogic
    SIGNAL DRDY_n   : std_ulogic
    SIGNAL M0   : std_ulogic
    SIGNAL M1   : std_ulogic
    SIGNAL MCLK   : std_ulogic
    SIGNAL MFLAG   : std_ulogic
    SIGNAL PWDN_n   : std_ulogic
    SIGNAL RESET_n : std_ulogic
    SIGNAL SCLK   : std_ulogic
    SIGNAL SYNC   : std_ulogic

AINP1 — AINP1   SCLK — SCLK
AINN1 — AINN1   DOUT — DOUT
AINP2 — AINP2   DIN — DIN
AINN2 — AINN2

MCLK — MCLK   DRDY_n — DRDY_n
M0 — M0   SYNC — SYNC
M1 — M1   PWDN_n — PWDN_n
MFLAG — MFLAG   CLK — CLK
        RESET_n — RESET_n

AhbLiteComponents_test
ads1282
I_dut

AhbLiteComponents_test
ads1282_tester
I_tester