60 V, single N-channel Trench MOSFET

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- · Very fast switching
- Trench MOSFET technology
- ESD protected

3. Applications

- Relay driver
- · High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	60	V
V_{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{sp} = 25 °C		-	-	300	mA
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	190	mA
Static charact	teristics		'		•	'	
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 100 \text{ mA}; T_j = 25 \text{ °C}$		-	3	4.5	Ω

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D I
2	S	source		
3	D	drain	TO-236AB (SOT23)	G S 017aaa255

6. Ordering information

Table 3. Ordering information

Type number	Package	ckage					
	Name	Description	Version				
2N7002NXAK	TO-236AB	plastic surface-mounted package; 3 leads	SOT23				

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
2N7002NXAK	%CM

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j = 25 °C		-	60	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{sp} = 25 °C		-	300	mA
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	190	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	120	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	760	mA
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	265	mW
			[1]	-	325	mW
		T _{sp} = 25 °C		-	1330	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain	n diode					•
Is	source current	T _{amb} = 25 °C	[1]	-	190	mA

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

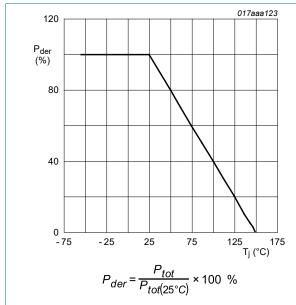


Fig. 1. Normalized total power dissipation as a function of junction temperature

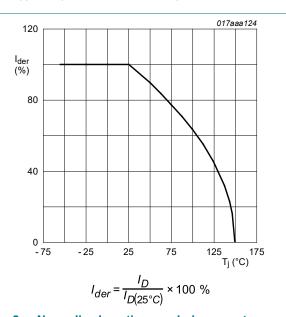
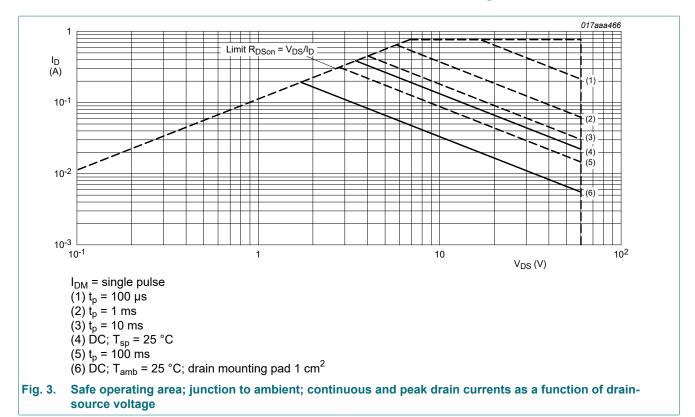


Fig. 2. Normalized continuous drain current as a function of junction temperature

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	410	470	K/W
ju	junction to ambient		[2]	-	330	380	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	95	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

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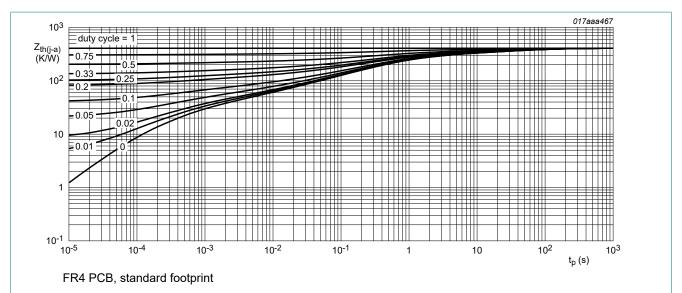


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

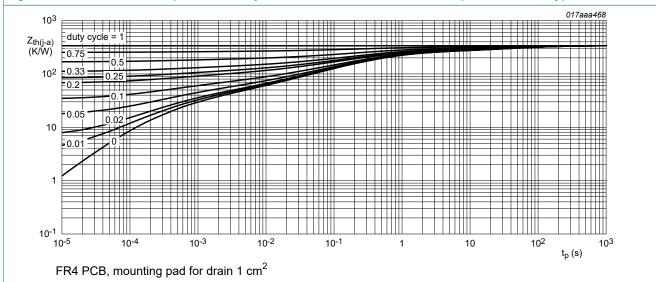


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	60	-	-	V
V_{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	1.1	1.6	2.1	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _j = 150 °C	-	-	10	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	2	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-2	μΑ
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	0.5	μA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-0.5	μA
		V _{GS} = 5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
		V _{GS} = -5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 100 mA; T _j = 25 °C	-	3	4.5	Ω
		V _{GS} = 10 V; I _D = 100 mA; T _j = 150 °C	-	6.2	9.2	Ω
		V _{GS} = 5 V; I _D = 100 mA; T _j = 25 °C	-	3.7	5.2	Ω
9 _{fs}	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 200 \text{ mA}; T_j = 25 \text{ °C}$	-	500	-	mS
Dynamic ch	naracteristics					
Q _{G(tot)}	total gate charge	V _{DS} = 30 V; I _D = 200 mA; V _{GS} = 4.5 V;	-	0.33	0.43	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.12	-	nC
Q_{GD}	gate-drain charge		-	0.09	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V;	-	15	20	pF
C _{oss}	output capacitance	T _j = 25 °C	-	3.4	-	pF
C _{rss}	reverse transfer capacitance		-	2	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 40 \text{ V}; R_L = 250 \Omega; V_{GS} = 10 \text{ V};$	-	6	12	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	7	-	ns
$t_{d(off)}$	turn-off delay time	1 – – – – – – – – – – – – – – – – – – –	-	11	20	ns
t _f	fall time	1	-	5	-	ns
Source-dra	in diode		1			
V _{SD}	source-drain voltage	I _S = 115 mA; V _{GS} = 0 V; T _i = 25 °C	0.47	0.8	1.2	V

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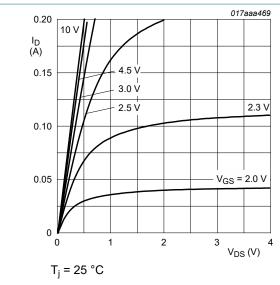
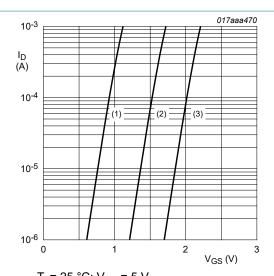


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values



 T_i = 25 °C; V_{DS} = 5 V(1) minimum values

- (2) typical values
- (3) maximum values

Sub-threshold drain current as a function of Fig. 7. gate-source voltage

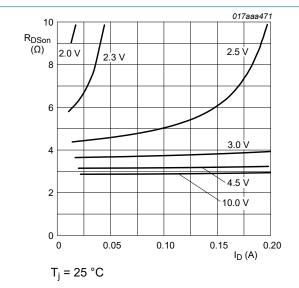
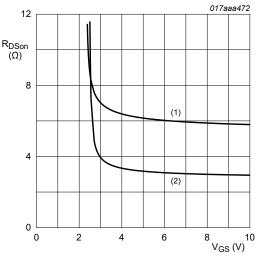


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values



 $I_D = 0.2 A$ (1) $T_j = 150 \, ^{\circ}C$ (2) $T_j = 25 \,^{\circ}\text{C}$

Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

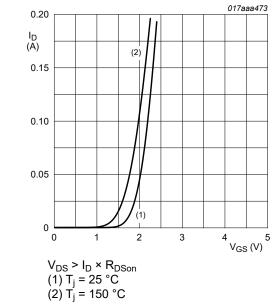
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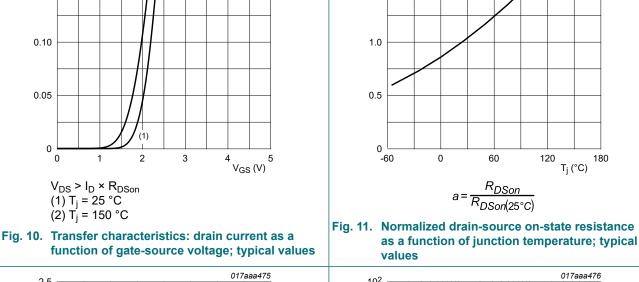
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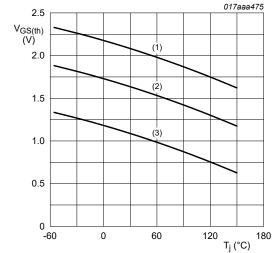
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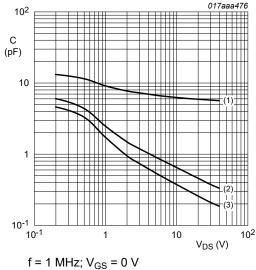




 $I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}$

- (1) maximum values
- (2) typical values
- (3) minimum values

Fig. 12. Gate-source threshold voltage as a function of junction temperature



- (1) C_{iss}
- (2) C_{oss}
- (3) C_{rss}

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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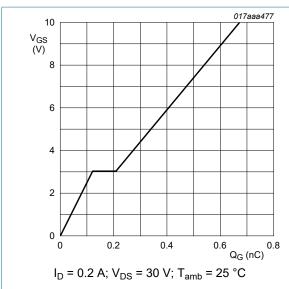


Fig. 14. Gate-source voltage as a function of gate charge; typical values

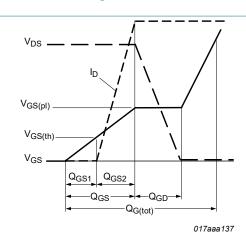
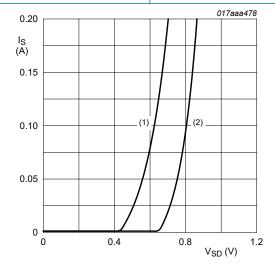


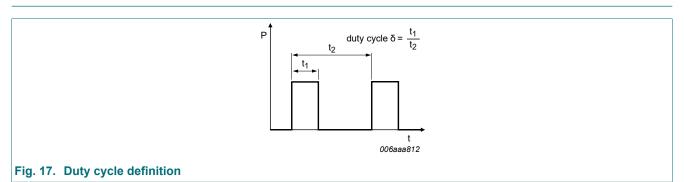
Fig. 15. MOSFET transistor: Gate charge waveform definitions



 $V_{GS} = 0 V$ (1) $T_j = 150 \,^{\circ}C$ (2) $T_j = 25 \,^{\circ}C$

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information



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12. Package outline

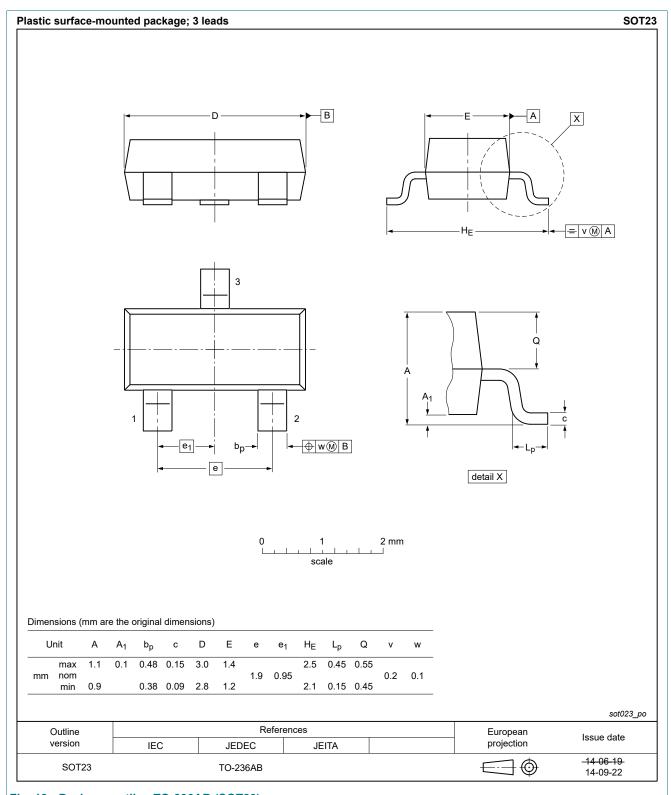
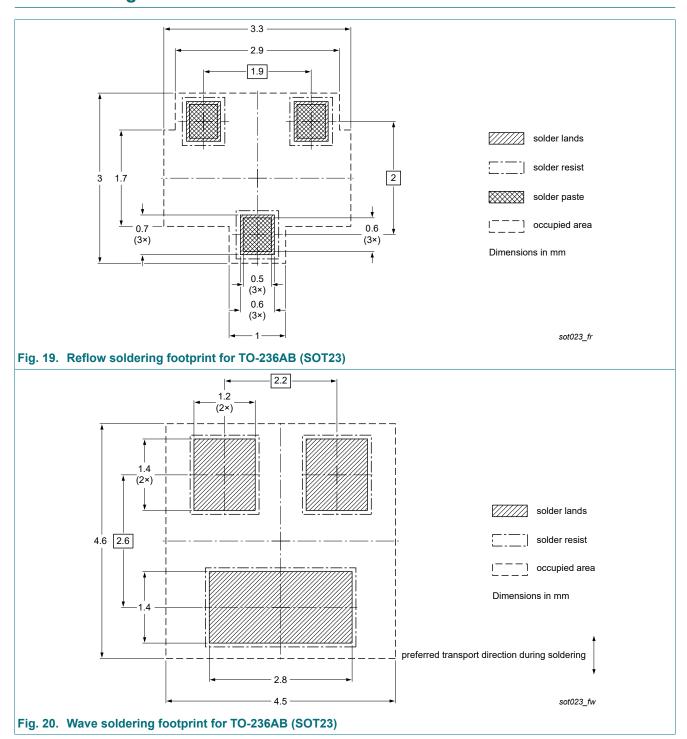


Fig. 18. Package outline TO-236AB (SOT23)

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
2N7002NXAK v.1	20190701	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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