## ALU Slices PLA truth tables Instructor: Prof. Lei

Slices 1-30  Instruction add	In8 func5 1 1 1 1 1 1	In7 func4 0 0 0	In6 func3 0 0	InS func2	PLA inputs In4 func1	In3 func0	In2 A	Inl B	In0 Ci	Out2	PLA outputs Out1	Out0	TS PLA terms	Co PLA terms	R PLA terms	
add	1 1 1 1	0 0 0				func0	A	R	~	-			TS PLA terms	Co PLA terms	R PLA terms	
	1 1 1	0									<u>Co</u>	R				
addu	1 1 1	0	U	0	0	0	0	0	0	1	0	0	1xxxxxxxx 1xxxxxxxx		100000001	Co, R = A + B + C
addu	1 1 1		0	0	0	0	0	1	0	1	0	1	1xxxxxxxx 1xxxxxxxx		10000001	
addu	1 1		0	0	0	0	0	1	1	1	1	0	1xxxxxxxx	100000011	100000010	
addu	1	0	0	0	0	0	1	0	0	1	0	1	1xxxxxxxx		100000100	
addu		0	0	0	0	0	1	0	1	1	1	0	1xxxxxxxx	100000101		
addu	1	0	0	0	0	0	1	1	0	1	1	0	1xxxxxxxx	100000110 100000111	100000111	
	1	0	0	0	0	0	0	0	0	1	0	0	1xxxxxxxx 1xxxxxxxx	100000111	100000111	same as add
	1	0	0	0	0	1	0	0	1	1	0	1	1xxxxxxxx		100001001	same as auu
	1	0	0	0	0	1	0	1	0	1	0	1	1xxxxxxxx		100001010	
	1	0	0	0	0	1	0	1	1	1	1	0	1xxxxxxxx	100001011		
	1	0	0	0	0	1	1	0	0	1	0	1	1xxxxxxxx		100001100	
	1	0	0	0	0	1	1	0	1	1	1	0	1xxxxxxxx 1xxxxxxxx	100001101 100001110		
	1	0	0	0	0	1	1	1	1	1	1	1	1xxxxxxxx 1xxxxxxxx	100001110	100001111	
sub	1	0	0	0	1	0	0	0	0	1	0	1	1xxxxxxxx	100001111	100010000	$Co_*R = A+B'+Ci$
	1	0	0	0	1	0	0	0	1	1	1	0	1xxxxxxxx	100010001		
	1	0	0	0	1	0	0	1	0	1	0	0	1xxxxxxxx			
	1	0	0	0	1	0	0	1	1	1	0	1	1xxxxxxxx		100010011	
	1	0	0	0	1	0	1	0	0	1	1	0	1xxxxxxxx	100010100	100010101	
	1	0	0	0	1	0	1	0	1	1	1 0	1	1xxxxxxxx 1xxxxxxxx	100010101	100010101 100010110	
	1	0	0	0	1	0	1	1	1	1	1	0	1xxxxxxxx 1xxxxxxxx	100010111	100010110	
subu	1	0	0	0	1	1	0	0	0	1	0	1	1xxxxxxxx		100011000	same as sub
	1	0	0	0	1	1	0	0	1	1	1	0	1xxxxxxxx	100011001		
	1	0	0	0	1	1	0	1	0	1	0	0	1xxxxxxxx			
	1	0	0	0	1	1	0	1	1	1	0	1	1xxxxxxxx		100011011	
	1	0	0	0	1	1	1	0	0	1	1	0	1xxxxxxxx	100011100 100011101	100011101	
	1	0	0	0	1	1	1	1	0	1 1	0	1	1xxxxxxxx 1xxxxxxxx	100011101	100011101	
	1	0	0	0	1	1	1	1	1	i	1	0	1xxxxxxxx	100011111	100011110	
and	1	0	0	1	0	0	0	0	0	1	0	0	1xxxxxxxx			R = A & B; Co = 0
0X24	1	0	0	1	0	0	0	0	1	1	0	0	1xxxxxxxx			
	1	0	0	1	0	0	0	1	0	1	0	0	1xxxxxxxx			
	1	0	0	1	0	0	0	1	1	1	0	0	1xxxxxxxx			
	1	0	0	1	0	0	1	0	0	1	0	0	1xxxxxxxx			
	1	0	0	1	0	0	1	1	0	1 1	0	1	1xxxxxxxx 1xxxxxxxx			
	1	0	0	1	0	0	1	1	1	i	0	1	1xxxxxxxx		100100111	
or	1	0	0	1	0	1	0	0	0	1	0	0	1xxxxxxxx			R = A   B; Co = 0
	1	0	0	1	0	1	0	0	1	1	0	0	1xxxxxxxx			
	1	0	0	1	0	1	0	1	0	1	0	1	1xxxxxxxx		100101010	
	1	0	0	1	0	1	0	1	1	1	0	1	1xxxxxxxx		100101011 100101100	
	1	0	0	1	0	1	1	0	1	1	0	1	1xxxxxxxx 1xxxxxxxx		100101100	
	1	0	0	1	0	1	1	1	0	1	0	1	1xxxxxxxx		100101110	
	1	0	0	1	0	1	1	1	1	1	0	1	1xxxxxxxx		100101111	
xor	1	0	0	1	1	0	0	0	0	1	0	0	1xxxxxxxx			$R = A^B$ ; $Co = 0$
	1	0	0	1	1	0	0	0	1	1	0	0	1xxxxxxxx			
	1	0	0	1	1	0	0	1	0	1	0	1	1xxxxxxxx		100110010	
	1	0	0	1	1	0	1	0	0	1	0	1	1xxxxxxxx 1xxxxxxxx		100110011 100110100	
	1	0	0	1	1	0	1	0	1	1	0	1	1xxxxxxxx		100110101	
	1	0	0	1	1	0	1	1	0	1	0	0	1xxxxxxxx			
	1	0	0	1	1	0	1	1	1	1	0	0	1xxxxxxxx			
nor	1	0	0	1	1	1	0	0	0	1	0	1	1xxxxxxxx		100111000	$R = (A \mid B)'; Co = 0$
	1	0	0	1	1	1	0	0	1	1 1	0	0	1xxxxxxxx 1xxxxxxxx		100111001	
	1	0	0	1	1	1	0	1	1	1 1	0	0	1xxxxxxxx 1xxxxxxxx			
	1	0	0	1	1	1	1	0	0	1	0	0	1xxxxxxxx 1xxxxxxxx			
	1	0	0	1	1	1	1	0	1	1	0	0	1xxxxxxxx			
	1	0	0	1	1	1	1	1	0	1	0	0	1xxxxxxxx			
	1	0	0	1	1	1	1	1	1	1	0	0	lxxxxxxxx			
slt	1	0	1	0	1	0	0	0	0	1	0	0	1xxxxxxxx	101010001		Co same as for sub/subu; R = 0
	1	0	1	0	1	0	0	0	1	1	1 0	0	1xxxxxxxx 1xxxxxxxx	101010001		
	1	0	1	0	1	0	0	1	1	1	0	0	1xxxxxxxx 1xxxxxxxx			
	1	0	1	0	1	0	1	0	0	1	1	0	1xxxxxxxx	101010100		
	1	0	1	0	1	0	1	0	1	1	1	0	1xxxxxxxx	101010101		
	1	0	1	0	1	0	1	1	0	1	0	0	1xxxxxxxx			
	1	0	1	0	1	0	1	1	1	1	1	0	1xxxxxxxx	101010111		
sltu	1	0	1	0	1	1	0	0	0	1	0	0	1xxxxxxxx			Same as for slt
	1	0	1	0	1	1	0	0	1	1	1 0	0	1xxxxxxxx	101011001		
	1	0	1	0	1	1	0	1	0	1	0	0	1xxxxxxxx 1xxxxxxxx			
	1	0	1	0	1	1	j	0	0	1	1	0	1xxxxxxxx 1xxxxxxxx	101011100		
	1	0	1	0	1	1	1	0	1	1	1	0	1xxxxxxxx	101011101		
	1	0	1	0	1	1	1	1	0	1	0	0	1xxxxxxxx			
	1	0	1	0	1	1	1	1	1	1	1	0	1xxxxxxxx	101011111		

Slice MSB					PLA input	S					1	LA outputs					
	In8	In7	In6	In5	In4	In3	In2	In1	InO	Out3	Out2	Out1	Out0				
Instruction	func5	func4	func3	func2	func1	func0	_ A	В	С	Ov	TS	slt	R	Ov PLA terms	TS PLA terms	slt PLA terms	R PLA terms
add	1	0	0	0	0	0	0	0	0	0	1	0	0		lxxxxxxxx		
	1	0	0	0	0	0	0	0	1	1	1	0	1	100000001	lxxxxxxxx		100000001
	1	0	0	0	0	0	0	1	0	0	1	0	1		1xxxxxxxx		100000010
	1	0	0	0	0	0	0	1	1	0	1	0	0		lxxxxxxxx		
	1	0	0	0	0	0	1	0	0	0	1	0	1		lxxxxxxxx		100000100
	1	0	0	0	0	0	1	0	1	0	1	0	0		lxxxxxxxx		
	1	0	0	0	0	0	1	1	0	1	1	0	0	100000110	lxxxxxxxx		
	1	0	0	0	0	0	1	1	1	0	1	0	1		lxxxxxxxx		100000111
addu	1	0	0	0	0	1	0	0	0	0	1	0	0		lxxxxxxxx		
	1	0	0	0	0	1	0	0	1	1	1	0	1	100001001	1xxxxxxxx		100001001
	1	0	0	0	0	1	0	1	0	0	1	0	1		lxxxxxxxx		100001010
	1	0	0	0	0	1	0	1	1	0	1	0	0		lxxxxxxxx		

	l 1	0	0	0	0	1	1	0	0	0	1	0	1		1xxxxxxxx		100001100
	1	0	0	0	0	1	1	0	1	0	1	0	0		lxxxxxxxx		
	1	0	0	0	0	1	1	1	0	1	1	0	0	100001110	lxxxxxxxx		
	1	0	0	0	0	1	1	1	1	0	- 1	0	1		lxxxxxxxx		100001111
sub	1	0	0	0	1	0	0	0	0	0	1	0	1		lxxxxxxxx		100010000
	1	0	0	0	1	0	0	0	1	0	1	0	0		lxxxxxxxx		
	1	0	0	0	1	0	0	1	0	0	1	0	0	100010011	1xxxxxxxx		100010011
	1	0	0	0	1	0	0	1	1	1	1	0	1 0	100010011 100010100	1xxxxxxxx		100010011
	1	0	0	0	1	0	1	0	1	0	1	0	1	100010100	1xxxxxxxx 1xxxxxxxx		100010101
	1	0	0	0	1	0	1	1	0	0	1	0	1		1xxxxxxxx		100010101
	i	0	0	0	1	0	1	1	1	0	1	0	0		lxxxxxxxx		
subu	1	0	0	0	1	1	0	0	0	0	1	0	1		lxxxxxxxx		100011000
	1	0	0	0	1	1	0	0	1	0	1	0	0		lxxxxxxxx		
	1	0	0	0	1	1	0	1	0	0	1	0	0		1xxxxxxxx		
	1	0	0	0	1	1	0	1	1	1	1	0	1	100011011	1xxxxxxxx		100011011
	1	0	0	0	1	1	1	0	0	1	1	0	0 1	100011100	1xxxxxxxx		100011101
	1	0	0	0	1 1	1	1	1	0	0	1	0	1		1xxxxxxxx 1xxxxxxxx		100011101
	1 1	0	0	0	1	1	1	1	1	0	1	0	o o		1xxxxxxxx		100011110
and	1	0	0	1	0	0	0	0	0	0	1	0	0		1xxxxxxxx		
	1	0	0	1	0	0	0	0	1	0	1	0	0		lxxxxxxxx		
	1	0	0	1	0	0	0	1	0	0	1	0	0		1xxxxxxxx		
	1	0	0	1	0	0	0	1	1	0	1	0	0		1xxxxxxxx		
	1	0	0	1	0	0	1	0	0	0	1	0	0		lxxxxxxxx		
	1	0	0	1	0	0	1	0	1	0	1	0	0		1xxxxxxxx		100100110
	1	0	0	1	0	0	1	1	0	0	1	0	1		1xxxxxxxx 1xxxxxxxx		100100110 100100111
or	i	0	0	1	0	1	0	0	0	0	1	0	0		1xxxxxxxx		100100111
-	1	0	0	1	0	1	0	0	1	0	1	0	0		lxxxxxxxx		
	1	0	0	1	0	1	0	1	0	0	1	0	1		1xxxxxxxx		100101010
	1	0	0	1	0	1	0	1	1	0	1	0	1		lxxxxxxxx		100101011
	1	0	0	1	0	1	1	0	0	0	1	0	1		lxxxxxxxx		100101100
	1	0	0	1	0	1	1	0	1	0	1	0	1		lxxxxxxxx		100101101
	1	0	0	1	0	1	1	1	0	0	1	0	1		1xxxxxxxx 1xxxxxxxx		100101110 100101111
xor	1	0	0	1	1	0	0	0	0	0	1	0	0		1xxxxxxxx		100101111
ALL		0	0	1	1	0	0	0	1	0	1	0	0		1xxxxxxxx		
	1	0	0	1	1	0	0	1	0	0	1	0	1		1xxxxxxxx		100110010
	1	0	0	1	1	0	0	1	1	0	1	0	1		lxxxxxxxx		100110011
	1	0	0	1	1	0	1	0	0	0	1	0	1		1xxxxxxxx		100110100
	1	0	0	1	1	0	1	0	1	0	1	0	1		lxxxxxxxx		100110101
	1	0	0	1	1	0	1	1	0	0	1	0	0		1xxxxxxxx		
nor	1	0	0	1	1	1	0	0	0	0	1	0	1		1xxxxxxxx 1xxxxxxxx		100111000
	l i	0	0	1	1	1	0	0	1	0	1	0	1		1xxxxxxxx		100111001
	1	0	0	1	1	1	0	1	0	0	1	0	0		1xxxxxxxx		
	1	0	0	1	1	1	0	1	1	0	1	0	0		1xxxxxxxx		
	1	0	0	1	1	1	1	0	0	0	1	0	0		1xxxxxxxx		
	1	0	0	1	1	1	1	0	1	0	1	0	0		1xxxxxxxx		
	1	0	0	1	1	1	1	1	0	0	1	0	0		1xxxxxxxx 1xxxxxxxx		
slt	1	0	1	0	1	0	0	0	0	0	1	1	0		1xxxxxxxx 1xxxxxxxx	101010000	
	1	0	1	0	1	0	0	0	1	0	1	0	0		1xxxxxxxx	101010000	
	1	0	1	0	1	0	0	1	0	0	1	0	0		1xxxxxxxx		
	1	0	1	0	1	0	0	1	1	0	1	0	0		1xxxxxxxx		
	1	0	1	0	1	0	1	0	0	0	1	1	0		lxxxxxxxx	101010100	
	1	0	1	0	1	0	1	0	1	0	1	1	0		1xxxxxxxx	101010101	
	1	0	1	0	1	0	1	1	0	0	1	1	0		1xxxxxxxx	101010110	
situ	1	0	1	0	1	0	0	0	0	0	1	1	0		1xxxxxxxx 1xxxxxxxx	101011000	
anu	1	0	1	0	1	1	0	0	1	0	1	0	0		lxxxxxxxx	101011000	
	1	0	1	0	1	1	0	1	0	0	1	1	0		1xxxxxxxx	101011010	
	1	0	1	0	1	1	0	1	1	0	1	1	0		1xxxxxxxx	101011011	
	1	0	1	0	1	1	1	0	0	0	1	0	0		1xxxxxxxx		
	1	0	1	0	1	1	1	0	1	0	1	0	0		1xxxxxxxx		
	1	0	1	0	1	1	1	1	0	0	1	1	0		1xxxxxxxx	101011110	
	1	0	1	0	1	1	1	1	1	0	111	0	0		lxxxxxxxx		

Slice 0															
	In8	In7	In6	In5	In4	In3	In2	In1	InO	Out2	Out1	Out0			
Instruction	func5	func4	func3	func2	func1	func0	, A	В	slt	TS	Co	R	TS PLA terms	Co PLA terms	R PLA terms
add	1	0	0	0	0	0	0	0	0	1	0	0	lxxxxxxxx		
	1	0	0	0	0	0	0	0	1	1	0	0	lxxxxxxxx		
	1	0	0	0	0	0	0	1	0	1	0	1	lxxxxxxxx		100000010
	1	0	0	0	0	0	0	1	1	1	0	1	1xxxxxxxxx		100000011
	1	0	0	0	0	0	1	0	0	1	0	1	1xxxxxxxxx		100000100
	1	0	0	0	0	0	1	0	1	1	0	1	1xxxxxxxx		100000101
	1	0	0	0	0	0	1	1	0	1	1	0	lxxxxxxxx	100000110	
	1	0	0	0	0	0	1	11	1	1	11	0	1xxxxxxxx	100000111	
addu	1	0	0	0	0	1	0	0	0	1	0	0	1xxxxxxxx		
	1	0	0	0	0	1	0	0	1	1	0	0	1xxxxxxxx		
	1	0	0	0	0	1	0	1	0	1	0	1	1xxxxxxxx		100001010
	1	0	0	0	0	1	0	1	1	1	0	1	1xxxxxxxx		100001011
	1	0	0	0	0	1	1	0	0	1	0	1	1xxxxxxxx		100001100
	1	0	0	0	0	1	1	0	1	1	0	1	1xxxxxxxx		100001101
	1	0	0	0	0	1	1	1	0	1	1	0	1xxxxxxxx	100001110	
	1	0	0	0	0	1	1	1	1	1	1	0	1xxxxxxxx	100001111	
sub	1	0	0	0	1	0	0	0	0	1	1	0	lxxxxxxxx	100010000	
	1	0	0	0	1	0	0	0	1	1	1	0	lxxxxxxxx	100010001	
	1	0	0	0	1	0	0	1	0	1	0	1	lxxxxxxxx		100010010
	1	0	0	0	1	0	0	1	1	1	0	1	1xxxxxxxx		100010011
	1	0	0	0	1	0	1	0	0	1	1	1	lxxxxxxxx	100010100	100010100
	1	0	0	0	1	0	1	0	1	1	1	1	lxxxxxxxx	100010101	100010101
	1	0	0	0	1	0	1	1	0	1	1	0	lxxxxxxxx	100010110	
	1	0	0	0	1	0	1	1	1	1	1	0	lxxxxxxxx	100010111	
subu	1	0	0	0	1	1	0	0	0	1	1	0	1xxxxxxxx	100011000	
	1	0	0	0	1	1	0	0	1	1	1	0	1xxxxxxxx	100011001	
	1	0	0	0	1	1	0	1	0	1	0	1	1xxxxxxxx		100011010
	1	0	0	0	1	1	0	1	1	1	0	1	1xxxxxxxx		100011011
	1	0	0	0	1	1	1	0	0	1	1	1	1xxxxxxxx	100011100	100011100
	1	0	0	0	1	1	1	0	1	1	1	1	1xxxxxxxx	100011101	100011101
	1	0	0	0	1	1	1	1	0	1	1	0	1xxxxxxxx	100011110	
	•														

	1 1	0	0	0	1	1	l 1	1	1	1 1	1	0	1xxxxxxxx	100010111	
and	1	0	0	1	0	0	0	0	0	1	0	0	1xxxxxxxx		
	1	0	0	1	0	0	0	0	1	1	0	0	1xxxxxxxx		
	1	0	0	1	0	0	0	1	0	1	0	0	1xxxxxxxx		
	1	0	0	1	0	0	0	1	1	1	0	0	1xxxxxxxx		
	1	0	0	1	0	0	1	0	0	1	0	0	1xxxxxxxx		
	1	0	0	1	0	0	1	0	1	1	0	0	1xxxxxxxx		
	1	0	0	1	0	0	1	1	0	1	0	1	1xxxxxxxx		100100110
	1	0	0	1	0	0	1	1	1	1	0	1	1xxxxxxxx		100100111
or	1	0	0	1	0	1	0	0	0	1	0	0	1xxxxxxxx		
	1	0	0	1	0	1	0	0	1	1	0	0	1xxxxxxxx		
	1	0	0	1	0	1	0	1	0	1	0	1	1xxxxxxxx		100101010
	1	0	0	1	0	1	0	1	1	1	0	1	1xxxxxxxx		100101011
	1	0	0	1	0	1	1	0	0	1	0	1	lxxxxxxxx		100101100
	1	0	0	1	0	1	1	0	1	1	0	1	lxxxxxxxx		100101101
	1	0	0	1	0	1	1	1	0	1	0	1	lxxxxxxxx		100101110
-	1	0	0	1	0	1	1	1	1	1	0	1	lxxxxxxxx		100101111
xor	1	0	0	1	1	0	0	0	0	1	0	0	1xxxxxxxx		
	1	0	0	1	1	0	0	0	1	1	0	0	1xxxxxxxx		
	1	0	0	1	1	0	0	1	0	1	0	1	lxxxxxxxx		100110010
	1	0	0	1	1	0	0	1	1	1	0	1	1xxxxxxxx		100110011
	1	0	0	1	1	0	1	0	0	1	0	1	1xxxxxxxx		100110100
	1	0	0	1	1	0	1	0	1	1	0	1	1xxxxxxxx		100110101
	1	0	0	1	1	0	1	1	0	1	0	0	1xxxxxxxx		
	1	0	0	1	1	0	1	1	1	1	0	0	1xxxxxxxx		
nor	1	0	0	1	1	1	0	0	0	1	0	1	1xxxxxxxx		100111000
	1	0	0	1	1	1	0	0	1	1	0	1	1xxxxxxxx		100111001
	1	0	0	1	1	1	0	1	0	1	0	0	1xxxxxxxx		
	1	0	0	1	1	1	0	1	1	1	0	0	1xxxxxxxx		
	1	0	0	1	1	1	1	0	0	1	0	0	1xxxxxxxx		
	1	0	0	1	1	1	1	0	1	1	0	0	lxxxxxxxx		
	1	0	0	1	1	1	1	1	0	1	0	0	lxxxxxxxx		
	1	0	0	1	1	1	1	1	1	1	0	0	lxxxxxxxx		
sit	1	0	1	0	1	0	0	0	0	1	1	0	1xxxxxxxx	101010000	
	1	0	1	0	1	0	0	0	1	1	1	1	1xxxxxxxx	101010001	101010001
	1	0	1	0	1	0	0	1	0	1	0	0	1xxxxxxxx		
	1	0	1	0	1	0	0	1	1	1	0	1	1xxxxxxxx		101010011
	1	0	1	0	1	0	1	0	0	1	1	0	1xxxxxxxx	101010100	
	1	0	1	0	1	0	1	0	1	1	1	1	1xxxxxxxx	101010101	101010101
	1	0	1	0	1	0	1	1	0	1	1	0	1xxxxxxxx	101010110	
	1	0	1	0	1	0	1	11	1	1	1	1	1xxxxxxxx	101010111	101010111
sltu	1	0	1	0	1	1	0	0	0	1	1	0	1xxxxxxxx	101011000	
	1	0	1	0	1	1	0	0	1	1	1	1	1xxxxxxxx	101011001	101011001
	1	0	1	0	1	1	0	1	0	1	0	0	1xxxxxxxx		
	1	0	1	0	1	1	0	1	1	1	0	1	1xxxxxxxx		101011011
	1	0	1	0	1	1	1	0	0	1	1	0	1xxxxxxxx	101011100	
	1	0	1	0	1	1	1	0	1	1	1	1	1xxxxxxxx	101011101	101011101
	1	0	1	0	1	1	1	1	0	1	1	0	1xxxxxxxx	101011110	
-	1_1_	0	1	0	1	1	1	1	1	1	1	1	1xxxxxxxx	101011111	101011111