Composable Algebra with Dependencies

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Abstract

1. Introduction

Algebras can often be used with *fold* to evaluate recursive expressions. However, *fold* supports only compositional interpretations, meaning that an interpretation of a whole is determined solely from the interpretations of its parts. The compositionality of an interpretation is a significant limitation to expressivity: sometimes a 'primary' interpretation of the whole depends also on 'secondary' interpretations of its parts.

In the context of Embedded Domain Specific Languages (DSL), Jeremy Gibbons [?] proposed two approaches on F-Algebra to tackle the problems of compositionality with dependencies. We will examine the two approaches in section 4 and show that each of them has their problems. In this paper, F-Algebra will also be used as the primary representation of algebras. In section 6, we will show that the problem can be handled using other representations of algebras as well.

Contributions This paper presents

2. DSL for parallel prefix circuits

In Jeremy Gibbons's paper, parallel prefix circuit is used as am example of a DSL. To make better comparison between his and our approaches, we will also proceed on top of the DSL of circuits. Given an associative binary operator \circ , a prefix computation of width n i 0 takes a sequence x_1, x_1, \ldots, x_n of inputs and produces the sequence $x_1, x_1 \circ x_2, \ldots, x_1 \circ x_2 \circ \ldots \circ x_n$ of outputs. A parallel prefix circuit performs this computation in parallel, in a fixed format independent of the input value x_i .

Figuire one shows an example of a circuit. The inputs are fed in at the top, and the outputs fall out at the bottom. Each node represents a local computation, combining the values on each its input wires using o, in left-to-right order, and providing copies of the result on each its output wires.

Such cirucits can be represented by the following data structure:

```
data Circuit =
Identity Int
| Fan Int
| Above Circuit Circuit
| Beside Circuit Circuit
| Stretch [Int] Circuit
```

- **Identity:** *Identity n* creates a circuit consisting of n parallel wires that copy input to output.
- Fan: Fan n takes takes n inputs, and adds its first input to each of the others.
- **Above:** *Above x y* is the seires or veritical composition. It takes two circuits c and d of the same width, and connects the outputs of c to the inputs of d.

- **Beside:** Beside x y is the parallel or horizontal composition. It places c beside d, leaving them unconnected. There are no width constraints on c and d.
- **Stretch:** *Stretch ws x* takes a non-empty list of positive widths ws = [w1, ..., wn] of length n, and "stretchs" c out to width *sum ws* by interleaving some additional some additional wires. Of the first bundle of w1 inputs, the last is routed to the first input of c and the rest pass straight through; of the next bundle of w2 inputs, the last is routed to the second input of c and the rest pass straight through; and so on.

On possible construction of the Brent-Kung parallel prefix circuit in Figure 1 is:

3. F-Algebras

Alternatively, the circuit presented above can be represented using functors. The shape of the circuit is given by functor *CircuitF* as follows, where r marks the recursive spots:

```
data CircuitF r =
  IdentityF Int
    | FanF Int
    | AboveF r r
    | BesideF r r
    | StretchF [Int] r
  deriving Functor
```

We can recover the Circuit datatype from its shape functor *CircuitF*:

```
\mathbf{data}\;\mathsf{Circuit}=\mathsf{In}\;(\mathsf{CircuitF}\;\mathsf{Circuit})
```

An algebra for CircuitF consists of a type a and a function taking a CircuitF of a-values to an a-value:

```
type CircuitAlg a = CircuitF a \rightarrow a
```

Suppose we want to obtain the width of a circuit, we can pick *Width* as our evaluation target (i.e. the carrier type of *widthAlg*):

```
type Width = Int
widthAlg :: CircuitAlg Width
widthAlg (IdentityF w) = w
widthAlg (FanF w) = w
widthAlg (AboveF x y) = x
widthAlg (BesideF x y) = x + y
widthAlg (StretchF xs x) = sum xs
```

widthAlg here will give us the final evaluation result (i.e. the width) of a circuit, assuming all children of AboveF, BesideF and StretchF are already evaluated and are of type Width.

Similarly, we can define depthAlg to obtain the depth of a circuit:

```
type Depth = Int

depthAlg :: CircuitAlg Depth
depthAlg (IdentityF w) = 0
depthAlg (FanF w) = 1
depthAlg (AboveF x y) = x + y
depthAlg (BesideF x y) = x \cdot max' y
depthAlg (StretchF xs x) = x
```

Given a nested circuit, we need a fold to traverse the recursive data structure, using algebras defined earlier for evaluation at each recursive step:

```
fold :: CircuitAlg a \rightarrow Circuit \rightarrow a fold alg (In x) = alg (fmap (fold alg) x)
```

Now compositional observation functions for our circuit can be defined as:

```
width :: Circuit \rightarrow Width width = fold widthAlg depth :: Circuit \rightarrow Depth depth = fold depthAlg
```

In order to conveniently construct circuits with *CircuitF*, we define the following smart constructos:

```
\begin{split} & \text{identity} :: \mathsf{Int} \to \mathsf{Circuit} \\ & \text{identity} = \mathsf{In} \circ \mathsf{IdentityF} \\ & \text{fan} :: \mathsf{Int} \to \mathsf{Circuit} \\ & \text{fan} = \mathsf{In} \circ \mathsf{FanF} \\ & \text{above} :: \mathsf{Circuit} \to \mathsf{Circuit} \to \mathsf{Circuit} \\ & \text{above} \times \mathsf{y} = \mathsf{In} \; (\mathsf{AboveF} \times \mathsf{y}) \\ & \text{beside} :: \mathsf{Circuit} \to \mathsf{Circuit} \to \mathsf{Circuit} \\ & \text{beside} \times \mathsf{y} = \mathsf{In} \; (\mathsf{BesideF} \times \mathsf{y}) \\ & \text{stretch} :: [\mathsf{Int}] \to \mathsf{Circuit} \to \mathsf{Circuit} \\ & \text{stretch} \times \mathsf{x} \times = \mathsf{In} \; (\mathsf{StretchF} \times \mathsf{xs} \times) \\ \end{split}
```

Therefore, the Brent-Kung parallel prefix circuit in Figure 1 can be constructed as:

It can be directly evaluated by the observation functions defined earlier:

```
test1 = width circuit1
test2 = depth circuit2
```

4. Existing Approaches

To maintain the compositionality of an interpretation while bringing in dependencies, Jeremy Gibbons proposed two approaches based on F-Algebra.

4.1 Pairs for multiple interpretations with dependencies

While it is straightforward to add additional interpretaions that are independent of previously defined ones [?], adding an interpretaion

that depends on 'secondary' interpretations of its parts can be tricky.

For example, whether a circuit is well formed or not depends on the widths of its constituent parts. Since the interpretation is non-compositional [?], there is no corresponding *CircuitAlg*. To allow multiple interpretations with dependencies using algebras, Gibbons [?] proposed the following *zygomorphism* [?], making the semantic domain of the interpretation (i.e. the carrier type of the algebra) a pair:

```
\label{eq:type-wellSized} \begin{split} \text{type WellSized} &= \text{Bool} \\ \text{wswAlg :: CircuitAlg (WellSized, Width)} \\ \text{wswAlg (IdentityF w)} &= (\text{True, w}) \\ \text{wswAlg (FanF w)} &= (\text{True, w}) \\ \text{wswAlg (AboveF x y)} &= (\text{fst } \times \&\& \text{ fst y } \&\& \text{ snd } x \Rightarrow \text{ snd y, snd x}) \\ \text{wswAlg (BesideF x y)} &= (\text{fst } \times \&\& \text{ fst y, snd } x + \text{ snd y}) \\ \text{wswAlg (StretchF ws x)} &= (\text{fst } \times \&\& \text{ length ws} \Rightarrow \text{snd x, sum ws}) \end{split}
```

In this way, *fold wswAlg* is still a fold, and individual interpretations can be recovered as follows:

```
\label{eq:wellSized1} \begin{split} & \text{wellSized1} :: \mathsf{Circuit} \to \mathsf{WellSized} \\ & \text{wellSized1} \ \mathsf{x} = \mathsf{fst} \ (\mathsf{fold} \ \mathsf{wswAlg} \ \mathsf{x}) \\ & \text{width1} :: \mathsf{Circuit} \to \mathsf{Width} \\ & \text{width1} \ \mathsf{x} = \mathsf{snd} \ (\mathsf{fold} \ \mathsf{wswAlg} \ \mathsf{x}) \end{split}
```

4.2 Church encoding for multiple interpretations

From the previous section we can see that it is possible to provide dependent interpretaions by pairing semantics up and projecting the desired interpretation from the tuple. However, it is still clumsy and not modular: existing code needs to be revised every time a new interpretations is added. Moreover, for more than two interpretations, we have to either create a combination for each pair of interpretations, or use tuples which generally lack good language support.

Therefore, Gibbons [?] presented a single parametrized interpretation, which provides a universal generic interpretation as the *Church encoding*:

```
\label{eq:newtype Circuit1} \begin{split} & \text{newtype Circuit1} = \text{C1 } \big\{ \text{unC1} :: \forall \text{a.CircuitAlg a} \rightarrow \text{a} \big\} \\ & \text{identity1 w} = \text{C1 } \big( \lambda \text{alg} \rightarrow \text{alg (IdentityF w)} \big) \\ & \text{fan1 w} & = \text{C1 } \big( \lambda \text{alg} \rightarrow \text{alg (FanF w)} \big) \\ & \text{above1 } \times \text{y} = \text{C1 } \big( \lambda \text{alg} \rightarrow \text{alg (AboveF (unC1 \times \text{alg) (unC1 y alg))} \big)} \\ & \text{beside1 } \times \text{y} = \text{C1 } \big( \lambda \text{alg} \rightarrow \text{alg (BesideF (unC1 \times \text{alg) (unC1 y alg))} \big)} \\ & \text{stretch1 ws } \times = \text{C1 } \big( \lambda \text{alg} \rightarrow \text{alg (StretchF ws (unC1 \times \text{alg))} \big)} \\ & \text{It can then specialize to } width \text{ and } depth: \\ & \text{width2} :: \text{Circuit1} \rightarrow \text{Width} \\ & \text{width2} \times = \text{unC1} \times \text{widthAlg} \\ & \text{depth2} :: \text{Circuit1} \rightarrow \text{Depth} \\ & \text{depth2} \times = \text{unC1} \times \text{depthAlg} \end{split}
```

However, one big problem with the above church encoding approach is that it does not support dependent interpretations.

5. Composable Algebras

Our first goal is to compose algebras modularly. It will work as the foundation for letting us bring in dependent interpretions in later sections. By composing two algebras together, we can get a new algebra with a carrier type containing the types of its components. In this section, we will use algebras for *width* and *depth* as examples and show how we can compose the two together.

Since a composed algebra has a composed carrier type, instead of using Width and Depth defined earlier to represent the semantic domain of each interpretation, we make use of the newtype wrapper to allow multiple interpretations over the same underlying type:

```
newtype Width2 = Width2 { width :: Int }
newtype Depth2 = Depth2 { depth :: Int }
```

Algebras can be defined in the same way as before:

```
widthAlg2 :: CircuitAlg Width2
widthAlg2 (IdentityF w) = Width2 w
widthAlg2 (FanF w)
                       = Width2 w
widthAlg2 (AboveF \times y) = Width2 (width x)
widthAlg2 (BesideF x y) = Width2 (width x + width y)
widthAlg2 (StretchF xs x) = Width2 (sum xs)
depthAlg2 :: CircuitAlg Depth2
depthAlg2 (IdentityF w) = Depth2 0
depthAlg2 (FanF w)
                       = Depth2 1
depthAlg2 (AboveF \times y) = Depth2 (depth \times + depth y)
depthAlg2 (BesideF x y) = Depth2 (depth x 'max' depth y)
depthAlg2 (StretchF xs x) = Depth2 (depth x)
```

Next we introduce the following type class to state a membership relationship between type i and e:

```
class i:<: e where
   inter :: e \rightarrow i
```

Here i :<: e means that type i is a component of a larger collection of types represented by e, and gives the corresponding projection functions:

```
instance i:<: i where
  inter = id
instance i :<: (Compose i i2) where
  inter = fst
instance (i :<: i2) \Rightarrow i :<: (Compose i1 i2) where
  inter = inter \circ snd
```

To actually compose two algebras together, we define the operator (<+>):

```
type Compose i1 i2 = (i1, i2)
```

```
(<+>) :: CircuitAlg a 
ightarrow CircuitAlg b 
ightarrow CircuitAlg (Compose a ()) a circuit. Therefore, wsAlg can be defined as follows:
(\langle + \rangle) a1 a2 (IdentityF w) = (a1 (IdentityF w), a2 (IdentityF w))
(<+>) a1 a2 (FanF w)
                              = (a1 (FanF w), a2 (FanF w))
(<+>) a1 a2 (AboveF x y) =
  (a1 (AboveF (inter x) (inter y)), a2 (AboveF (inter x) (inter y)))
(<+>) a1 a2 (BesideF x y) =
  (a1 (BesideF (inter x) (inter y)), a2 (BesideF (inter x) (inter y)))
(<+>) a1 a2 (StretchF xs x) =
  (a1 (StretchF xs (inter x)), a2 (StretchF xs (inter x)))
```

(<+>) takes two algebras with carrier types a and b as inputs and gives back an algebra with a composed carrier type (Compose a b). For *AboveF*, *BesideF* and *StretchF*, their children x and y are of type e, where Width2:<: e and WellSized2:<: e. In the output tuple, (inter x) and (inter y) will have types corresponding to the carrier type of a1 and a2 respectively.

Now it is straightforward to compose algebras together:

```
cAlg = widthAlg2 < + > depthAlg2
```

cAlg is composed of widthAlg2 and depthAlg2, with a carrier type of (Compose Width2 Depth2).

We can define the evaluation function of our circuit as a fold:

```
eval = fold cAlg
```

To retrieve a target evaluation type from a composed type, we define gwidth and gdepth:

```
gwidth :: (Width2 :<: e) \Rightarrow e \rightarrow Int
\mathsf{gwidth} = \mathsf{width} \circ \mathsf{inter}
gdepth :: (Depth2 :<: e) \Rightarrow e \rightarrow Int
gdepth = depth \circ inter
```

Individual interpretations can be defined as:

```
width3:: Circuit \rightarrow Int
width 3 = gwidth \circ eval
depth3 :: Circuit \rightarrow Int
depth3 = gdepth \circ eval
```

They can be used to evaluate the Brent-Kung parallel prefix circuit defined in section 3:

```
test1 = width3 circuit1
test2 = depth3 circuit1
```

6. Dependent Algebras

In the previous section we talked about how algebras can be composed together to allow multiple interpretations. In this section, we will introduce an approach that allows multiple interpretations with dependencies. With our approach, each property we want to evaluate has a corresponding algebra. There is no need to construct a pair of interpretations when one depends on the other. For example, unlike wswAlg in section 4.1, we have wsAlg that corresponds to wellSized, where the definition of widthAlg is no longer needed.

The first step is to change our definition of alegebra from CircuitAlg to GAlg:

```
\mathbf{type}\ \mathsf{GAlg}\ \mathsf{r}\ \mathsf{a} = \mathsf{CircuitF}\ \mathsf{r} \to \mathsf{a}
```

GAIg stands for *generic algebra*. It consists of two types r and a, and a function taking CiruictF of r-vlaues to an a-value, where a:<: r. For wsAlg, the first type r represents a collection of types containing both WellSized2 and Width2 (specified by (WellSized2:<: r, Width2 :<: r)). Since each child of AboveF, BesideF and StretchF is of type r, gwidth can be used to retrieve the width

```
newtype WellSized2 = WellSized2 { wellSized :: Bool }
```

```
wsAlg :: (WellSized2 :<: r, Width2 :<: r) \Rightarrow GAlg r WellSized2
wsAlg (IdentityF w) = WellSized2 True
                     = WellSized2 True
wsAlg (FanF w)
wsAlg (AboveF \times y) =
  WellSized2 (gwellSized \times && gwellSized y && gwidth x == gwidth y)
wsAlg (BesideF \times y) =
  WellSized2 (gwellSized x && gwellSized y)
wsAlg (StretchF xs x) =
  WellSized2 (gwellSized \times && length xs == gwidth x)
```

Since Width2 needs to be part of the carrier type of wsAlg such that we can retreive the width of a circuit and test if it is well-formed, we need to compose widthAlg2 and wsAlg together for evaluation. While the (<+>) operator is very similar to the one defined in the previous section, we need to specify the relationships between types of algebras we are compsoing. Given an algebra from type r to type a, and another from type r to type b, where r contains both a and b, it gives back a new algebra from type r to type (Compose a b).

```
(<+>) :: (a:<:r,b:<:r) \Rightarrow GAlg r a \rightarrow GAlg r b \rightarrow
  GAlg r (Compose a b)
(<+>) a1 a2 (IdentityF w) =
  (a1 (IdentityF w), a2 (IdentityF w))
(<+>) a1 a2 (FanF w)
  (a1 (FanF w), a2 (FanF w))
(<+>) a1 a2 (AboveF x y) =
(<+>) a1 a2 (BesideF x y) =
  (a1 (BesideF (inter x) (inter y)), a2 (BesideF (inter x) (inter y)))
(<+>) a1 a2 (StretchF xs x) =
  (a1 (StretchF xs (inter x)), a2 (StretchF xs (inter x)))
```

Now we can define cAlg2 that is composed of widthAlg2 and

```
cAlg2 = widthAlg2 < + > wsAlg
```

With observation functions width2 and wellSized2 defined as:

```
width2:: Circuit \rightarrow Int
width2 x = gwidth (fold cAlg2 x)
\mathsf{wellSized2} :: \mathsf{Circuit} \to \mathsf{Bool}
wellSized2 x = gwellSized (fold cAlg2 x)
```

Extensibility in Both Dimensions

So far we have only talked about extensibility in one dimension, namely, how to add new observation functions in a modular way with algebras for our DSL. What if we want to have extensibility in a second dimension, which is to extend our grammer by adding new constructors modularly? To make the problem more interesting, these additional constructors may also bring dependencies in their corresponding observation functions at the same time. In this section, we will show that our approach of composing algebras while incorporating dependencies works well with the Modular Refiable Matching (MRM) approach, which allows us to add additional constructors modularly. We will present a two-level composition of algebras: for each modular component, we compose its algebras together if an interpretation is dependent; for different components, we combine their corresponding algebras together to allow evaluation of a composed data structure.

For example, say at first we only have three constructs in our DSL of circuits: Identity, Fan, and Beside. We can define a functor CircuitFB to represent this datatype, where B stands for Base:

```
data CircuitFB r =
 Identity Int
   Fan Int
   Beside r r
 deriving Functor
```

There is no dependencies involved for the algebras of this circuit, since with only *Identity*, Fan and Beside, whether a circuit is well formed or not is not dependent on the width of its parts. However, we will keep our representation for dependent algebras to be consistent with algeras we will later define for extended datatypes:

```
type GAlgB r a = CircuitFB r \rightarrow a
```

Algebras for width and wellSized are exactly the same as before:

```
widthAlgB :: (Width2 :<: r) \Rightarrow CircuitFB r \rightarrow Width2
widthAlgB (Identity w) = Width2 w
```

```
widthAlgB (Fan w)
                          = Width2 w
widthAlgB (Beside xy) = Width2 (gwidth x + gwidth y)
wsAlgB :: (Width2 :<: r, WellSized2 :<: r) \Rightarrow
\mathsf{CircuitFB}\; \mathsf{r} \to \mathsf{WellSized2}
wsAlgB (Identity w) = WellSized2 True
                      = WellSized2 True
wsAlgB (Fan w)
wsAlgB (Beside \times y) = WellSized2 (gwellSized \times \&\& gwellSized y)
```

Now suppose we want to extend our circuits by adding new con-(a1 (AboveF (inter x) (inter y)), a2 (AboveF (inter x) (inter y)) structs Above and Stretch. We add the datatype constructors as a functor CircuitFE, where E stands for Extended:

```
data CircuitFE r =
  Above r r
  | Stretch [Int] r
  deriving Functor
```

Algebras correspond to this functor are similar to the ones above. The only difference is that the interpretation for checking if a circuit is well formed now depends on the widths of its part. Same as in section 6, we use gwidth to retrieve the width of a circuit:

```
type GAlgE r a = CircuitFE r \rightarrow a
\mathsf{widthAlgE} :: (\mathsf{Width2} :<: \mathsf{r}) \Rightarrow \mathsf{CircuitFE} \ \mathsf{r} \rightarrow \mathsf{Width2}
widthAlgE (Above \times y) = Width2 (gwidth \times)
widthAlgE (Stretch xs x) = Width2 (sum xs)
wsAlgE :: (Width2 :<: r, WellSized2 :<: r) \Rightarrow
CircuitFE r \rightarrow WellSized2
wsAlgE (Above x y) =
   WellSized2 (gwellSized \times && gwellSized y && gwidth x == gwidth y)
wsAlgE (Stretch xs x) =
   WellSized2 (gwellSized \times \&\& length xs = gwidth x)
```

Unlike the <+> operator defined in previous sections, here we associate it with a type class to compose algebras correponding to different functors. With this approach, we don't have to define a different operator for algebra composition each time a new functor is added. Instead, all we have to do is to make a new instance of type class *Comb* and define the corresponding behavior of < + >. Since we have two functors CircuitFB and CircuitFE, we create two instances of *Comb* and define <+> for each of them:

```
class Comb f r a b where
  (<+>):: (f r \rightarrow a) \rightarrow (f r \rightarrow b) \rightarrow (f r \rightarrow (Compose a b))
instance (a :<: r, b :<: r) \Rightarrow Comb CircuitFB r a b where
  (<+>) a1 a2 (Identity w) = (a1 (Identity w), a2 (Identity w))
  (<+>) a1 a2 (Fan w)
                                = (a1 (Fan w), a2 (Fan w))
  (<+>) a1 a2 (Beside x y) =
     (a1 (Beside (inter x) (inter y)), a2 (Beside (inter x) (inter y)))
instance (a :<: r, b :<: r) \Rightarrow Comb CircuitFE r a b where
  (<+>) a1 a2 (Above x y) =
     (a1 (Above (inter x) (inter y)), a2 (Above (inter x) (inter y)))
  (<+>) a1 a2 (Stretch xs x) =
     (a1 (Stretch xs (inter x)), a2 (Stretch xs (inter x)))
```

A circuit with all five constructs can be built from the modular components. First we define the type of the circuit:

```
type Circuit2 = Fix'[CircuitFB, CircuitFE]
```

The type Circuit2 denotes circuits that have Identity, Fan, Beside, Above and Stretch as their components.

Since Width2 needs to be part of the carrier type of wsAlgE such that we can retreive the width of a circuit and test if it is well-formed, for CircuitFE, we need to compose widthAlgE and wsAlgE together and use compAlgE for evaluation.

```
\mathsf{compAlgE} = \mathsf{widthAlgE} < + > \mathsf{wsAlgE}
```

Then we use (:::) to combine algebras correspond to different functors together [?]. Since the algebras in the list constructed by (:::) need to have the same carrier and return type, we compose widthAlgB and wsAlgB for CircuitFB and get compAlgB:

```
compAlgB = widthAlgB < + > wsAlgB
```

The *fold* operator defined in MRM library [?] takes an fs-algebra and Fix fs arguments. We define the evaluation function for our circuit as a fold using the combined algebras:

```
eval :: Circuit2 \rightarrow Compose Width2 WellSized2 eval = fold (compAlgB ::: (compAlgE ::: Void))
```

Invidual interpretations can then be retrieved by *gwidth* and *gwell-Sized*:

```
width3 :: Circuit2 \rightarrow Int width3 = gwidth \circ eval wellSized3 :: Circuit2 \rightarrow Bool wellSized3 = gwellSized \circ eval
```

They can be used with smart constructors to evaluate a concrete circuit:

```
\begin{split} & \mathsf{circuit2} = \mathsf{above} \; (\mathsf{beside} \; (\mathsf{fan} \; 2) \; (\mathsf{fan} \; 2)) \\ & (\mathsf{above} \; (\mathsf{stretch} \; [2,2] \; (\mathsf{fan} \; 2)) \\ & (\mathsf{beside} \; (\mathsf{identity} \; 1) \; (\mathsf{beside} \; (\mathsf{fan} \; 2) \; (\mathsf{identity} \; 1)))) \\ & \mathsf{test1} = \mathsf{width3} \; \mathsf{circuit2} \\ & \mathsf{test2} = \mathsf{wellSized3} \; \mathsf{circuit2} \end{split}
```

8. Other representations of algebra

8.1 Type Class with Proxies

```
data Proxy a = Proxy
class Circuit inn out where
  \mathsf{identity} :: \mathsf{Proxy} \; \mathsf{inn} \to \mathsf{Int} \to \mathsf{out}
          :: \mathsf{Proxy} \; \mathsf{inn} \to \mathsf{Int} \to \mathsf{out}
  \mathsf{above} \; :: \mathsf{inn} \; \to \mathsf{inn} \to \mathsf{out}
  \mathsf{beside} \, :: \mathsf{inn} \, \to \mathsf{inn} \to \mathsf{out}
  \mathsf{stretch} :: [\mathsf{Int}] \to \mathsf{inn} \to \mathsf{out}
instance (Circuit inn Width2, Width2:<: inn) ⇒
  Circuit inn Width2 where
  identity \; (Proxy :: Proxy \; inn) \; w = Width2 \; w
  fan (Proxy :: Proxy inn) w = Width2 w
  above x y = Width2 (gwidth x)
  beside x y = Width2 (gwidth x + gwidth y)
  stretch \times s \times = Width2 (sum \times s)
instance (Circuit inn WellSized2.
  Width2:<: inn.
  WellSized2 :<: inn) ⇒ Circuit inn WellSized2 where
  identity (Proxy :: Proxy inn) w = WellSized2 True
  fan (Proxy :: Proxy inn) w = WellSized2 True
  above x y = WellSized2 (gwellSized x \&\& gwellSized y \&\&
```

```
gwidth x == gwidth y)
      beside x y = WellSized2 (gwellSized x \&\& gwellSized y)
      stretch xs x = WellSized2 (gwellSized x &&
                    length xs == gwidth x)
    instance (Circuit inn inn1, Circuit inn inn2) ⇒
      Circuit inn (Compose inn1 inn2) where
      identity (Proxy :: Proxy inn) w =
           ((identity (Proxy :: Proxy inn) w) :: inn1,
              (identity (Proxy :: Proxy inn) w) :: inn2)
      fan (Proxy :: Proxy inn) w =
           ((fan (Proxy :: Proxy inn) w) :: inn1,
              (fan (Proxy :: Proxy inn) w) :: inn2)
      above x y = ((above x y) :: inn1, (above x y) :: inn2)
      beside x y = ((beside x y) :: inn1, (beside x y) :: inn2)
      stretch xs x = ((stretch xs x) :: inn1, (stretch xs x) :: inn2)
    type ComposedType = Compose Width2 WellSized2
    gfan w
      fan (Proxy :: Proxy ComposedType) w :: ComposedType
    gidentity w =
      identity (Proxy :: Proxy ComposedType) w :: ComposedType
    gbeside \times y = (beside \times y) :: ComposedType
    gabove \times y = (above \times y) :: ComposedType
   gstretch xs x = (stretch xs x) :: ComposedType
   c = (gfan 2 'gbeside' gfan 2) 'gabove'
      gstretch [2, 2] (gfan 2) 'gabove'
      (gidentity 1 'gbeside' gfan 2 'gbeside' gidentity 1)
   width4 :: (Width2 :<: e) \Rightarrow e \rightarrow Int
   width4 = gwidth
   \mathsf{wellSized4} :: (\mathsf{WellSized2} :<: e) \Rightarrow \mathsf{e} \rightarrow \mathsf{Bool}
   \mathsf{wellSized4} = \mathsf{gwellSized}
8.2 Records
    data Circuit inn out = Circuit {
      identity :: Int \rightarrow out,
             :: \mathsf{Int} \to \mathsf{out},
      above :: inn \rightarrow inn \rightarrow out,
      beside :: inn \rightarrow inn \rightarrow out,
      stretch :: [Int] \rightarrow inn \rightarrow out
   widthAlg :: (Width2 :<: inn) ⇒ Circuit inn Width2
   widthAlg = Circuit {
      identity = \lambda w \rightarrow Width2 w,
      fan = \lambda w \rightarrow Width2 w,
      above = \lambda x y \rightarrow Width2 (gwidth x),
      beside = \lambda x y \rightarrow \text{Width2} (gwidth x + gwidth y),
      stretch = \lambda xs x \rightarrow Width2 (sum xs)
   wsAlg :: (Width2 : <: inn, WellSized2 : <: inn) \Rightarrow
      Circuit inn WellSized2
```

wsAlg = Circuit {

identity = $\lambda w \rightarrow WellSized2$ True,

 $= \lambda w \rightarrow WellSized2 True,$

above $= \lambda x y \rightarrow WellSized2$ (gwellSized x && gwellSized y &&

 $gwidth \times = gwidth y),$

```
beside = \lambda x y \rightarrow \text{WellSized2} (gwellSized x \&\& \text{ gwellSized } y),
   stretch = \lambda xs x \rightarrow WellSized2 (gwellSized x \&\&
                       length xs == gwidth x)
}
(<+>)::(inn1:<:inn,inn2:<:inn) \Rightarrow
   Circuit inn inn1 \rightarrow Circuit inn inn2 \rightarrow
   Circuit inn (Compose inn1 inn2)
(<+>) a1 a2 = Circuit {
   identity = \lambda w \rightarrow (identity a1 w, identity a2 w),
   fan = \lambda w \rightarrow (fan a1 w, fan a2 2),
   above = \lambda x y \rightarrow (above a1 (inter x) (inter y),
                       above a2 (inter x) (inter y)),
   beside = \lambda x y \rightarrow (beside a1 (inter x) (inter y),
                       beside a2 (inter x) (inter y)),
  stretch = \lambda xs x \rightarrow (stretch a1 xs (inter x),
                       stretch a2 xs (inter x))
}
cAlg :: Circuit (Compose Width2 WellSized2)
   (Compose Width2 WellSized2)
\mathsf{cAlg} = \mathsf{widthAlg} < + > \mathsf{wsAlg}
cidentity = identity cAlg
cfan = fan cAlg
cabove = above cAlg
cbeside = beside cAlg
\mathsf{cstretch} = \mathsf{stretch} \; \mathsf{cAlg}
c = (cfan \ 2 \ 'cbeside' \ cfan \ 2) \ 'cabove'
   \mathsf{cstretch}\ [2,2]\ (\mathsf{cfan}\ 2) 'cabove'
   (cidentity 1 'cbeside' cfan 2 'cbeside' cidentity 1)
```

9. Related Work

10. Conclusion