



POWER

IIC

SPI_FLASH

2.4G_WARE

USER_LED

USB2.0

CPU

STM32F103ZET6 V1.2

KEY

JTAG

BOOT_MODE

MODE	BOOT0	BOOT1
FLASH	0	X
ISP	1	0
SRAM	1	1

POWER_LED

BOOT_SEL

BEEP

BATTERY

Title		
Sta	Number	Revision
E		
Date	19 Dec 2016	Sheet of
File	F:\STM32F103ZET6\STM32F103ZET6_V1.2\PCB\PCB.dwg	Sheet of 1