CSE 341 Microprocessors

Spring 2023

Quiz 1

Marks: 10

ANSWER ALL THE QUESTIONS   Time: 20 mins

Name_____ ID_____

Section _____

C01Q1. a. State the advantage of pipelining. [1]

      b. Consider the instruction ADD AH, BH. Suppose the processor incorrectly performed SUB BH, AH. Which part of the architecture do you think is responsible for the error? [1].

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C02 Q2 a.. Find the **5th largest** and the **5th smallest** possible segment numbers and logical addresses (**segment: offset** pair) for the given physical address - **23432h [4]**

      b. Write two different logical addresses (segment: offset pair) for **58F32h [4]**