Revision	Date	Author	Description
1	06/07/2021	M. Cossali	First draft.
2	18/10/2021	G.Boschini	- Connected R23,R25 to LED_RED net instead of VIO - Changed R23, R25 to 1k - Changed R24, R26 to 10k - Changed CE2 pin of U1 to pulled up by LED_RED instead of VBAT - Added I2C isolation for BME sensor - Changed L14 to LQW18AN12NG80 and L15 to LQW15AN4N5G80D following Ignion MN1 values - Updated J1 and J2 pinout - Updated L4, R19 and C32 values following feedback from ST - Added R36 to allocate footprint for DC blocking capacitor - Updated J5 and J6 P/N - Added STDC14 connections to J5 and J6 - Added TAKACHI enclosure and screws

ASS1
TWF5-3-9W

ASS2
KNOB M2.3x6-INOX

ASS3
KNOB M2.3x6-INOX

ASS4
KNOB M2.3x6-INOX

ASS5
KNOB M2.3x6-INOX

ASS6
KNOB M2.6x12-INOX

ASS7
KNOB M2.6x12-INOX

ASS8
KNOB M2.6x12-INOX

ASS9
KNOB M2.6x12-INOX

ASS10
MH3080-1

RF SHIELD 20x30mm

OBJ1
NU

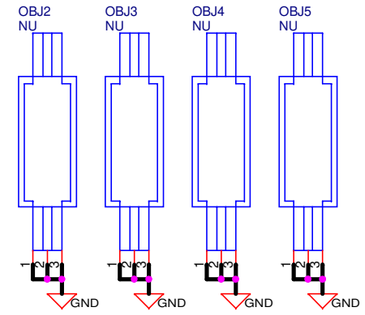
RF SHIELD CLIPs

OBJ2
NU

OBJ3
NU

OBJ4
NU

OBJ5
NU



CS1
CS V2 4L 35um

FID1 FID2 FID3 FID4

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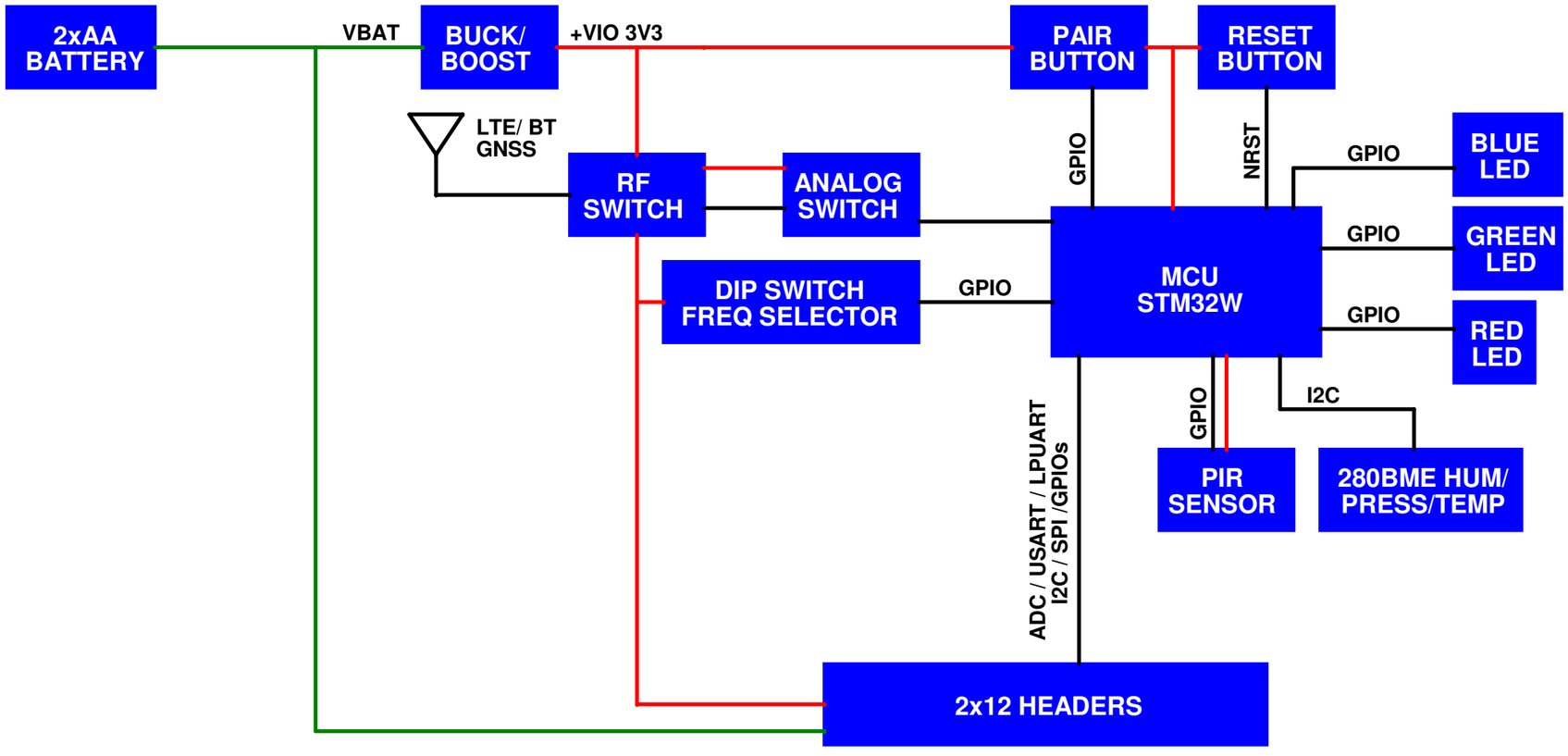
Board	**REFERENCE SENSOR BOARD**		
Project Code	**2021-1494**	Page name	**00 - REVISION**
Internal Code	**-**	Code	-
Data: Monday, November 15, 2021	Rev. **1**	Rev. changes: See Revision Page	Sheet **1 / 4**



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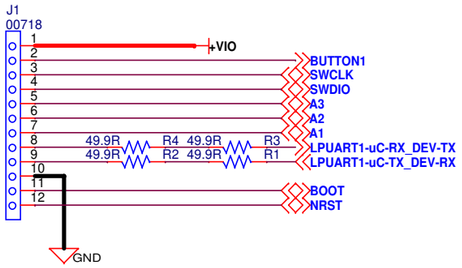
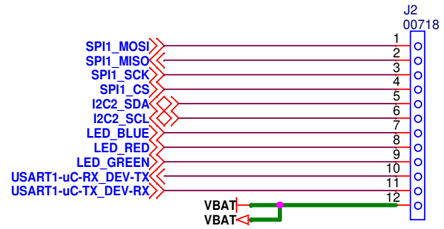
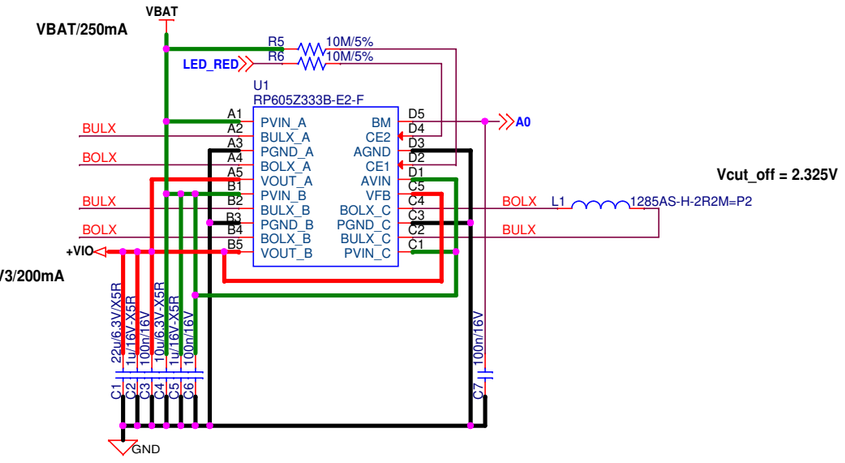
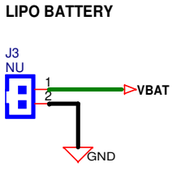
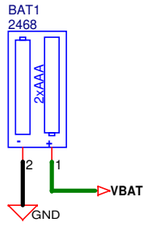
	Project Code **2021-1494** Internal Code **-**	Page name **01 - BLOCK DIAGRAM** Code **-**	
Data Friday, November 05, 2021	Rev. **1**	Rev. changes See Revision Page	Sheet **2 / 4**

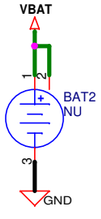


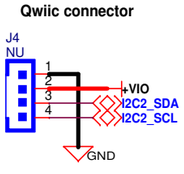












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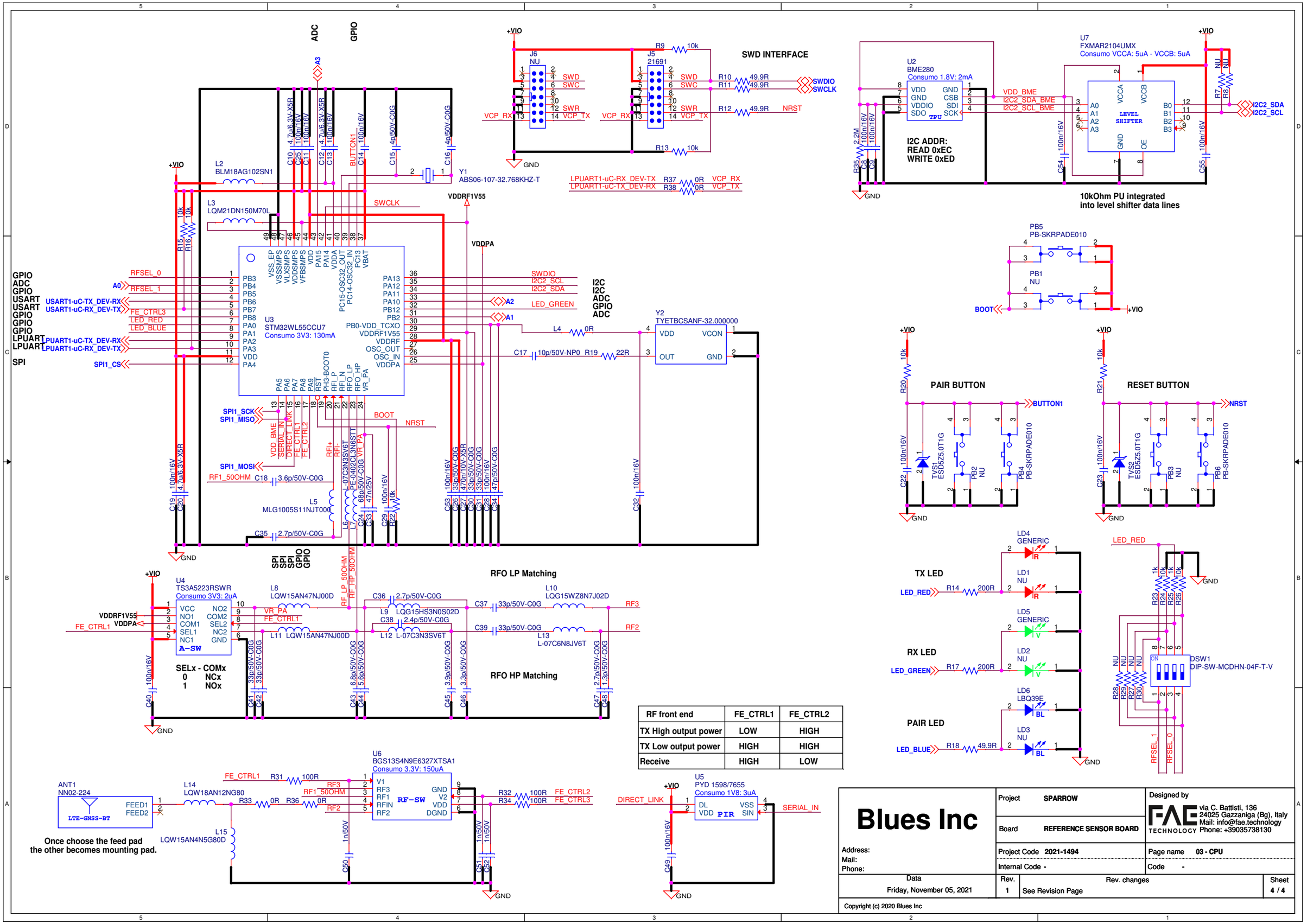

RF front end	FE_CTRL1	FE_CTRL2
TX High output power	LOW	HIGH
TX Low output power	HIGH	HIGH
Receive	HIGH	LOW

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Board	REFERENCE SENSOR BOARD
Project Code	2021-1494
Internal Code	-
Page name	03 - CPU
Code	-

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Friday, November 05, 2021	1	See Revision Page	4 / 4