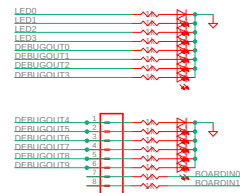
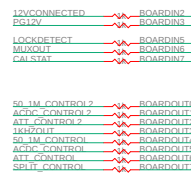




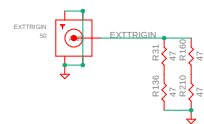
2.5V outputs from
the FPGA for debugging, etc.
These go to LEDs for monitoring



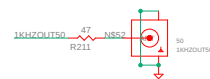
2.5V inputs and outputs to/from
the FPGA for status monitoring
and control of things, etc.



50 Ohm external trigger input
(was BOARDIN4)



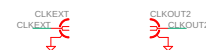
50 Ohm 1kHz / Aux output
(was DEBUGOUT10)



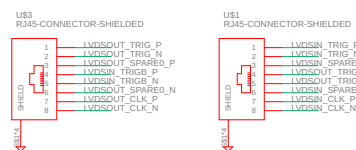
1kOhm 1kHz output for probe compensation



Extra clock input and output

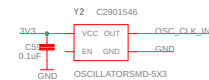


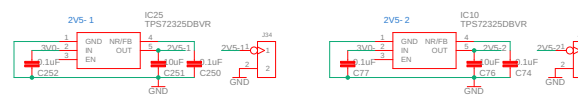
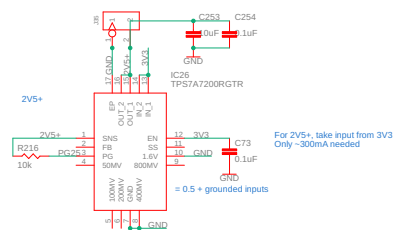
LVDS outputs and inputs for sync between boards



Cyclone IV E left and right I/O banks support
true LVDS transmitters, so use them for LVDS outputs

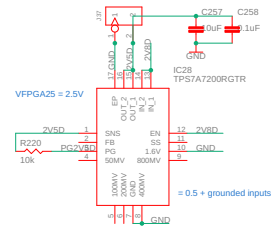
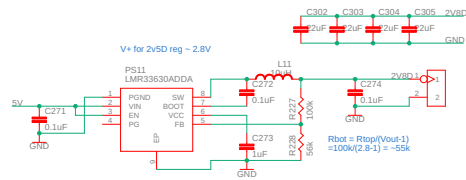
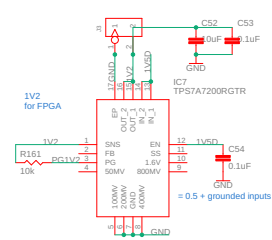
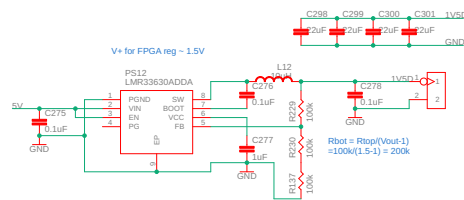
50 MHz clock for FPGA



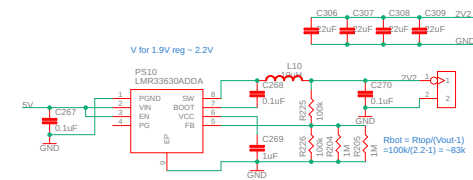
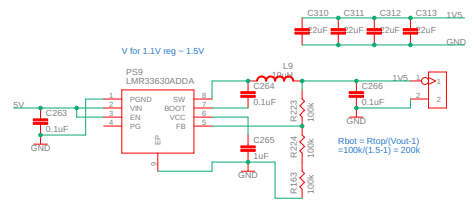
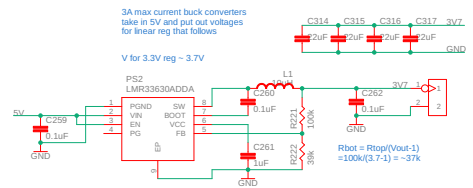
[illegible]

12/24/2024 6:06 PM f=0.47 C:\gitwork\master\HaasoscopePro\adc board\haasoscope_pro_adc_fpga_board.sch (Sheet: 11/16)

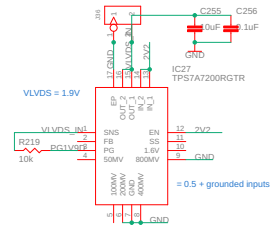
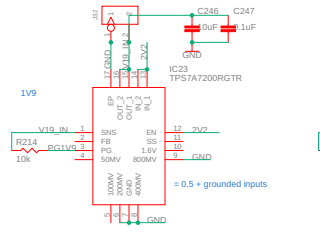
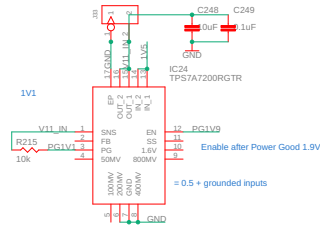
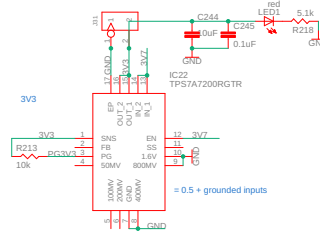
Need some power for the FPGA



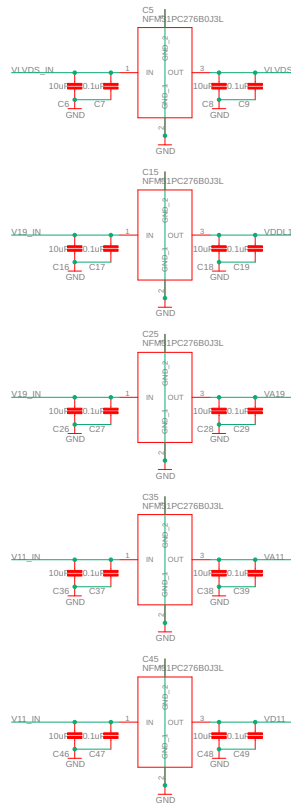
Need 3v3 1v1 and 1v9 for the main ADC



2A max current linear regs with 180mV dropout at 2A



ADC power filters, as recommended in datasheet



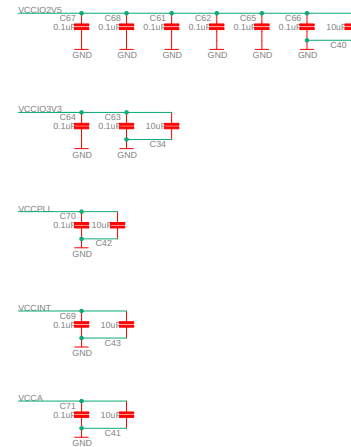
ADC power decoupling

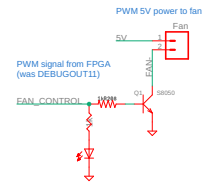
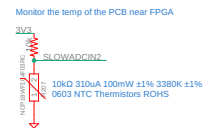
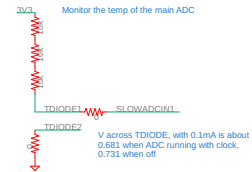
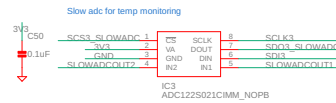
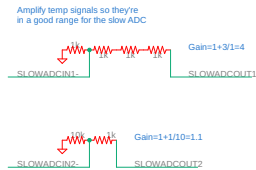


These transfer power to actual FPGA voltage inputs



FPGA power decoupling





Some extra things to put on
the board for testing only
Not connected to the rest of the system!

