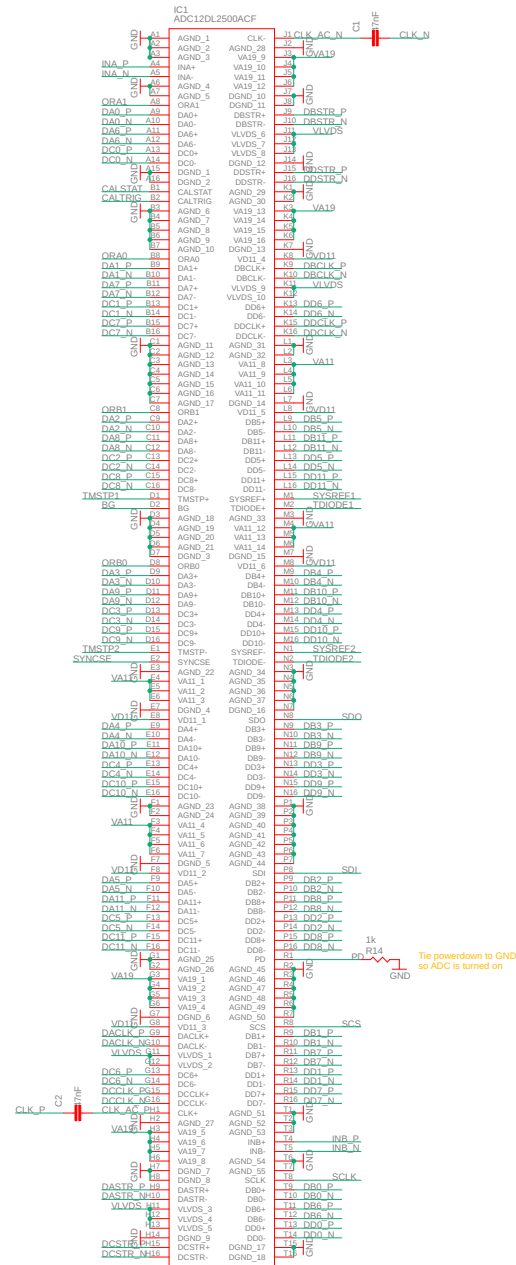
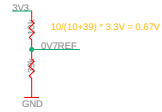
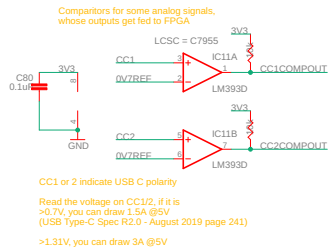
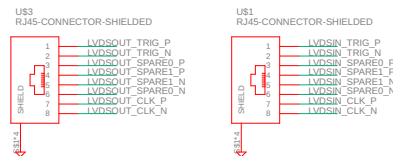
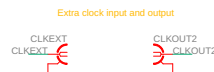
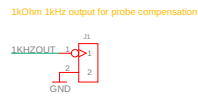
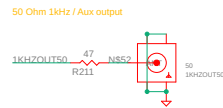
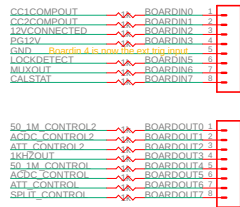
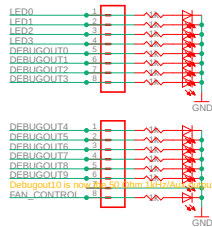


All the connections to the main ADC
(Yes, the schematic symbol is terrible.)

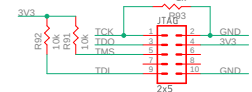
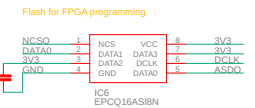


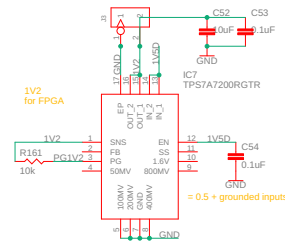
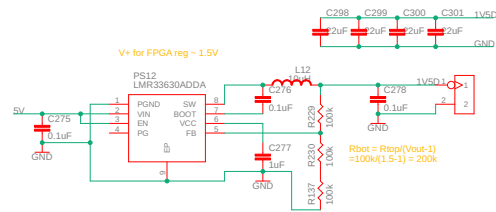
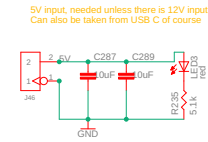
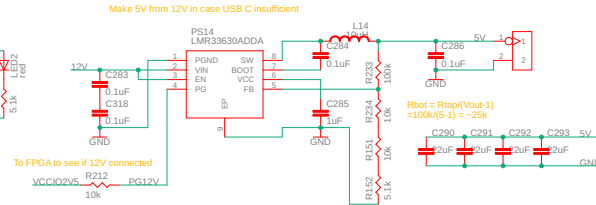
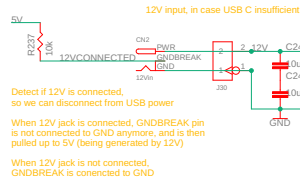
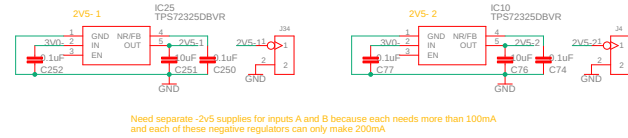
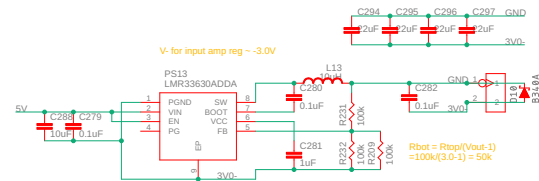


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GND	F12	GND_14	IO 228	IO 228	IO 228
BOARDOUT14	IO 88	IO 229	IO 229	IO 229	IO 229
BOARDOUT14	IO 89	IO 230	IO 230	IO 230	IO 230
IO90	F15	IO 90	IO 231	IO 231	IO 231
IO91	F16	IO 91	IO 232	IO 232	IO 232
IO92	F17	IO 92	IO 233	IO 233	IO 233
VCCA	F18	VCCA2	IO 234	IO 234	IO 234
IO93	F19	IO 93	IO 235	IO 235	IO 235
IO94	F20	IO 94	IO 236	IO 236	IO 236
LVDSIN_S000	IO 95	IO 237	IO 237	IO 237	IO 237
LVDSIN_S000	IO 96	IO 238	IO 238	IO 238	IO 238
CLK1	G1	CLK1	IO 239	IO 239	IO 239
GND	G2	GND_15	IO 240	IO 240	IO 240
LVDSOUT_S000	IO 97	IO 241	IO 241	IO 241	IO 241
IO98	G3	IO 98	IO 242	IO 242	IO 242
VCCA	G4	VCCA3	IO 243	IO 243	IO 243
SCS3_IN1	IO 100	IO 244	IO 244	IO 244	IO 244
SCS3_IN2	IO 101	IO 245	IO 245	IO 245	IO 245
FTDI_DATA0	IO 102	IO 246	IO 246	IO 246	IO 246
SCS3_CLK1	IO 103	IO 247	IO 247	IO 247	IO 247
VCCINT	IO 104	IO 248	IO 248	IO 248	IO 248
IO105	IO 105	IO 249	IO 249	IO 249	IO 249
IO106	IO 106	IO 250	IO 250	IO 250	IO 250
IO107	IO 107	IO 251	IO 251	IO 251	IO 251
IO108	IO 108	IO 252	IO 252	IO 252	IO 252
IO109	IO 109	IO 253	IO 253	IO 253	IO 253
IO110	IO 110	IO 254	IO 254	IO 254	IO 254
VCCIO2V5H10	VCCIO2	IO 255	IO 255	IO 255	IO 255
GND	GND_16	IO 256	IO 256	IO 256	IO 256
LVDSIN_S000	CLK4	IO 257	IO 257	IO 257	IO 257
LVDSIN_S000	IO 101	IO 258	IO 258	IO 258	IO 258
LVDSOUT_S000	IO 102	IO 259	IO 259	IO 259	IO 259
GND	GND_17	IO 260	IO 260	IO 260	IO 260
VCCIO2V5H4	VCCIO1	IO 261	IO 261	IO 261	IO 261
IO113	IO 113	IO 262	IO 262	IO 262	IO 262
IO114	IO 114	IO 263	IO 263	IO 263	IO 263
IO115	IO 115	IO 264	IO 264	IO 264	IO 264
IO116	IO 116	IO 265	IO 265	IO 265	IO 265
VCCINT	IO 117	IO 266	IO 266	IO 266	IO 266
IO117	IO 117	IO 267	IO 267	IO 267	IO 267
IO118	IO 118	IO 268	IO 268	IO 268	IO 268
GND	GND_18	IO 269	IO 269	IO 269	IO 269
GND	GND_19	IO 270	IO 270	IO 270	IO 270
IO119	IO 119	IO 271	IO 271	IO 271	IO 271
IO120	IO 120	IO 272	IO 272	IO 272	IO 272
IO121	IO 121	IO 273	IO 273	IO 273	IO 273
IO122	IO 122	IO 274	IO 274	IO 274	IO 274
IO123	IO 123	IO 275	IO 275	IO 275	IO 275
IO124	IO 124	IO 276	IO 276	IO 276	IO 276
IO125	IO 125	IO 277	IO 277	IO 277	IO 277
DB0_P	IO 126	IO 278	IO 278	IO 278	IO 278
DB1_N	IO 127	IO 279	IO 279	IO 279	IO 279
LVDSOUT_S000	IO 128	IO 280	IO 280	IO 280	IO 280
LVDSOUT_S000	IO 129	IO 281	IO 281	IO 281	IO 281
IO130	IO 130	IO 282	IO 282	IO 282	IO 282
IO131	IO 131	IO 283	IO 283	IO 283	IO 283
IO132	IO 132	IO 284	IO 284	IO 284	IO 284
IO133	IO 133	IO 285	IO 285	IO 285	IO 285
IO134	IO 134	IO 286	IO 286	IO 286	IO 286
IO135	IO 135	IO 287	IO 287	IO 287	IO 287
GND	GND_20	IO 288	IO 288	IO 288	IO 288
VCCINT	IO 136	IO 289	IO 289	IO 289	IO 289
VCCINT	IO 137	IO 290	IO 290	IO 290	IO 290
VCCINT	IO 138	IO 291	IO 291	IO 291	IO 291
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VCCINT	IO 152	IO 305	IO 305	IO 305	IO 305
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VCCINT	IO 154	IO 307	IO 307	IO 307	IO 307
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VCCINT	IO 156	IO 309	IO 309	IO 309	IO 309
VCCINT	IO 157	IO 310	IO 310	IO 310	IO 310
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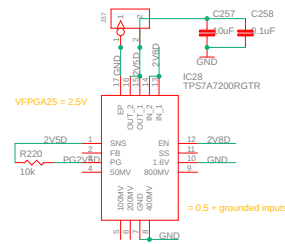
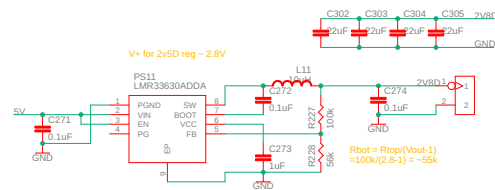


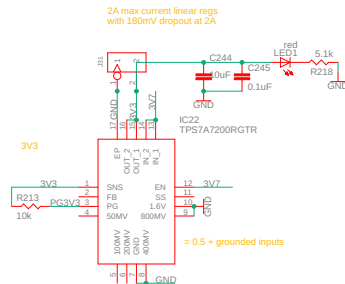
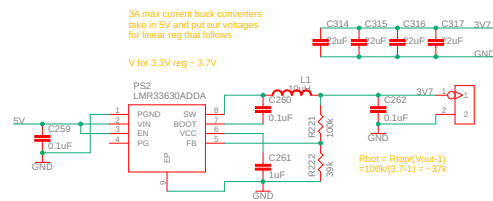
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AND_A1	GND-1	I0-103	M0-DAB-6		
VCCIO3V3_0	VCCIO-1	I0-104	M0-DAB-7		
ETDI_ACG_0	I0-1	I0-155	M2-DAT-7		
ETDI_ACG_1	I0-2	I0-156	M2-DAT-8		
LEND_A0	I0-3	I0-157	M7-IO157		
LEND_A1	I0-4	I0-158	M7-IO158		
LEND_A2	I0-5	I0-159	M7-IO159		
LEND_A3	I0-6	I0-160	M0-DC2-P		
SDIO3V3_DATA0	I0-7	I0-161	M7-VCCIO		
SDIO3V3_DATA1	I0-8	GND-35	M0-GND		
ETDI_ACG_2	CLKI0	GND-36	M2-GND		
ETDI_ACG_3	CLKI0	GND-37	M2-GND		
DEBIOGND_A0	I0-9	GND-38	M3-GND		
DEBIOGND_A1	I0-10	GND-39	M3-GND		
DEBIOGND_A2	I0-11	VCCIO1-7	M5-VCCIO1		
DEBIOGND_A3	I0-12	I0-161	M7-MSE0		
DEBIOGND_A4	I0-13	I0-162	M7-MSE1		
DEBIOGND_A5	I0-14	CONF_DONE	M3-CONF_DONE		
DEBIOGND_A6	I0-15	I0-163	M0-NBR-N		
DEBIOGND_A7	I0-16	I0-164	M0-NBR-N		
DEBIOGND_A8	VCCIO1_7	I0-165	M2-DB4-N		
DEBIOGND_A9	VCCIO1_8	I0-166	M2-DB4-N		
ORBI_B1	I0-17	I0-167	M2-DB4-N		
ORBI_B2	I0-18	I0-168	M2-DB4-N		
ETDI_ACG_4	I0-19	GND-39	N0-GND		
ETDI_ACG_5	I0-20	VCCIO2-1	M3-VCCIO2V5		
ETDI_ACG_6	I0-21	I0-171	M0-K168		
ETDI_ACG_7	I0-22	I0-169	M0-IO169		
ETDI_ACG_8	I0-23	I0-170	M0-IO170		
ETDI_ACG_9	I0-24	I0-171	M0-DC2-U		
SDIO3V3_DATA2	I0-25	VCCIO1-38	N0-GND		
SDIO3V3_DATA3	I0-26	GND-46	N0-GND		
CLKI1_B1	CLKI1	GND-41	N3-GND		
CLKI1_B2	CLKI1	GND-42	N3-GND		
DEBIOGND_A10	CLKI0	GND-43	N3-VCCIO1		
DEBIOGND_A11	I0-27	GND-43	N3-GND		
DEBIOGND_A12	I0-28	VCCIO1-39	N3-GND		
DEBIOGND_A13	I0-29	GND-46	N3-GND		
ENM_CONB0	I0-30	I0-172	N3-IO172		
BOARDIOE0	I0-31	I0-173	N3-IO173		
BOARDIOE1	I0-32	I0-174	N3-IO174		
BOARDIOE2	I0-33	I0-175	N3-IO175		
BOARDIOE3	I0-34	I0-176	N0-DB7-N		
IOES_B21	I0-35	I0-177	N3-IO176		
IOES_B22	I0-36	I0-178	N3-IO177		
ORAI_C1	I0-37	I0-179	P1-DAB-N		
ORAI_C2	I0-38	I0-180	P1-DAB-N		
ETDI_DATA0	I0-39	I0-181	P1-DAB-N		
ETDI_DATA1	I0-40	I0-182	P1-DAB-N		
ETDI_DATA2	I0-41	I0-183	P1-DAB-N		
ETDI_DATA3	I0-42	I0-184	P1-DAB-N		
ETDI_DATA4	I0-43	I0-185	P1-DAB-N		
GND_0	GND-4	VCCIO1-20	P3-VCCIO1		
GND_1	GND-5	VCCIO1-21	P3-VCCIO1		
GND_2	GND-6	VCCIO1-22	P3-VCCIO1		
GND_3	GND-7	VCCIO1-23	P3-VCCIO1		
GND_4	GND-8	VCCIO1-24	P3-VCCIO1		
GND_5	GND-9	VCCIO1-25	P3-VCCIO1		
GND_6	GND-10	VCCIO1-26	P3-VCCIO1		
GND_7	GND-11	VCCIO1-27	P3-VCCIO1		
GND_8	GND-12	VCCIO1-28	P3-VCCIO1		
GND_9	GND-13	VCCIO1-29	P3-VCCIO1		
GND_10	GND-14	VCCIO1-30	P3-VCCIO1		
GND_11	GND-15	VCCIO1-31	P3-VCCIO1		
GND_12	GND-16	VCCIO1-32	P3-VCCIO1		
GND_13	GND-17	VCCIO1-33	P3-VCCIO1		
GND_14	GND-18	VCCIO1-34	P3-VCCIO1		
GND_15	GND-19	VCCIO1-35	P3-VCCIO1		
GND_16	GND-20	VCCIO1-36	P3-VCCIO1		
GND_17	GND-21	VCCIO1-37	P3-VCCIO1		
GND_18	GND-22	VCCIO1-38	P3-VCCIO1		
GND_19	GND-23	VCCIO1-39	P3-VCCIO1		
GND_20	GND-24	VCCIO1-40	P3-VCCIO1		
GND_21	GND-25	VCCIO1-41	P3-VCCIO1		
GND_22	GND-26	VCCIO1-42	P3-VCCIO1		
GND_23	GND-27	VCCIO1-43	P3-VCCIO1		
GND_24	GND-28	VCCIO1-44	P3-VCCIO1		
GND_25	GND-29	VCCIO1-45	P3-VCCIO1		
GND_26	GND-30	VCCIO1-46	P3-VCCIO1		
GND_27	GND-31	VCCIO1-47	P3-VCCIO1		
GND_28	GND-32	VCCIO1-48	P3-VCCIO1		
GND_29	GND-33	VCCIO1-49	P3-VCCIO1		
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GND_31	GND-35	VCCIO1-51	P3-VCCIO1		
GND_32	GND-36	VCCIO1-52	P3-VCCIO1		
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GND_34	GND-38	VCCIO1-54	P3-VCCIO1		
GND_35	GND-39	VCCIO1-55	P3-VCCIO1		
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GND_37	GND-41	VCCIO1-57	P3-VCCIO1		
GND_38	GND-42	VCCIO1-58	P3-VCCIO1		
GND_39	GND-43	VCCIO1-59	P3-VCCIO1		
GND_40	GND-44				



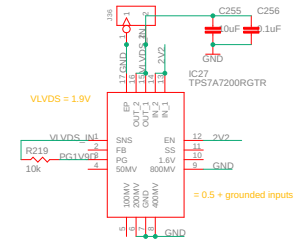
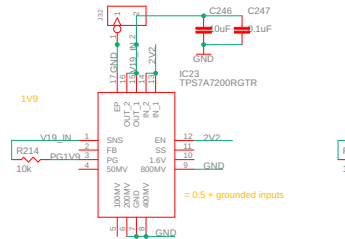
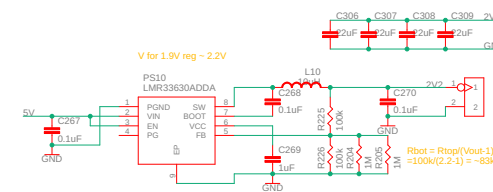
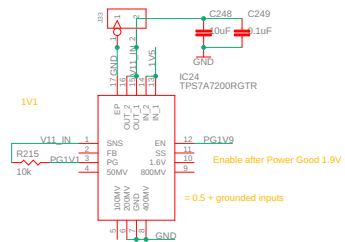
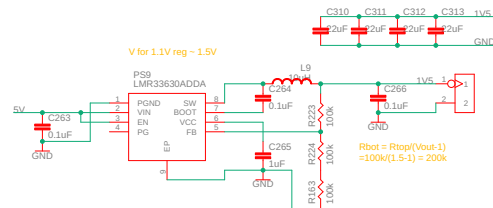


Need some power for the FPGA

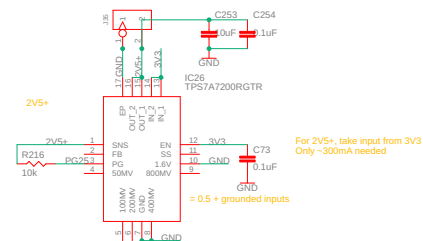




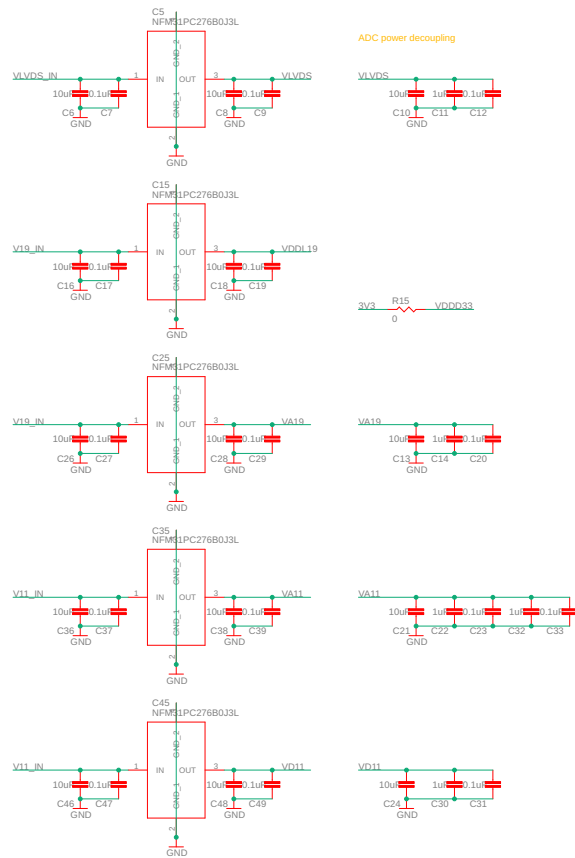
Need 3v3 plus 1v1 and 1v9 for the main ADC



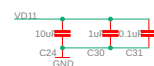
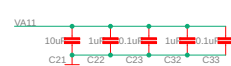
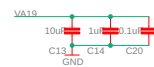
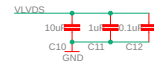
Need + and - 2.5V for the amplifiers



ADC power filters, as recommended in datasheet



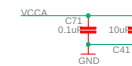
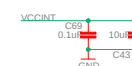
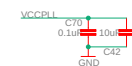
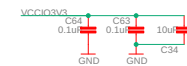
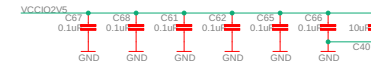
ADC power decoupling



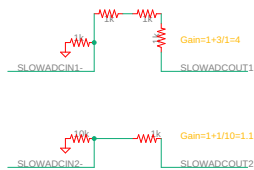
These transfer power to actual FPGA voltage inputs



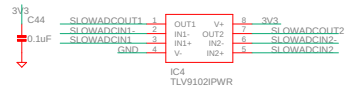
FPGA power decoupling



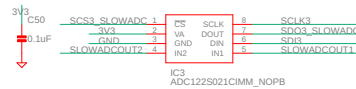
Amplify temp signals so they're
in a good range for the slow adc



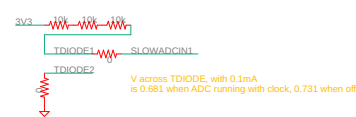
Buffer inputs before feeding to slow adc



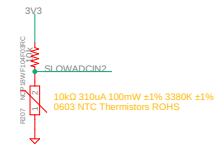
Slow adc for temp monitoring



Monitor the temp of the main ADC



Monitor the temp of the PCB near FPGA



PWM 5V power to fan

