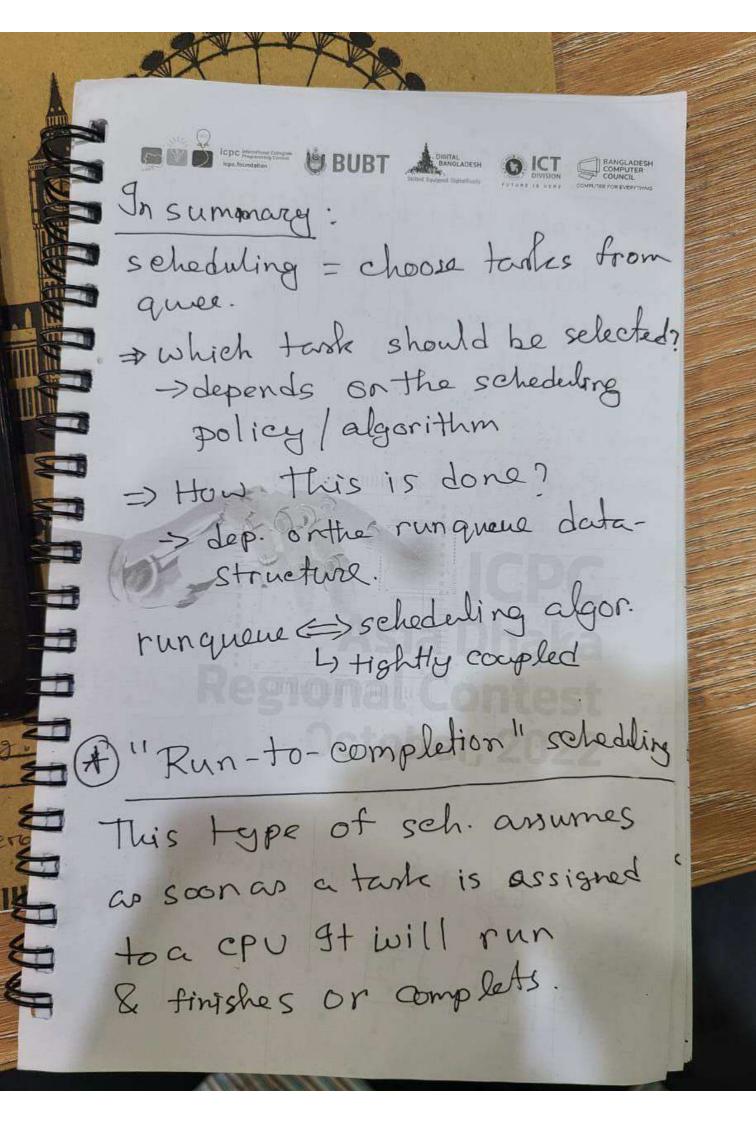
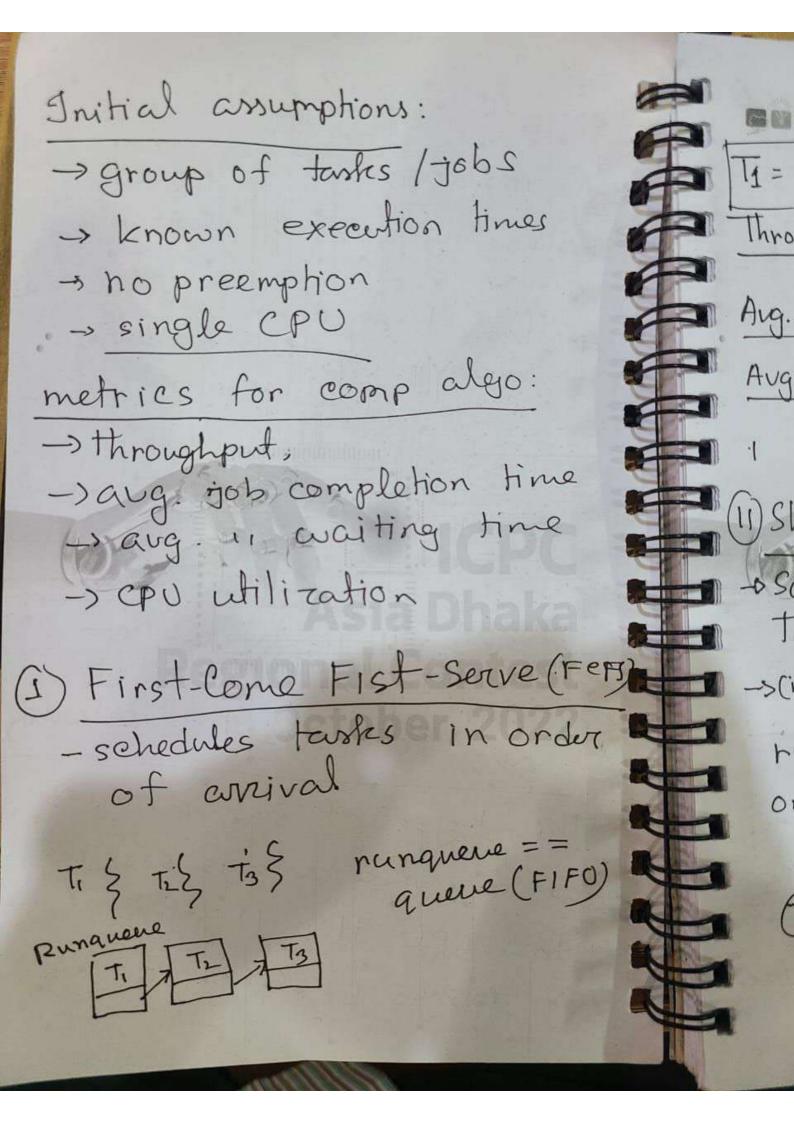
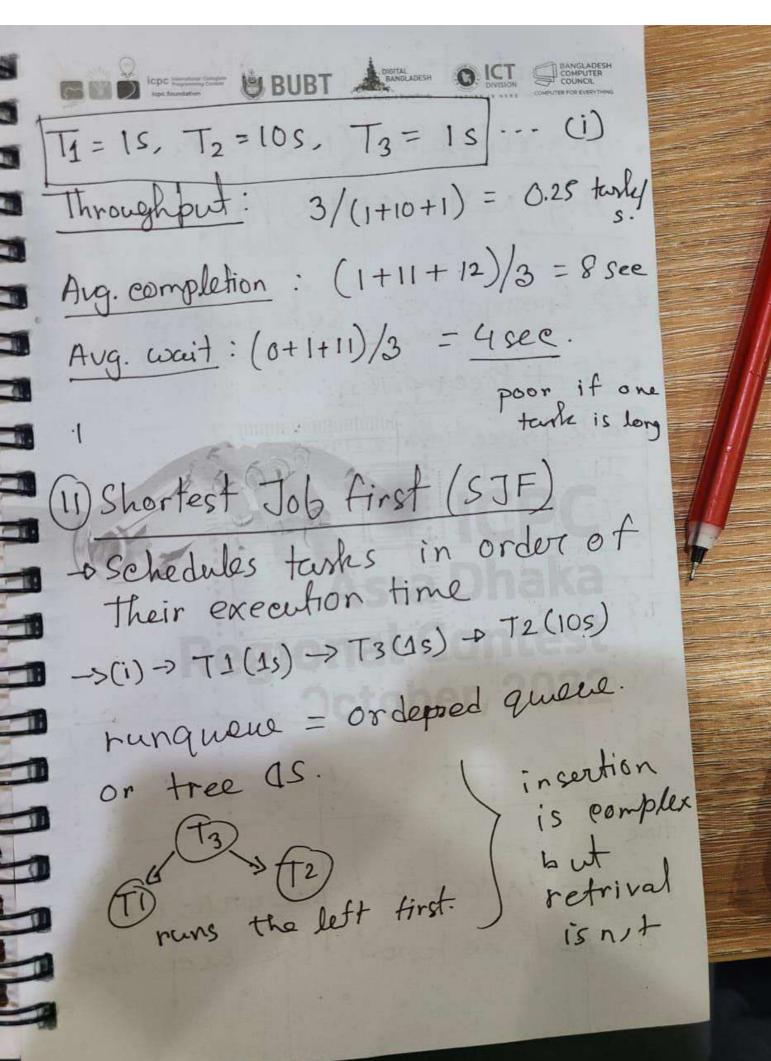


* CPU Scheduling ready queue scheduler CPU) fine silce exp. - (child executes x forkachilder. **43** interrupt wait for an interrupt 15 11 D chooses one of ready tanks to run on the CPU -> schedular runs when 13 => CPU becomes idle 3 =) new tasks become ready expired timeout. => timesclice before: context switch, enter user node, set PC & go! -> Thread is dispatched on CPU.

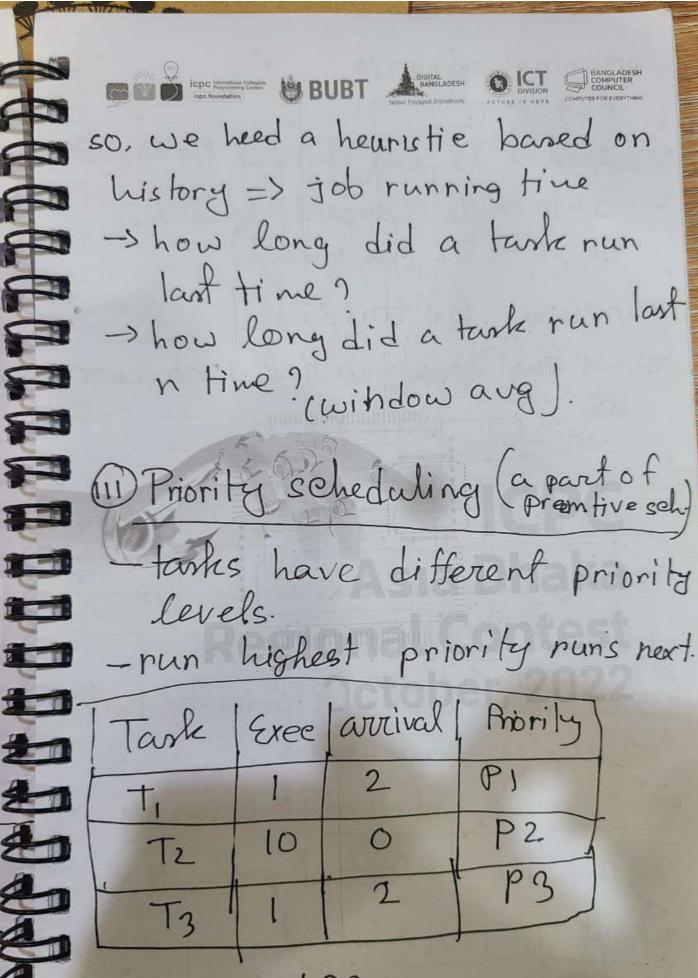




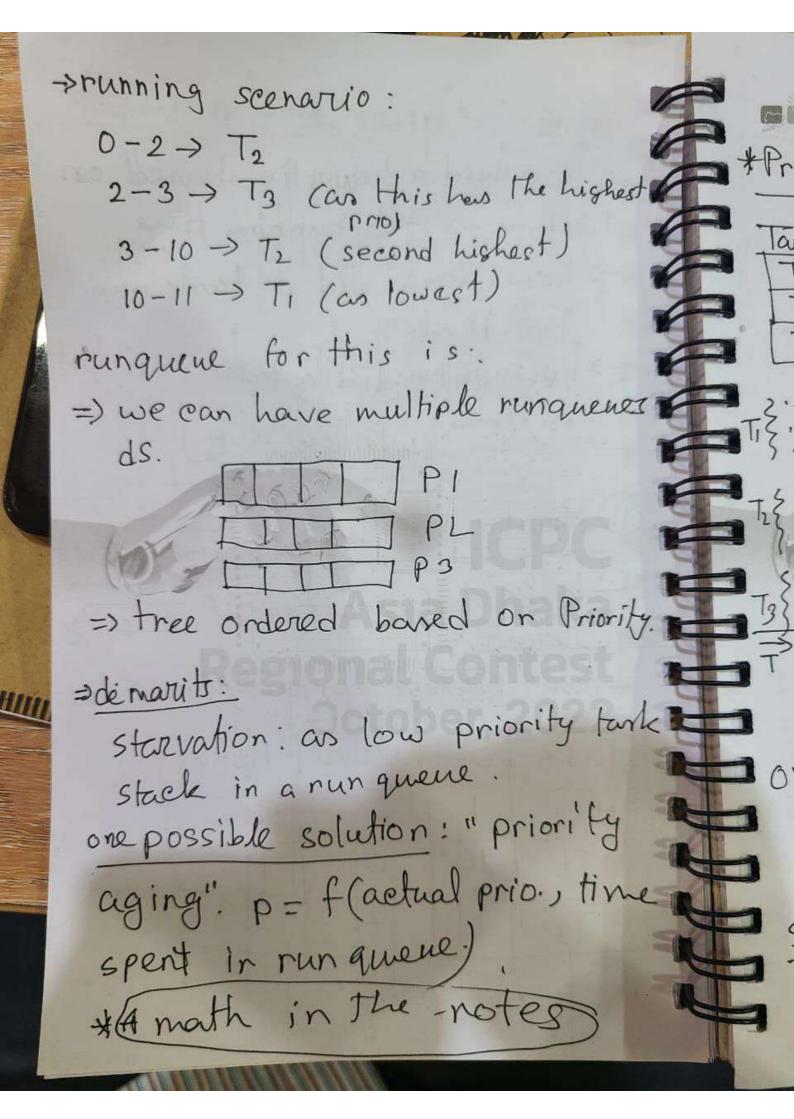


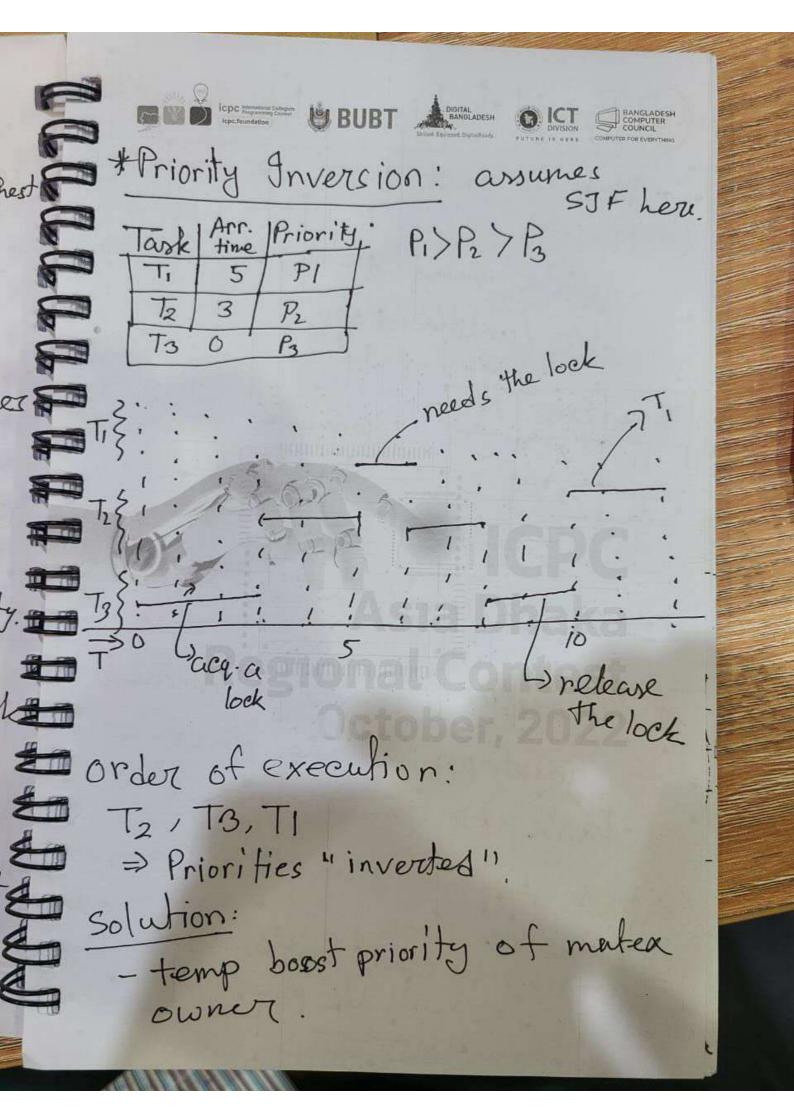
Throughput: (1+10+1)/3=0.25/ Aug. completion: (1+2+12)/3=5s so, Aug. wait: (0+1+2)/3=1 * Preemptive scheduling SJF + Preemption Tark Exectine Arrival time!

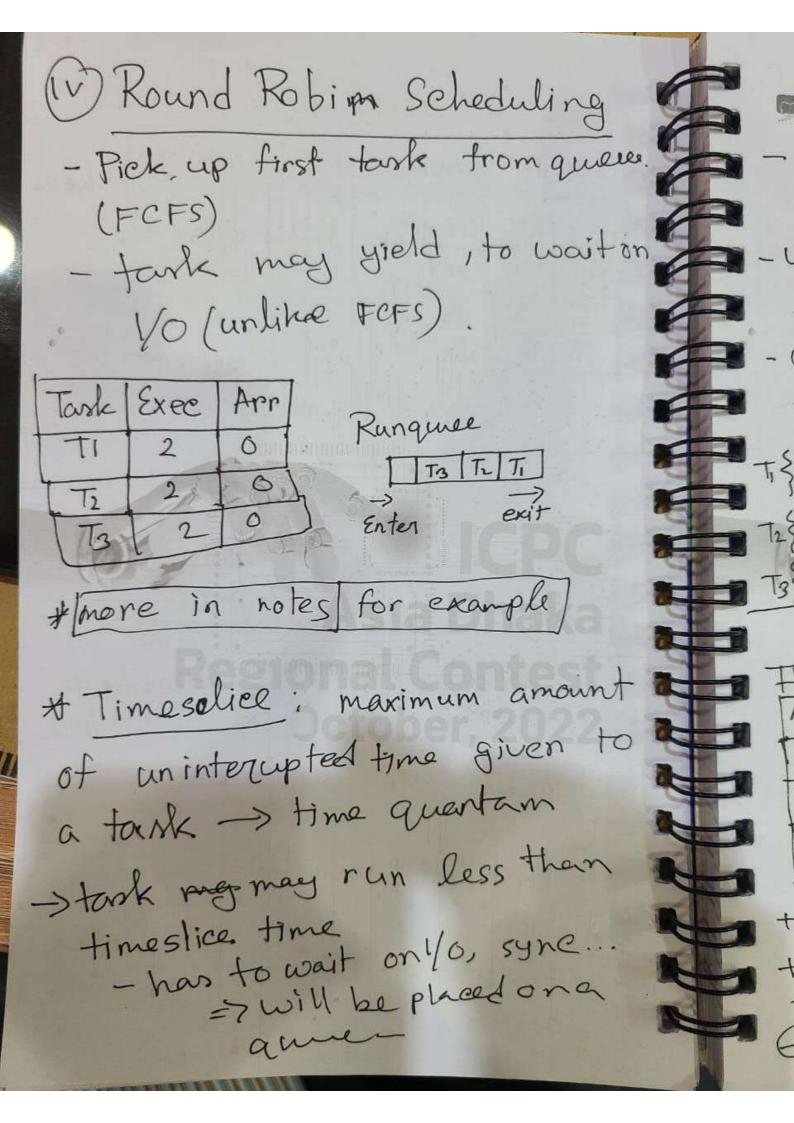
TI IS 2 72 103 0 if we In actual scenario, we do not me know the exec. line

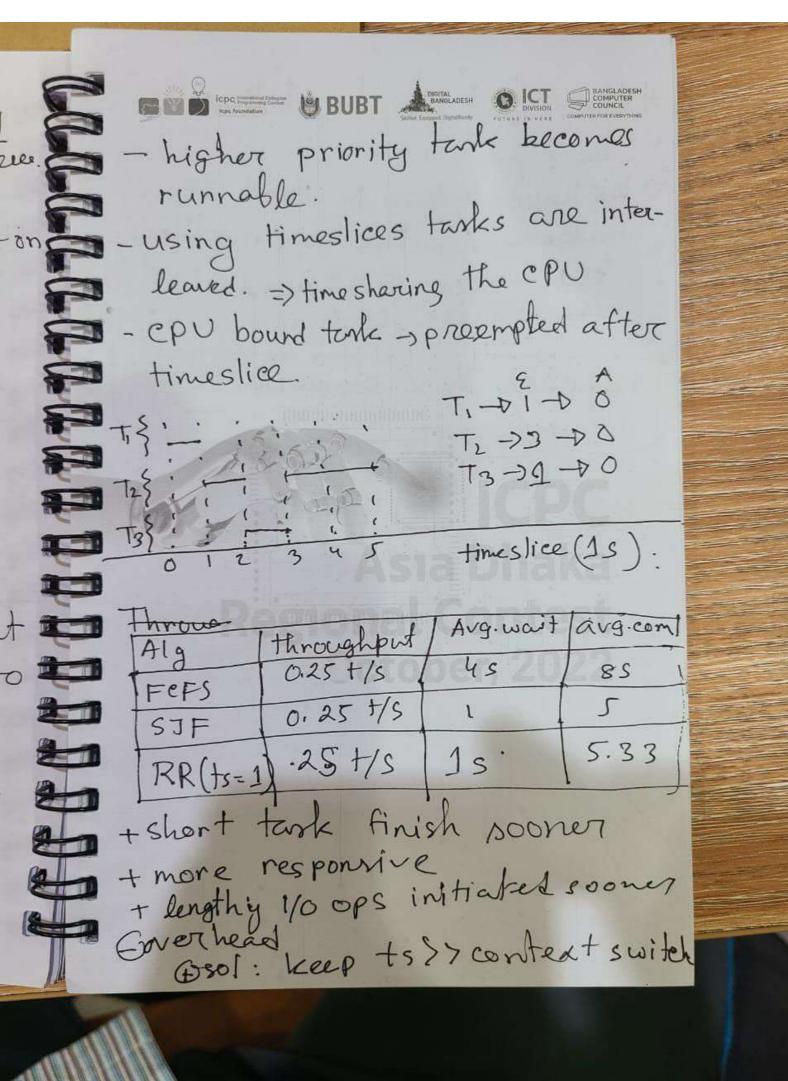


PICPLLP3









* How long should a timeslice >> ke have to keep in mind to balance benefits and overhead. sel: - for 1/0 bound -) foe CPU *t discuss these scenario with