```
Mainc
```

## LAB 5

```
# include LPC407x_8x_177x_8x.H.
   # include "Library / GPID.h"
              " library /PWM.h"
  # include
             " Library / Joystick.h"
  # include
    int speed = 0;
   wid init() }
       Joystick_Init ();
   Minitialize Direction Pins
     PORTI -> DIR 1= (1 ccf);
     PORTI- DIR 1 = (1CCb);
     PWM init ();
     PWM-Write (speed);
 usid update () }
 1 When joystick left B. is pressed, Motor
rotation direction will be charged to forward
    ( Jaystick - Left - Pressed ()) }
    vint32_t value = PORTI-) PIN;
     value 1= (147) ;
     value & = ~ [1 (c6);
     PORTI -> PIN = value;
     PWM_Wite (speed);
1/ When Joys. Up button is presed, Motor
 spood will be increased
   '4 (Joyst-Up-Pressed ()) }
     speed ++;
    for (int i=0; i 21000000; i++) }
    PWM_Write (speed);
   // When Down Button pressed, speed decr
```

```
if (Joystick_Down_Pressed (1) }
   Speed -- ;
   for (int j=0; jc1000000; j+t) }
   PWM-Wite (speed);
When CenterB is pressed = Stop
 if (Joys-center-PressL)) }
  vint32.t value = PORTI -> PIN;
  value &= ~ (1cc 2);
   value & = ~ (1006);
   PORTI-> PIN = value ;
/ When Right B is pressed, Motor rotedir.
 will be changed to backward.
  if (Joystick_Right_Resp()) }
   uint 32 _t value = PORT ( -> PIN;
   value 1 = (106);
   value & = ~ (1cc7);
   PORTI -) PIN = value;
  PWM_Write (speed) ,
 // When Joyst. no button is pressed, Motor
  will continue to perform the lost action
  int main () }
     init ();
      while (1) }
          upolate ();
```

```
PWM.
```

```
soid pwm_Init () }
" Change the dir. of the pin in here
PCONP 1=1 << 5;
1000U_ MOTOR_SPEED &= N(1 (L2);
10 CON_ MOTOR - SPEED 1= (1CCO) | (1CC1);
"Enable PWM output for corresponding pin.
   PWMO -) PCR 1 = (100 10);
"Reset the PWM Timer Counter and the
DWM prescale Counter on the next Pos. Edge
of PCLK
 PWMO-) TCR ( = (ICCI );
 PWMO -> MRO = (PERIPHERAL_CLOCK_PRED
                   /1000000 $ 20 × 1000;
Reset TC, when MRO matches TC
 PWMO -> MCR 1= (ICCI);
"Enable PWM Match Olatch
 pwMO -> LER 1 = (1cc 0);
1/ Enable Counter and PWM and clear Reset
on the PWM
 PWMO-TER 1= (100) 1 (100);
 PWMO-) TCR &= N(ICCI);
  bmumite (0);
 void PWM_Cycle-Rote (vint 32-t-perialin
Cycles) {
 PWMO-> MRO = [ Peripheral - Clock_frog
                     110000) *
                  period in cycle * 1000;
Menable PWM Match Olatch
    PWMO > LER 1 = (ICO); }
```

```
void PWMWrite (vint32_t T_DN) }

if (T_DN > 100) }

T_DN = 100; }

T_ON = Lvint32_t (((PWMO -> MRO) * T_DN)/100);

if (T_DN == PWMO -> MRO) }

T_DN ++;

}

PWMO -> MR2 = T_DN;

// Enable PWM Match Register Latch.

PWMO -> LER 1 = (1 CC 2);
```

## LAB6

```
Maine
```

```
Void init () }
(EQ_init();
Timer_Init();
}
```

When the time between 1:sing and falling or falling-rising edge is less than 100 milis. and greater than 5 miliscoona, turn on the LED on the Quick Stort Board Otherwise turn off the LED on the Quick stort board.

```
Void upolate () {

cullent-time = TIMER 3 + CR1;

if ((cullent Time - previous Time)!=0)

{

coptuled Time = cullenttime - previous;

if ((cop.Time > 5000) & (copt. closass))

{

clse {

    LED_OFF();
    prev.time = cullenttime;
}

int main () {

    int ();
    while (1) {

        update (1;
    }

    update (1;
    }
```

```
Timer.c
```

```
uln232 - t .currenttime = D;
           previoustime = 0;
           captured time = 0;
 void Timer_Init () }
  11 Give the correct func. values to
  10 CON-OUT
  10 CONOUT 1 = (1 (1) ) (1 (1));
  10CONOUT &=~ (1CC2);
Knable timer 3
  Proup 1 = (10023);
 // Change the mode of Timer 3-10 Times
Timer 3 -> CCR = 0;
// Disable Times Counter and Pres.c.
TimER3 -TCR &=~(ICCO);
MReset Time. and Pres. C. for Times ]
TIMER 3- TOR 1= (ICCI);
Khange PR register values 191 Imics
 second incremently
 TIMERS -> PR = 59;
Il Cophure rising and Falling Edge on CAPI
 Timer 3 > CCR 1= ((1CC3 1(1CC4));
// Remove the resol on counters of Tim?
 Timer 3-2 TCR &=~ (1CC1);
VEnable Timer 3 counter and Prescole
  conver lor conviva
  Timer 3 -> TCR 1= (1cco);
```

```
typeder struct }
volatile vint32-t IR;
                  TCR;
                 TC;
                  PC;
                 CR1;
                  reserved[3];
 STimer_Typedef
       TIMERO_BASE. 0x40004000.
define
                              900000
                              94000
                 ((Timetypodef *) Timer O_Base)
 define TIMERO
   define 10 cow_OUT_ADPR 0x4002C060.
          10conout * ((volatile vint 32-+ *) (10con_out_mope))
   extern vint 32-t concent time
                  copt. line
   void Time_int (Void);
```

```
V(+1950NC_Trigger_Timer_Ini+1)
                                               ( Pini_ ) 21190
     LOCON-Trieger 1= 3;
      PCONP 1= 1<(12;
      TM? -> CTCR Q = ~ (3); -> change made +0 Timp
      TMI -> T(R 1= (1((1) -) Reser time I crusher and presente courter
       TMI > PR = PER...CLOCK_FREQ / 1000000-1 -> chape PAPERgista va
       TMZ - TCR D = 1(1((1) - remove reset on counters
  Atomaic - Exptore - Timer - Init ()
       10(0N-Echol3 3 > 0x40010060
       PCONP 1= 15023.
        TM3 -> CTCR = 0 x00
         TM3+ T(R &= n (1((0)
         TM3 = T(R 1= (1 ((1)
                                   Const year Date Depart Server
             PR = -> same
         TM3 + ((R)= (1 (13)
         TM3 300R= (1685) = introlope when rising Edge occur
TM3 300R= M(1664) = introlope when rising Edge occur
          TM3 + T( P = ~ (4(1)
                TCR 1= (1(C0)
          NVIC-Clenfending (RQ (TIMER3-IRQn) -
           NVIC - Set Privity (TIMER3-180, 5)-
           NVIC - Englie IRQ (TIMER) . IRQn) =
 1 tenson+ - 5 tot - + 1 igge 1 1)
                                             in -int Tolan (ther loss)
     TM2 - EMB 1= (1(3) - gire high value to T2-MAT-3 pin
        >MR3 = TM2 >TC +10 -> 10 ms HIGH value
       SEMR D: N(1((11))) making LOW output value when mostly occurs
       SMCR 1= (1((19)) Reser TO and Stop (To dad PC) if matches TO
       STER 1= (1 (( 0) & enable counter and ps counter for counting
```

Vlarasinic Seasor Edgelount = 0

```
TIMER?_IRQ Hondles()

if (senser Edge (over == 0) {

scaser Raising Time = Tm3 + CR1 + sense vising time

Tm3 + scal = (1404)

+ car & = n(1403)

+ car & = (1405)

Senser Edge (over == 1)

NVIC. Clegal Fending IRQ (TIMER3 - IRQn)

Senser Edge (over == 1) {

senser
```

TIMER 3 -> IR 1 = (1665) -> Elevi intrempt flag Jor coptore channel 1 quest

```
sensor Distance = (falling time - rising Time) 158

wait (600);
```

Sensor Edge Count = 2;

cledifending

```
US _Tirer_Init()
      10CON- TRICGER 1= 0x03;
        PROND 12 ICCSF;
       TIMER2 - CTCR= 0x00
        TIMERS STOR 6 = ~ (ICCO)
        TIMER + TCR 1= (ICCI)
        TIMERZ+PR= Per--- Clad. Foreg ... /1000000-1
      - Write correct Conf. for FMR
           cint32-+ value=Time 27EMR,
                    ualue (= (1663)
                   value 1= (16610)
                   volve 1=(12011)
                   TIME 27 EMR=volue,
      - Froble TIMERZ_IRQn
             NUIC-EnableIRQLTIMER2_IRAn)
      - Set Priority Tier 2 (Res os 5
             NVIC _ Set Prizity (TIMERZ_IRAn,5)
      - Clear Rending for TIMER2
            NUIC_Clear Pending IRQ(TIMER2_IRQn)
void Ultrasonic_Capture_Trier_Init()
         1000N_ECHO (= 0x03
          PCONPl=16623
         TIMER 3->CTCR = 0x00
          TIMER 3 -TOR L= N(1CCO);
          TIMER 37TCK 1=(1221);
         TIMER SAPR = Perip-(led-freg /1000000-1;
         TIMER3 + CCR = (1463) | (1464) | (1465)
         TIMERS -TCR &= ~ (ICCI)
void ultrosoure-Shat-Trigger()
        - Give correct volve to MR3 Reg. for 10 mic.
          TIMERZ-MR3=10
         FERABLE interrupt for MRS register, if MRS reg.
          TIMER 2+ MCR 1= (1669);
         - Perove the reset or counters of TINERI
           TIMER2+ TCR &= ~ (1461)
         - Eroble Tier? Counter ad Preside Counter Len
```

TIMERZATOR (= (160)

```
lab 8 Dem
```

```
13 Utrasuic Sensor Trigger Ended = 0
            Intracon Sonder Edgelouit = 0
Void TIMERZ_IRQ Hadler ()
     if (15Utrosmic Same Trigger Forded == 0) {
          - chape MR3 Register Value der Suggested
          TIMER 1+4R3 = 60 + 1000
                     :0
     # Clase
             party for Tier 3
       NVIC_CLEARENDINGIRD (TIMER3_IRBn);
     11 Enoble TIME
        NVIC = FADDLEIRO (TM) - [ROA)
    felse s
        Tm 2 3 mR3 = 10
         is Vitadsanic Sensor Tripper Frided = 0;
    TM 2 31R 1= (1 ((3) ) = cled IR Flag
    TMI +TC = 0:
                                                       Modes
  void TM3_IRQHand(er())
                                                          Sleep Mode
       if (sonder Edge (ova+ == 0))
                                                            SCR & = ~4
          rising Time = TM3 -3 CR1
                                                             PCON &= N3
          edge Court = 1;
                                                              -WFI(),
                                                         Deep Sleep Moe
          NVIC_ (low Pending IR Q (Tra3_IR Q n);
                                                             SCR 1= 4;
       selse if ( senser Flee (a.m == 1) {
                                                             PCON L= n3
           Jelling Time = Trad + CR1
                                                             -NFI(1
                                                         Power Down
             edge (0.n+ = 2
                                                              SCR 1= 4
             WVIC- Clerk Product RQ (TIMERZIRA);
                                                              PCON 6 = ~ (1 ((1)
                                                              PCON 1: (1((0)
             NVIC-Desable IRQ (TIMER3 - IRQA);
                                                         Deep Power Down WFICE
                                                             SCR 1= 4
          Tm3 -3 1R = 1 < < 5;
                                                             PCON 1=3
                                                             --WP1()
```

```
ADC.c
- Change the made value of pin to made
     // Inactive = 00 ( L: 3 bits)
     ANALOG_PIN_10CON 6=~(24);
 - Change Analog/ Digital mode of pin to Analog
                      \beta = \sim (1 \langle 1 \rangle)
    ANHOG - - - .
  - Turn on ADC
     PCONP 1= (12612);
  - Set the CLKDIV and make the AID converter
     vint-32-+ val2=ADC >CR
                val 2 = ~ (0xFF (28)
                val 2 (= (ADC_CLKDIV &C8)
ADC - CR = val2
                ADC > CR (= (166 21)
               ADC >CR (=~ (12216)
       -Write a code for starting AID conversion (void ADC-StapC)

unint-32 val = ADC+CR;

val d = \infty (7 (C24)

val 1 = (16624)

ADC+CR=val
   void ADC_Stort()
                  ADC -> CR = value.
    wint32 _+ ADC_Rad()
          cint32-+ dota;
         - Configure CR SEL bits for sompled and converting correspond.
           ADC → CR (= (16612) //ADO(2)
          ADC-State).
          wint32-1 DONE_MASK= (12231);
          while [[(AOC + DRC2] & DONE-MASK));
          -Coner the data RESCUIT to 0-1000 rage and return
            UINTILL RESLUT - MASK, (OxFFF (CL);
                         (esult_ (AOC -) ORCED ( Result_MASK) >) 4:
                         data = (result *1000.0)/AOC_MAX_VALUE;
                         APC - Shop():
```

\eU ob.

Serial-initi)

See pin func for TX and RX

PCONP 1= (1(C3)

Serial-VARTS FCR 1= (1<<0) > enaber fifo

VART > LCR = (1<<7) > enable Divisi Latches

-> DLM = 0x01

ללומיתו בלחסלית בחתו ותון ()

-> DLL = 0 x 75

-> FOR = 0x01 << 0 / 0x03 <<3

TLCR &= N (1 (47) -> disoble disirer lather

→ [CR = 3 <<0 | 0 <<2 | 1 <<3 | 1 <<4; -> chonge 8 bit

serial . Write Data ( chor date)

while (( Serial\_UARTSLSR & (1 ((5)) == 0); -> woit until THR become empressionl\_UART -> THR = duta; -> write duta to THR

char Serial Read Data ()

chair datas

while ([Social\_UART = LSR & (1 << 0)) == 0); = work work Received

data = Social\_UART = RBR; = Read data from received buffer register

return data;

ear char To Int (chor Late)

int val = data - 101

return vol;

Second-Write ("Irla") -) new line

(char result string [72);

(sprintf (result String, "Odlrla", result)

s int to string