

Task #156 Fix logic examples

The default pins used in the provided Logic examples are to pins on the AVR128DA48 that are not analog. An analog pin is required to read a voltage sent to the pin. Not using analog pins for input means a voltage is never detected by the board and the logic gate will not function.

```
Logic0.enable = true;           // Enable logic block 0
Logic0.input0 = in::input_pullup; // Set PA0 as input with pullup
Logic0.input1 = in::input_pullup; // Set PA1 as input with pullup
Logic0.input2 = in::input_pullup; // Set PA2 as input with pullup
```

Figure 1: Default pin settings for a three input AND gate

Analog pins on the AVR128DA48 are found in the PD and PF registers. To fix the issue the default pins should be set to pins in these two registers.