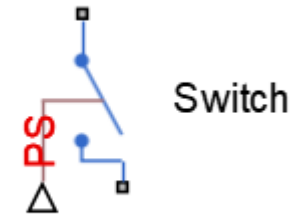
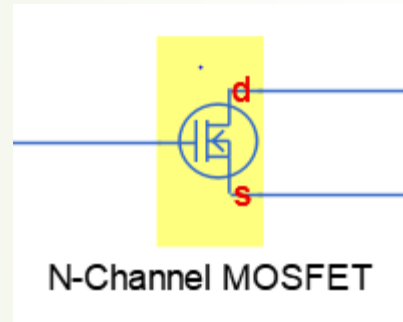


Basics of Mosfets

Enes AYAZ

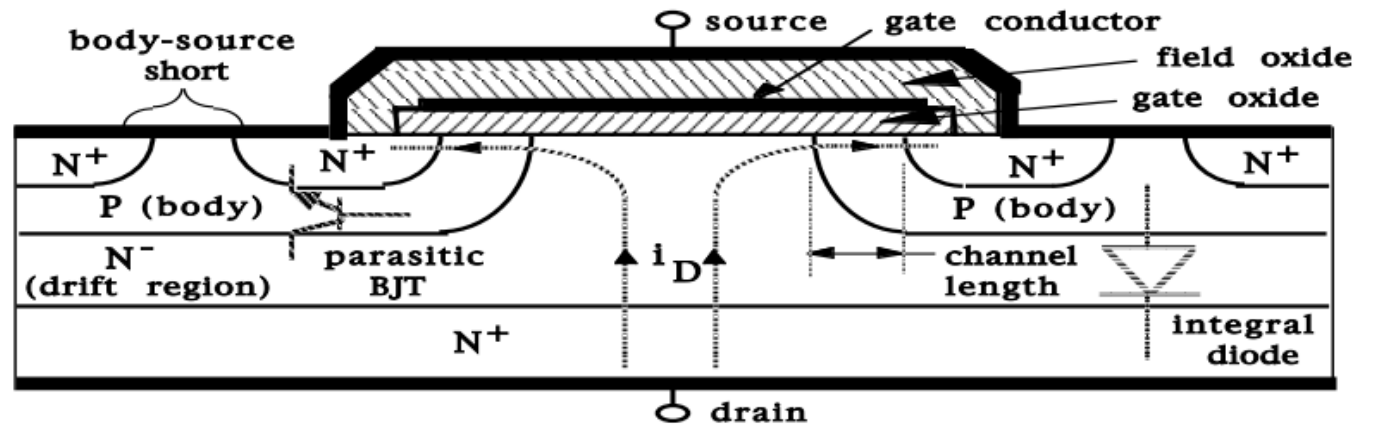
MOSFET (Metal Oxide Semiconductor Field Effect Transistor)

- ▶ Transistor is used for amplifying or switching electronic signals.



- ▶ There are 3 terminals.
 - ▶ GATE
 - ▶ SOURCE
 - ▶ DRAIN

Geometries of MOSFETS



- There is some manufacturing technologies to produce Mosfets.
- LDMOS, VMOS, UMOS, PLANAR, TrenchMos, HexFet
- VDMOS is shown above.
- Basic structure is n+pn-n+ doping.(n type enhancement)

Working Principles and Operation Regions

- Gate-to-Source voltage and Drain-to-Source voltage determines the operation of Mosfets.

- Cut-off Region: $V_{gs} < V_{th}$

$$I_d = 0$$

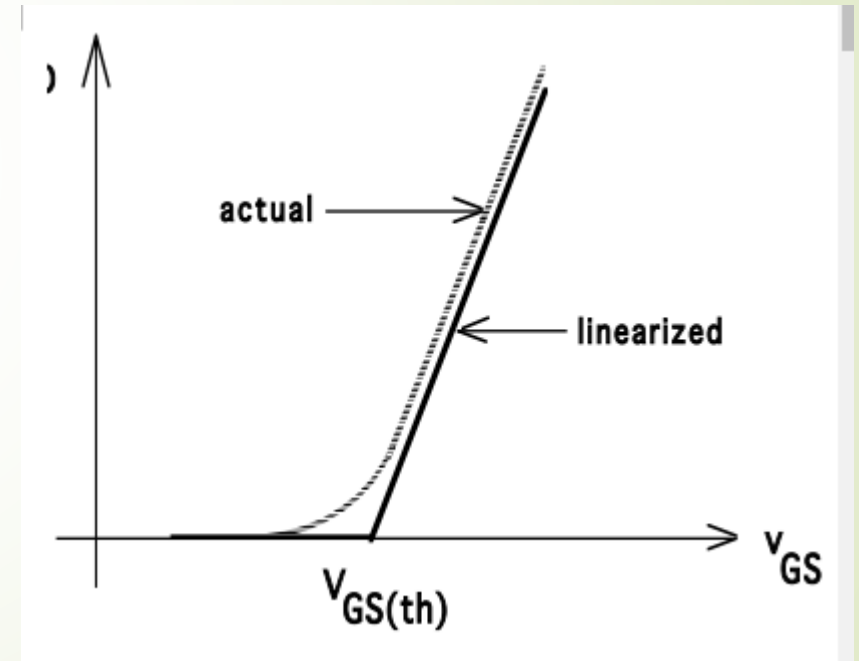
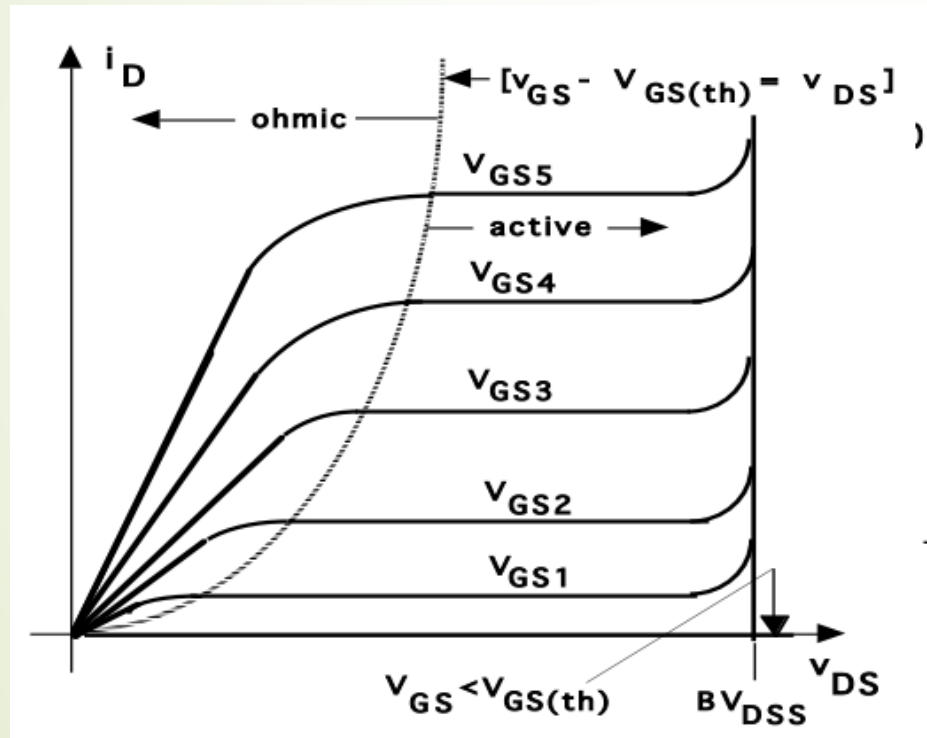
- Linear Region/Ohmic/Triode Region : $V_{gs} > V_{th}$ and $V_{ds} < V_{gs} - V_{th}$

$$I_d = K_n * ((V_{gs} - V_{th}) * V_{ds} - \frac{1}{2} * V_{ds}^2)$$

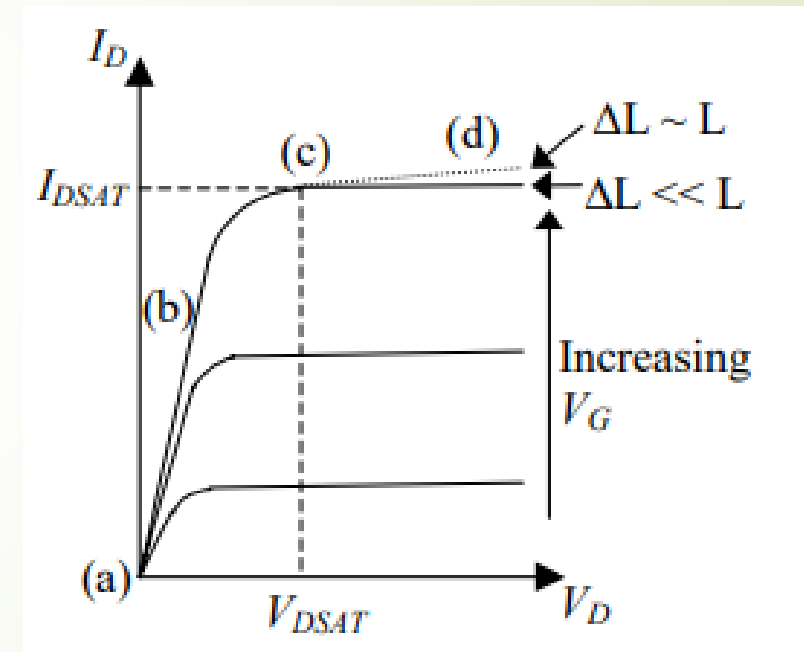
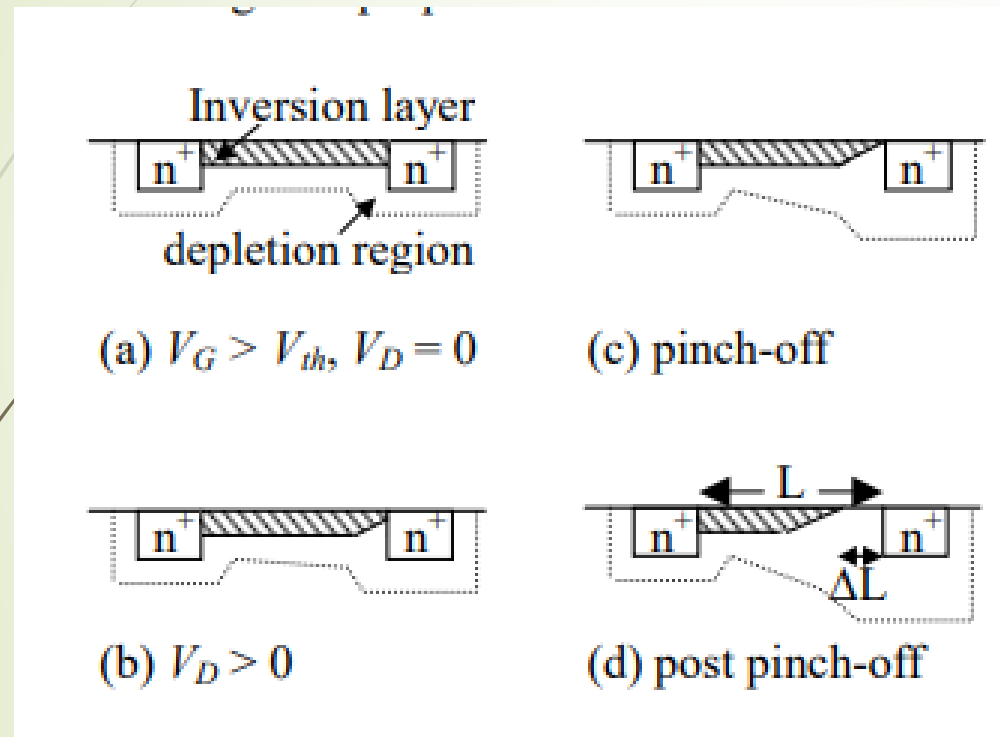
- Saturation Region: $V_{gs} > V_{th}$ and $V_{ds} > V_{gs} - V_{th}$

$$I_d = \frac{1}{2} * K_n * (V_{gs} - V_{th})^2$$

I-V Characteristics



Physics of Device Operation



Mosfet Parasitic Capacitance

