

## Weekly Report

- Schematic Design: Connection 0-ohm resistors and indicator LEDs
- Designs are multiplexed: Enumerations and Tags
- Ready for PCB design ?

## Trapping

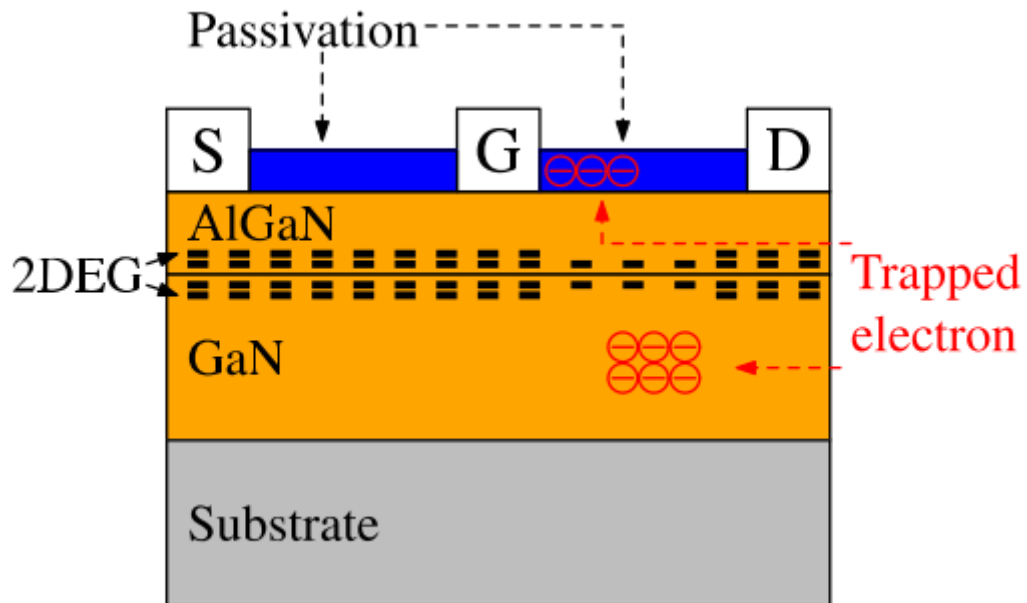


Figure 1: Trapped Electron Positions

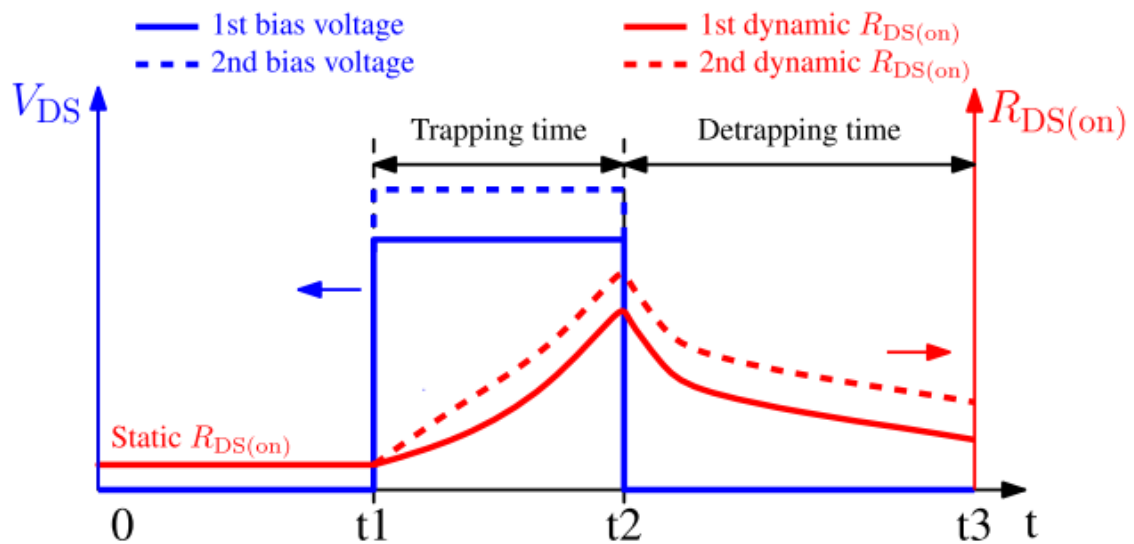


Figure 2: Trapping Time and Detrapping Time

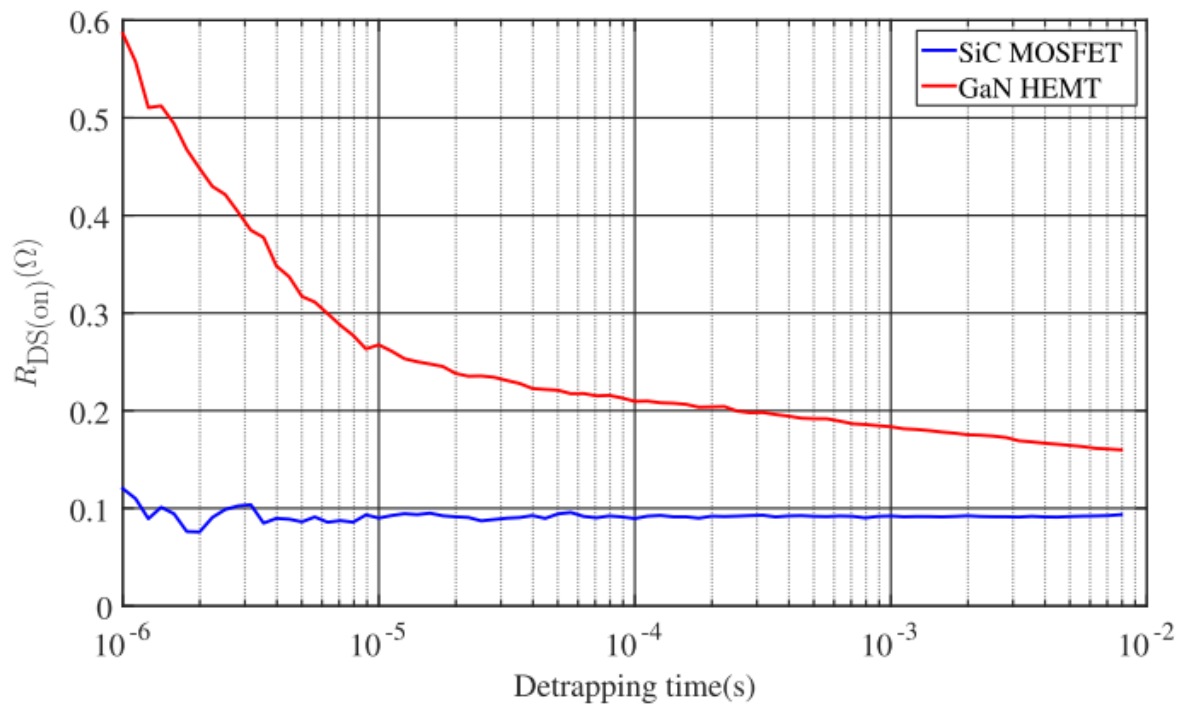
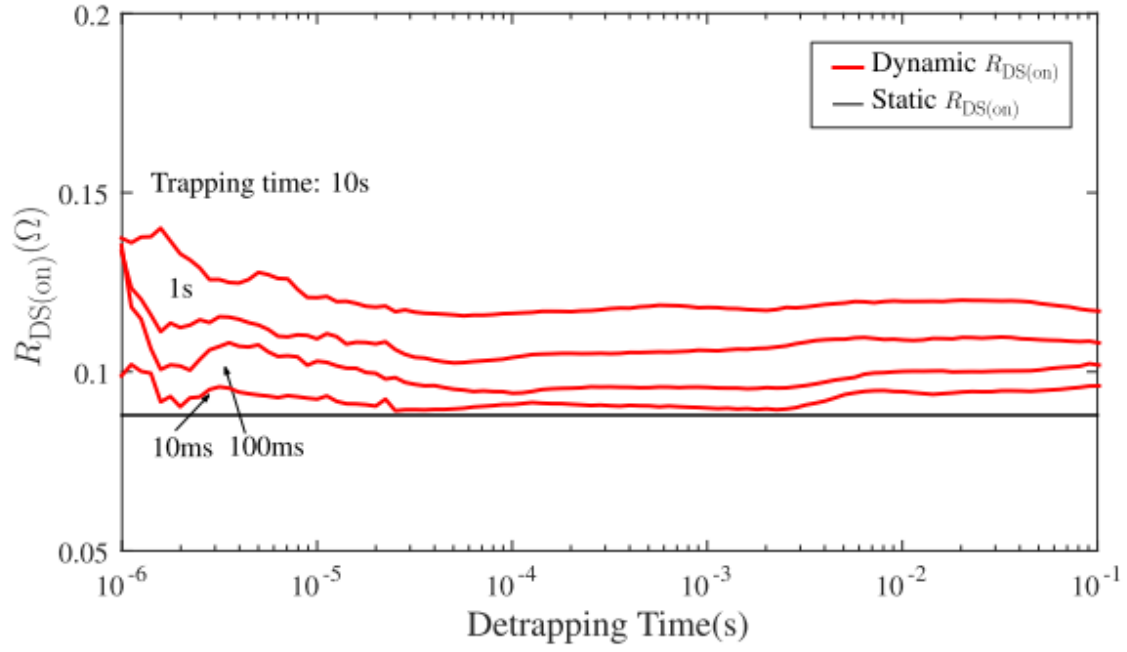
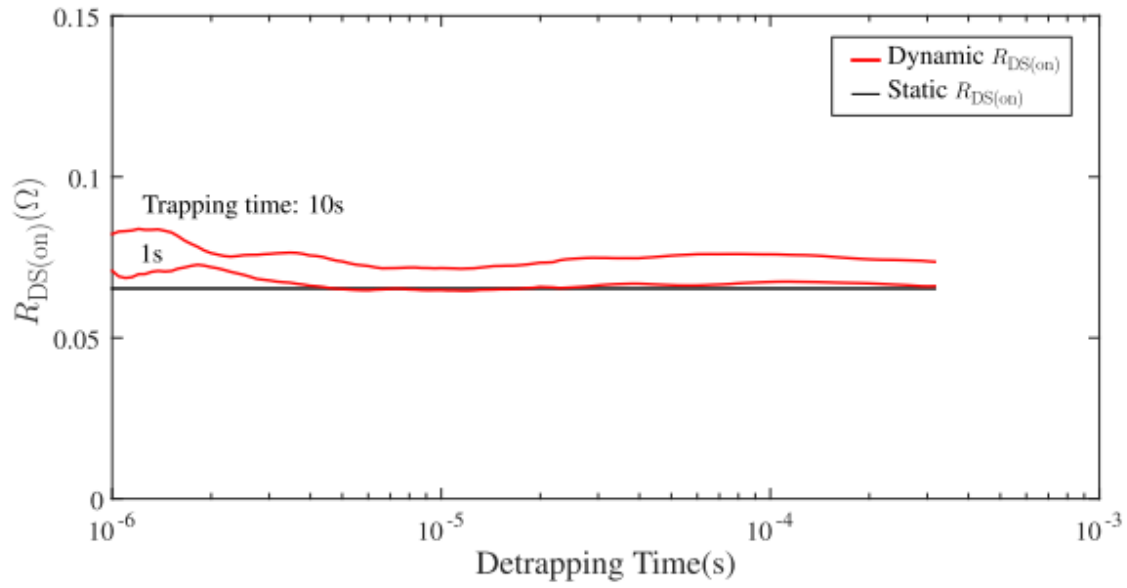


Figure 3: Rds-on Detrapping Period Behavior

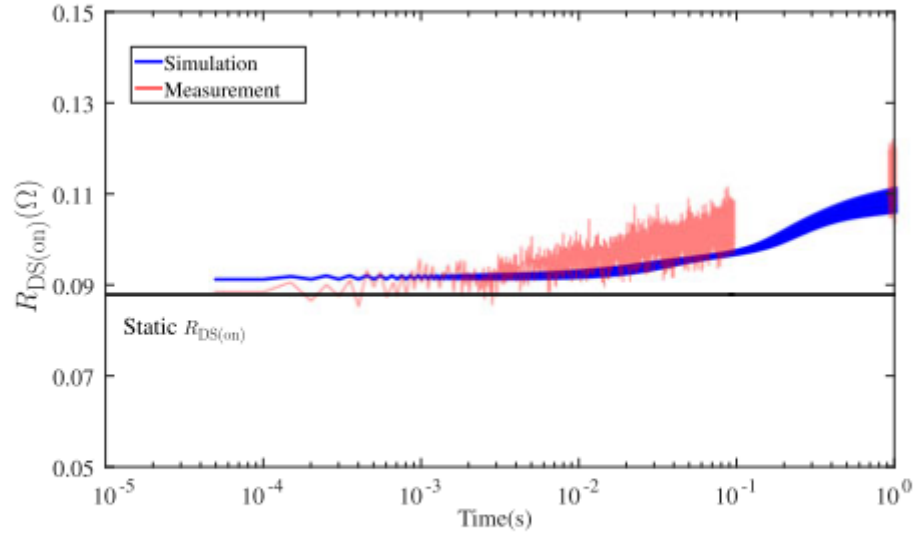


(a)

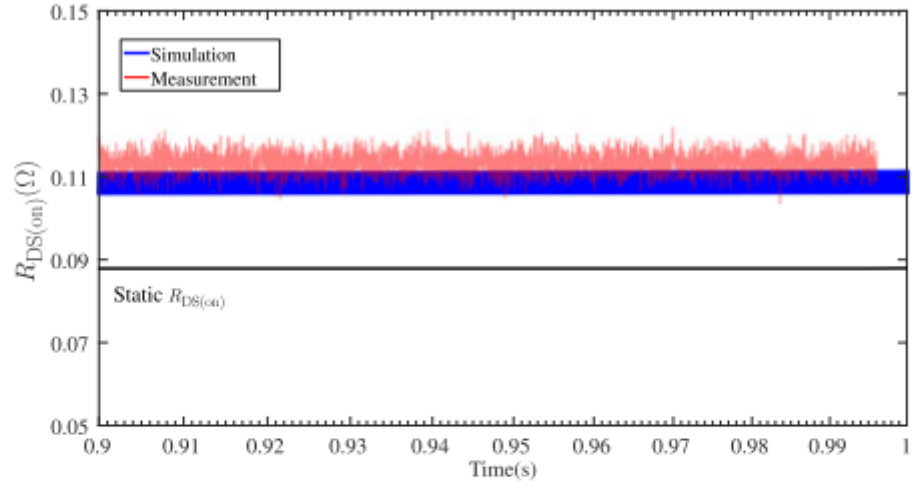


(b)

Figure 4: (a) is for 650V/15A GS66504B @400V bias voltage, (b) is for 100V/1A EPC2036 @100V bias voltage



(a)



(b)

Fig. 19. Comparison between the measurement and simulation on  $R_{DS(on)}$  values when device GS66504B switches at 400 V/2 A (50% duty cycle). (a) Dynamic  $R_{DS(on)}$  values at 0.1 s. (b) Dynamic  $R_{DS(on)}$  values at 1 s.