

Weekly Report – 26 Feb 2018

PWM Generation using DSP

Digital Input / Output

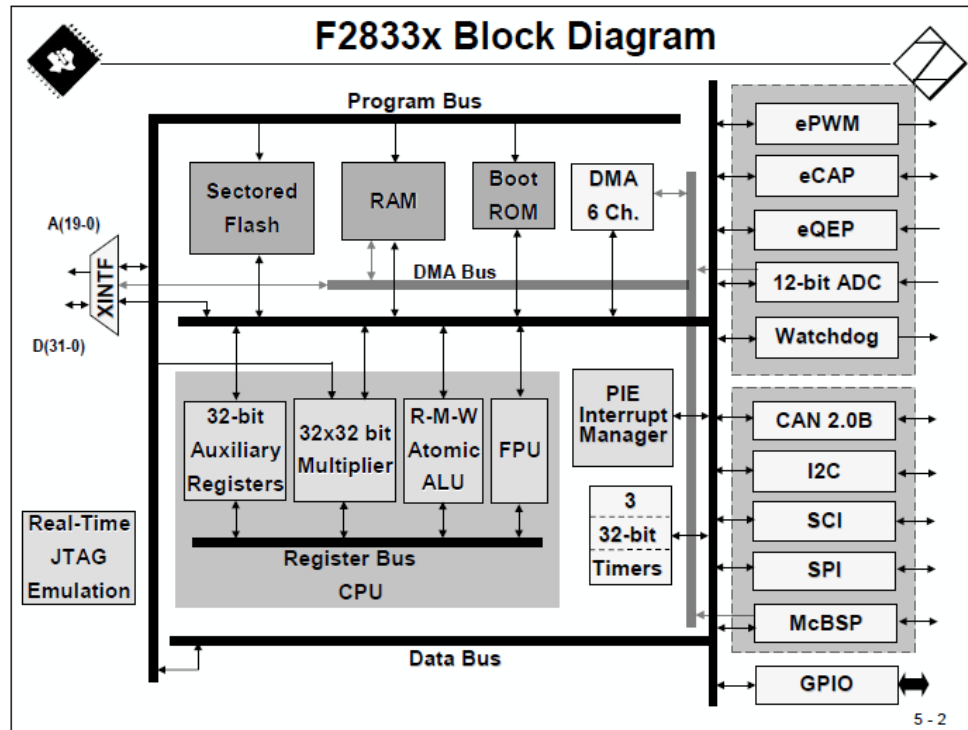


Figure 1: General Block Diagram of uP

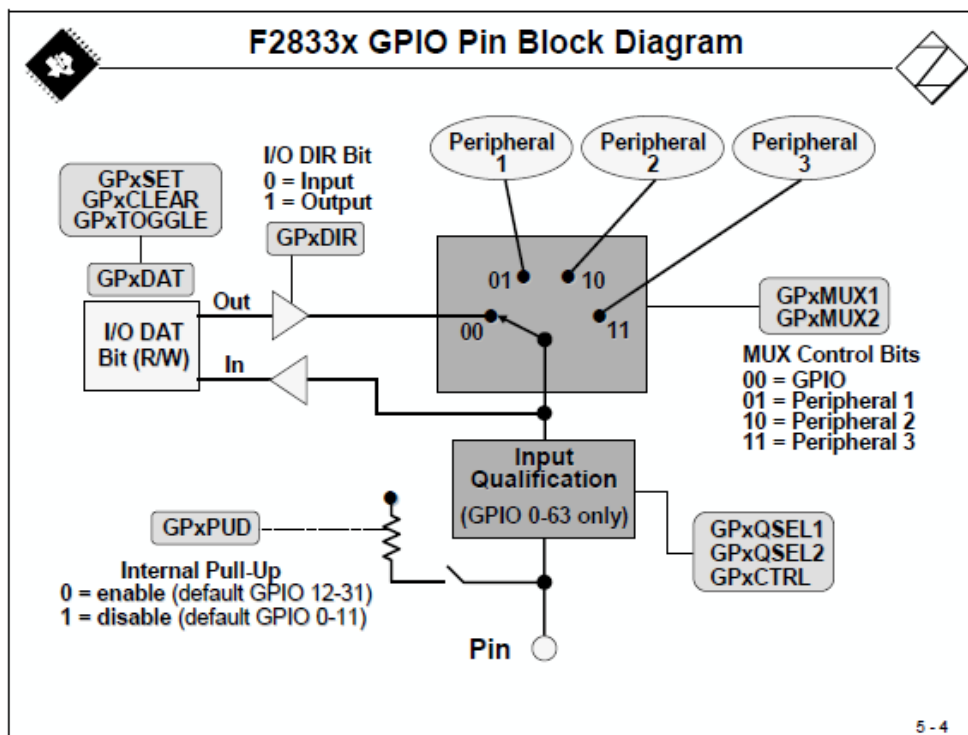


Figure 2: GPIO Pin Block Diagram

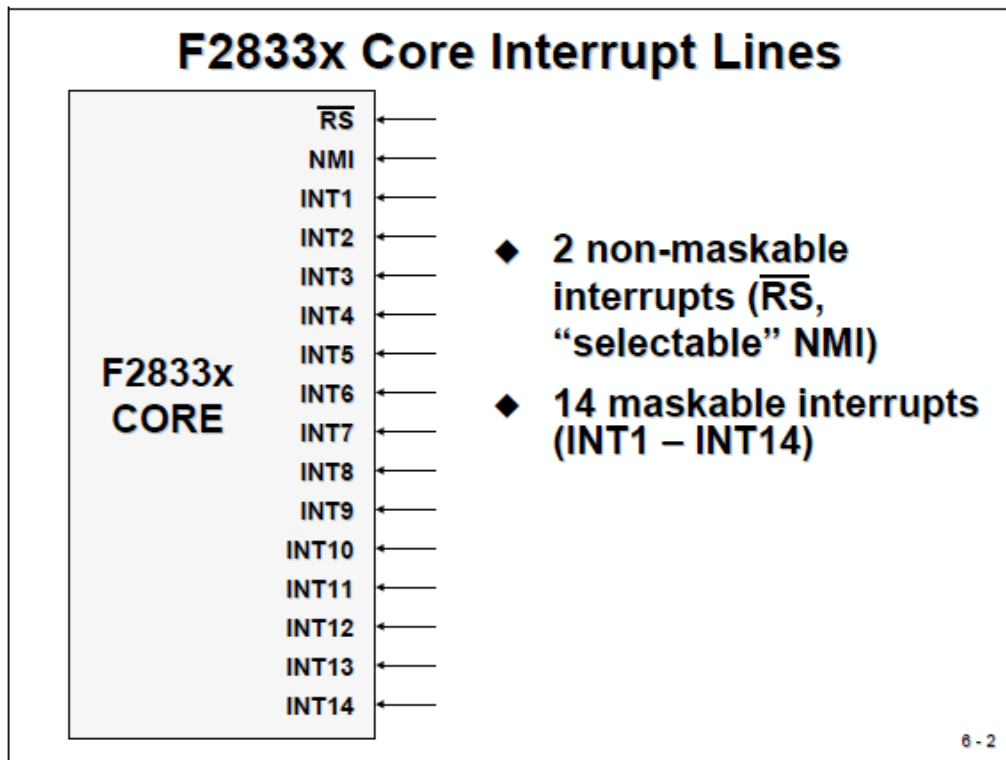


Figure 3: Interrupt Lines

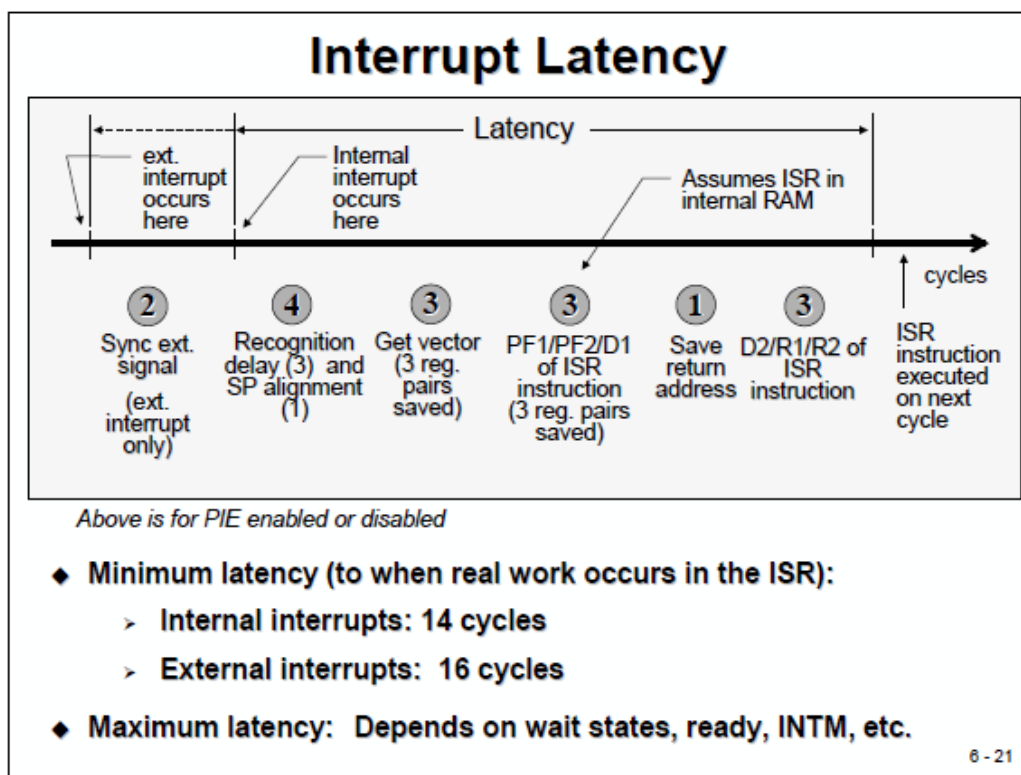


Figure 4: Interrupt Loop

PWM Generation

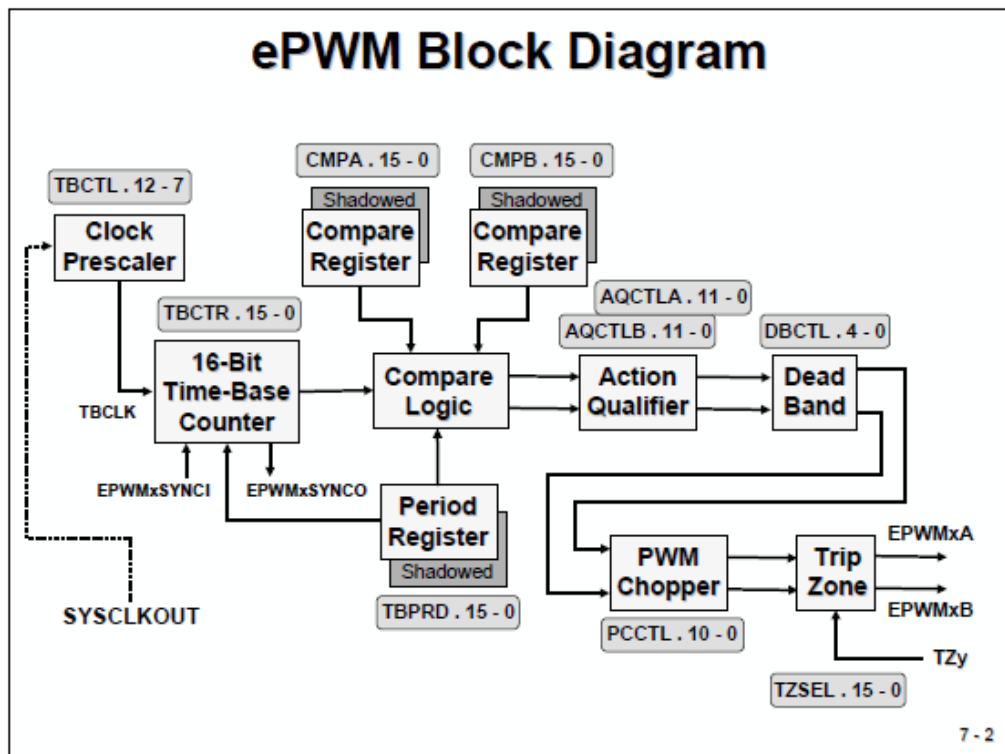


Figure 5: ePWM Block Diagram

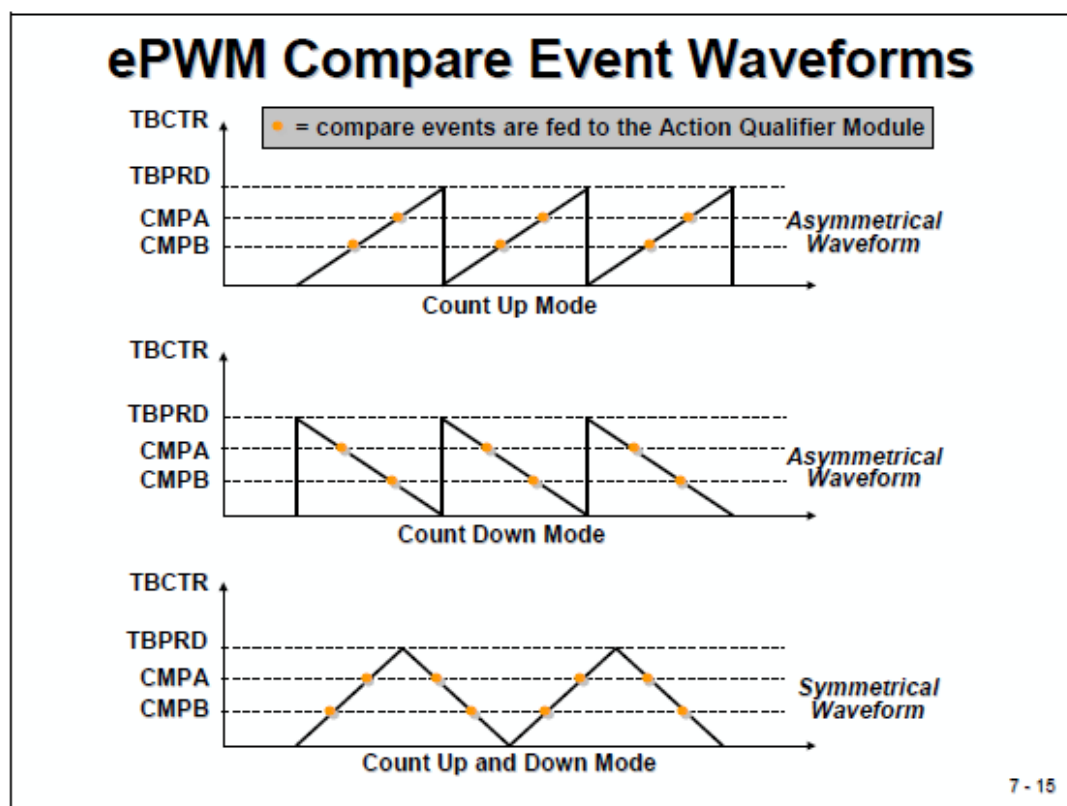


Figure 6: Timer Counter Modes

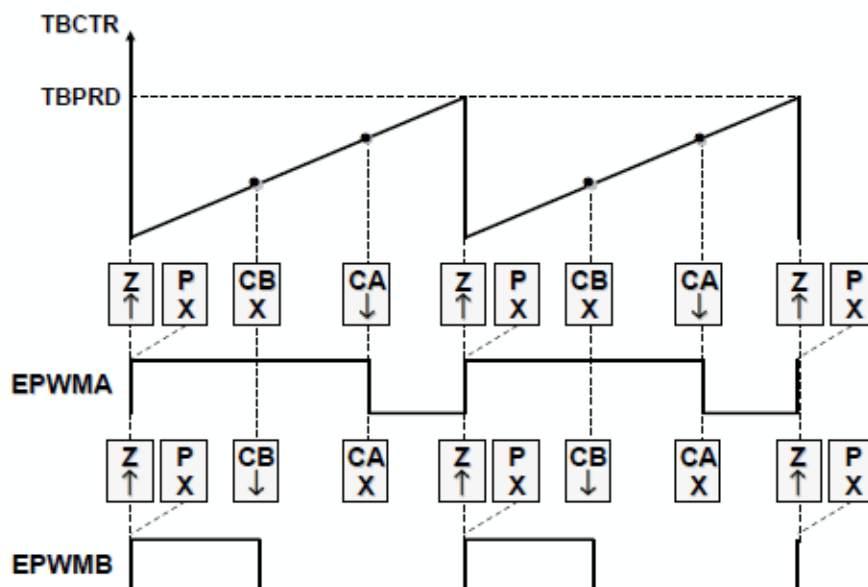
ePWM Action Qualifier Actions

S/W Force	Time-Base Counter equals:				EPWM Output Actions
	Zero	CMPA	CMPB	TBPRD	
SW X	Z X	CA X	CB X	P X	Do Nothing
SW ↓	Z ↓	CA ↓	CB ↓	P ↓	Clear Low
SW ↑	Z ↑	CA ↑	CB ↑	P ↑	Set High
SW T	Z T	CA T	CB T	P T	Toggle

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Figure 7: Action Qualifier

Independent Modulation on EPWMA / B



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Figure 8: How action qualifier works

Figure 2-29. Dead-Band Waveforms for Typical Cases ($0\% < \text{Duty} < 100\%$)

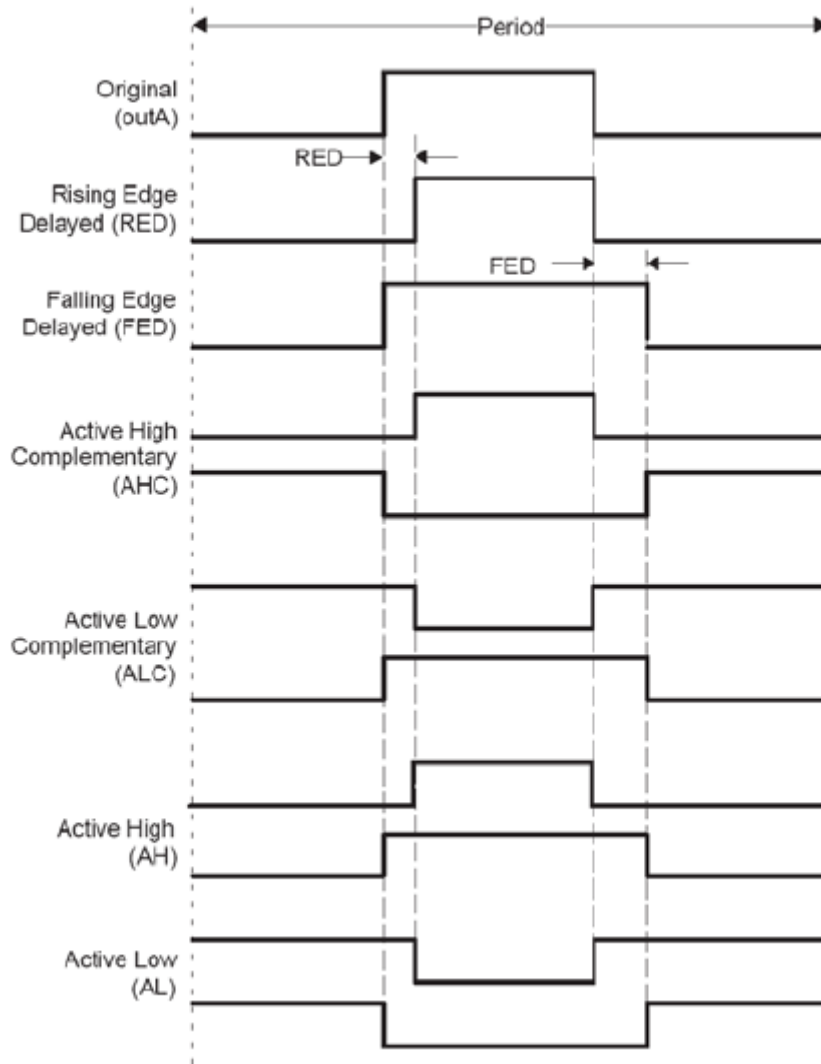


Figure 9: Dead Time

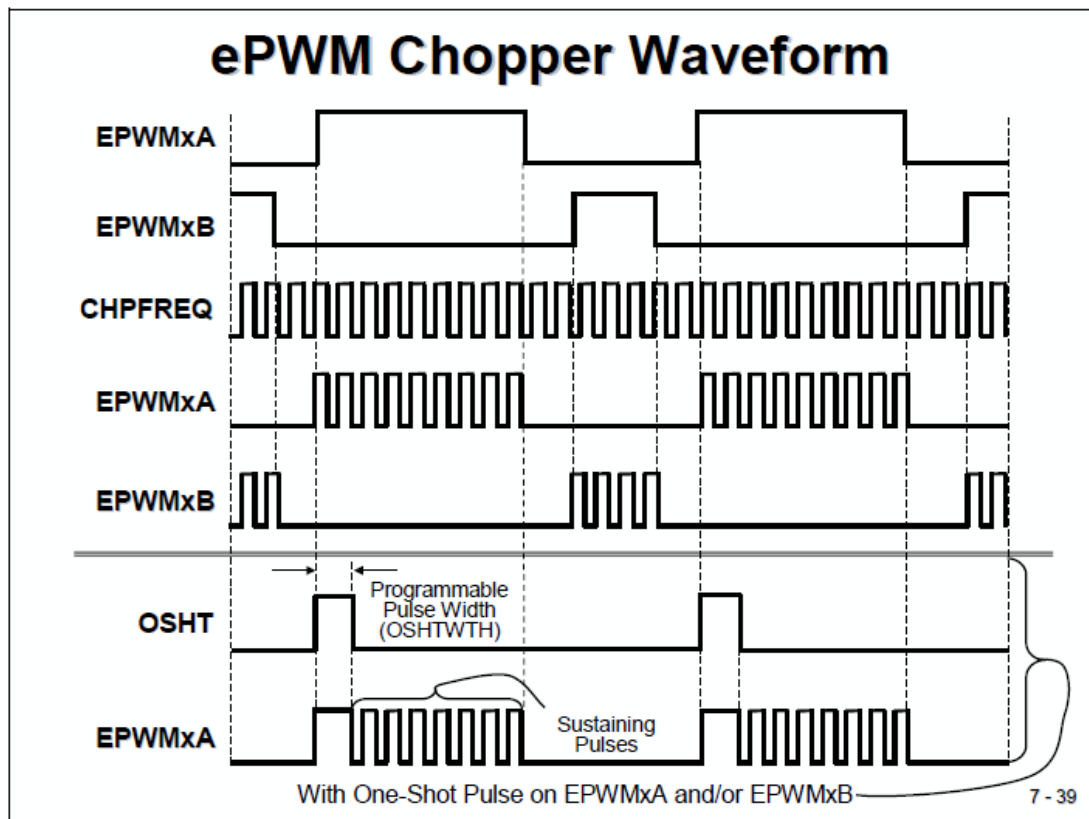


Figure 10: PWM Chopper

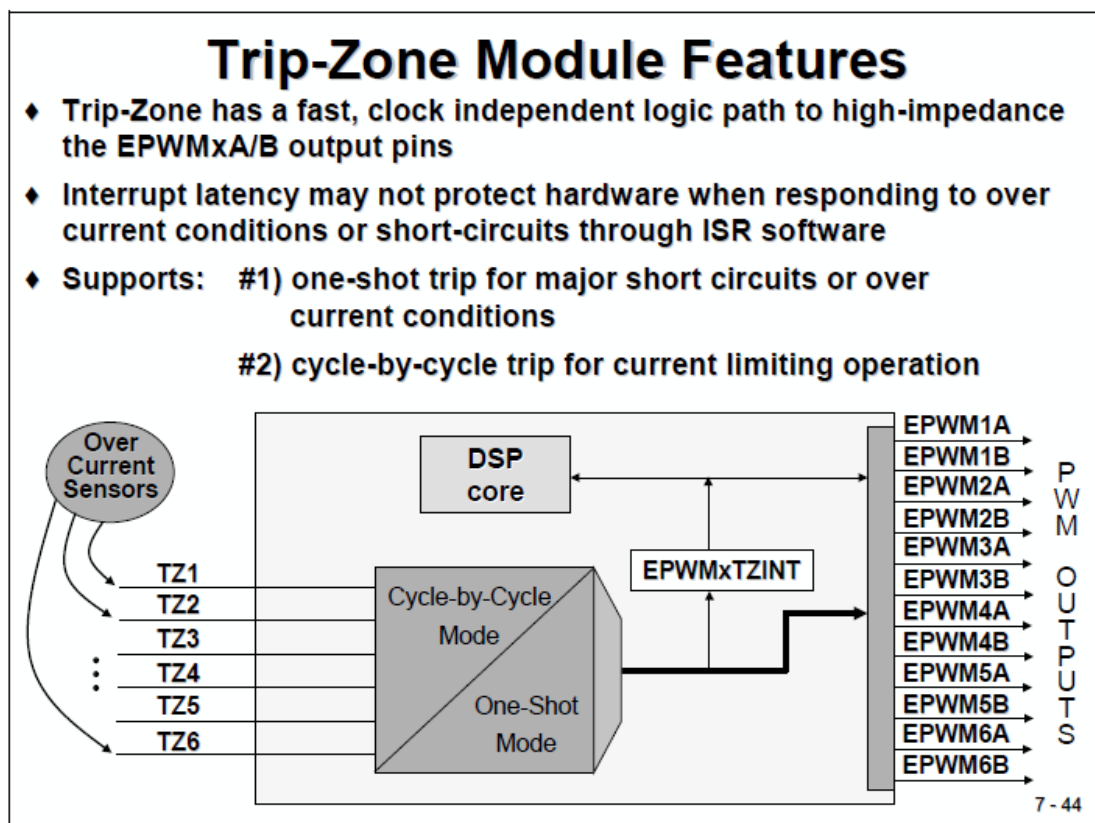


Figure 11: Trip Zone

ePWM Event-Trigger Interrupts and SOC

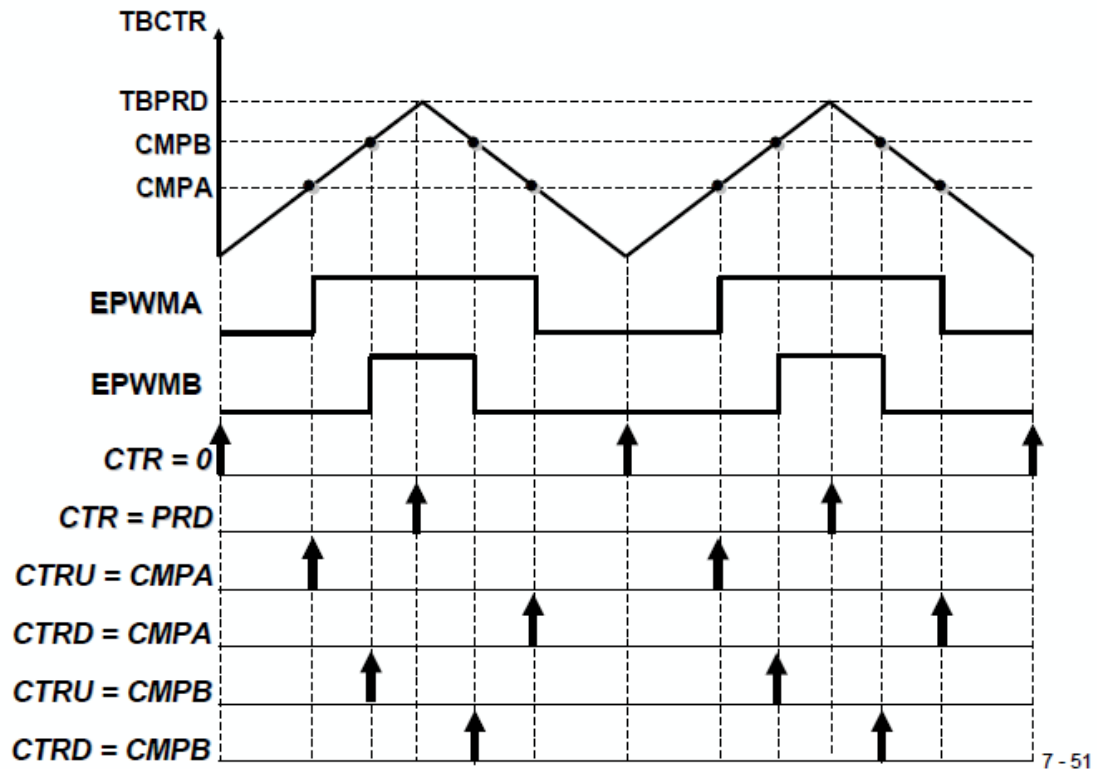


Figure 12: ePWM Interrupt