

State-space model of grid-connected inverters under current control mode

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Abstract: Growth of distributed generation has led to distribution systems with a mixture of rotating machine generators and inverter interfaced generators. The stability of such networks needs to be studied through the analysis of state-space models, and so suitable models of inverters are needed to complement the well-established models of rotating machines. As machine models include features such as automatic voltage regulators and wash-out functions, the inverter model also includes phase-locking functions and internal control loops. The model for voltage source inverters with an internal current control loop, an outer power regulation loop, a measurement of average power and a phase-locked loop has been developed. The model is presented in detail and is formed with a state-vector, similar to that used for rotating machines. The model includes nonlinear terms but can be linearised about an operating point. The state-space model is verified against a component-level time-step simulation in Simulink/PLECS.

1 Introduction

Many of the forms of new and renewable energy are not natural 50 or 60 Hz sources and the question arises of how to incorporate them into a standard electricity grid. Most of this technology (PV panels, high-speed micro-turbines and fuel cells) has an inverter as a generator interface. That inverter can operate either as a current source or as a voltage source is generally known. In either case, the implementation will normally be a voltage source inverter (VSI) and appropriate control loops will be added to make it appear as a controlled current source, if necessary. The inverter will be fitted with a coupling impedance and possibly further passive filter elements to attenuate the switching frequency components of voltage. The inverter will also require a means to measure exported power. The inverter must also be explicitly phase-locked through a phase-locked loop (PLL) or implicitly phase-locked through droop regulation. Thus, the inverter is a complex dynamic system, which interacts with the grid. In some circumstances, it will be necessary to establish the stability limits of the inverter and grid combination. For traditional generators, such as synchronous machines and induction (asynchronous) machines, there are well-established dynamic models, and for each type of stability study (covering various frequency ranges and types of interaction) an appropriate model can be selected that includes all of the necessary machine features and has a standard state vector. Many years of experience and design standardisation has led to the situation where power systems engineers know the appropriate features and modes to be included in each type of study. Inverter-interfaced generators are not yet that mature and the modelling of inverters for network

stability studies requires much more work, if grid stability is to be ensured with high penetrations of distributed generation (DG).

Component-level models (e.g. Spice-based simulators) allow the evaluation of inverter performance during every switching action [1]. Applications at this level of simulation detail include the calculation of switching losses and the testing of snubber networks [2]. This simulation approach offers an accurate representation of the system in the time domain but it can be highly time consuming to perform over the duration of a power frequency oscillation. Another approach to simulate the switching action of the power devices is to use the principle of switching functions [3, 4]. A disadvantage of this approach is that the switching instants of the devices must be known in advance. At the other end of the spectrum (in terms of the simulation detail to be used), there are applications in which modelling the whole inverter system as a simple voltage gain is good enough. In these applications, the inverter is normally a small part of a large system and the dynamics of the inverter have a small impact on the overall system.

Inverters are sometimes modelled in a space-state formulation with the power devices in the inverter represented as ideal switches [5]. Under this assumption, some accuracy is lost but the simulation time is normally improved by reducing both the time required by an analyst to set up a simulation and the time taken by a computer to produce the result. Space-state modelling of power converters is well documented in textbooks [6] and it has been used to simulate many different types of power converters. A common simplification is to linearise the switching circuit by creating a small-signal model and to consider the average behaviour of the switched circuit over a period. A small sample of the literature includes: DC–DC converters [7], AC–DC converters [8–10], DC–AC converters [11–14] and FACTS devices [15].

Despite the modelling efforts, to date, better VSI models are still needed for studies of DG integration. The model must represent all of the dynamics of the inverter in the frequency range of interest and allow coupling to reasonable

models of the distribution network. Models of VSIs for grid connection, normally, include the low pass (LC-based) filter required to attenuated switching frequency noise and the coupling impedance between the filter and connection node, in the distribution network. A complete model should also include the control system associated with the converter circuit [16–20]. This includes the PLL, the abc–dq transformations, the controllers for current and voltage and any low-pass filters applied to feedback or control signals (such as those used to obtain average power from instantaneous power or to attenuate noise in measured signals).

This paper presents a model of a three-phase inverter system, including a complete set of the sub-systems required for interfacing a DG source to a grid. The model is for current-control mode, which is one of the two main operation modes relevant to DG. It is presented in detail so that the meaning of the various parameters can be understood and can be related to features of real inverters. The validation of the model is carried out by comparing waveforms generated from the state-space model with those from a component level simulation in Simulink/PLECS.

The intended contribution of this paper is to provide verified models of inverter-interfaced generators to be used by a wider community by studying interactions between inverters and the power system to which they are connected.

2 VSI in current control mode

In situations where a strong network connection is available, that is, a connection point with an already tightly regulated voltage and frequency, it is common to export power into that connection through setting a current reference for the inverter. That reference can represent any combination of real and reactive power needs to be understood. The exported power will not significantly influence the voltage or frequency at the point of connection. A schematic of an inverter arranged in current-controlled mode is shown in Fig. 1 and follows the proposal made in [21]. The objective is to regulate real and reactive power to follow reference values (P^* and Q^*). The power references and the voltages at the point of connection (v_{Oa} , v_{Ob} , and v_{Oc}) are used to set references for the current controllers. The inverter is provided with a DC link voltage (V_{DC}) from the original energy source. These variables constitute the inputs to the model. The output variables of direct interest are the output currents (i_{Oa} , i_{Ob} and i_{Oc}). It might also be desirable to monitor the alignment angle of the PLL or, if the model is to be coupled to a model of the energy source, the DC link current (i_{DC}) can be arranged as an output. Fig. 1 shows that control is performed in a rotating reference frame (dq domain) and the reference angle for the abc–dq transformation is provided by the PLL. The inverter output is shown with a second-order LC element and an additional inductor for coupling to the connection point. The output current (i_{Odq}) is therefore equal to the current in the filter inductor minus the current in the shunt-connected capacitor of the filter. It is the inductor current i_L , which is controlled to follow the reference current (in turn determined by the power references) [22]. It is assumed that the current in the filter capacitor is of high frequency and, in terms of power export at fundamental frequency, the inductor and output currents are essentially identical. Other variables shown in Fig. 1 will be discussed in detail as the paper progresses.

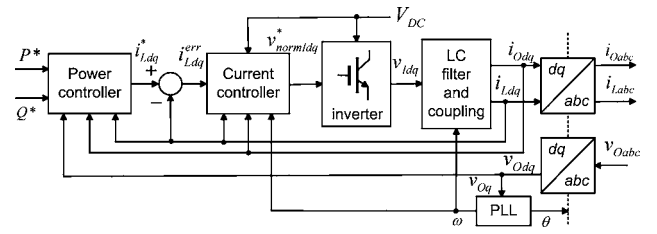


Fig. 1 Model of a current-controlled VSI

The objective is to build a single state-space model for all of the elements shown in Fig. 1 such that the dynamics of both the power processing elements and the associated local controls are included. This model will be of the form described by (1), which is a state-space form that allows nonlinear functions of x and u to be included. To accomplish this goal, in the following sub-sections a state-space model is derived for each sub-system. The sub-system models are, then, arranged into a complete model and duplicate variables eliminated

$$\begin{aligned}\dot{x} &= Ax + R(x, u) \\ y &= S(x, u)\end{aligned}\quad (1)$$

2.1 State-space model of the dq–abc transformation

The first sub-system in the inverter topology is the dq–abc transformation as defined by (2).

$$\begin{bmatrix} i_{Oa} \\ i_{Ob} \\ i_{Oc} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \cos \theta & -\sin \theta & \frac{1}{\sqrt{2}} \\ \cos(\theta - \frac{2\pi}{3}) & -\sin(\theta - \frac{2\pi}{3}) & \frac{1}{\sqrt{2}} \\ \cos(\theta + \frac{2\pi}{3}) & -\sin(\theta + \frac{2\pi}{3}) & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} i_{Od} \\ i_{Oq} \\ i_{Oo} \end{bmatrix} \quad (2)$$

The angle of the rotational frame is treated as an input to this sub-system and there are no state variables. Equation (2) represents a feed-through term from input to output and no state-vector is needed

$$\dot{x}_1 = [0] \quad (3)$$

2.2 State-space model of the PLL system

The PLL system indicated in Fig. 1 is given in more detail in Fig. 2.

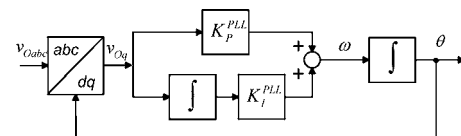


Fig. 2 Model for the PLL

The abc–dq transformation is defined by (4)

$$\begin{bmatrix} v_{Od} \\ v_{Oq} \\ v_{Oo} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \cos \theta & \cos(\theta - \frac{2\pi}{3}) & \cos(\theta + \frac{2\pi}{3}) \\ -\sin \theta & -\sin(\theta - \frac{2\pi}{3}) & -\sin(\theta + \frac{2\pi}{3}) \\ \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix} \times \begin{bmatrix} v_{Oa} \\ v_{Ob} \\ v_{Oc} \end{bmatrix} \quad (4)$$

In this paper, it is assumed that there is no path for the zero sequence components, therefore i_{Oo} in (2) and v_{Oo} in (4) could be ignored. However, these variables are left in the formulation to pave the way for a more general case.

The PLL form adopted here is based on aligning in closed-loop control the angle of the dq-transformation such that the voltage at the connection point has no q -axis component [23]. A PI regulator acts on the alignment error to set the rotation frequency (5) and that frequency is integrated to give the transformation angle (6).

$$\omega = K_p^{PLL} v_{Oq} + K_I^{PLL} \int v_{Oq} dt \quad (5)$$

$$\theta = \int \omega dt \quad (6)$$

To express (5) in a purely differential form, an additional variable is defined: $\Phi_{PLL} = \int v_{Oq} dt$. This variable does not have a particular physical meaning but it facilitates the development of the state-space model (and has the same dimensions as magnetic flux). The additional variable is a part of the state-vector together with θ (taken from (6)): $\mathbf{x}_2 = [\theta \ \Phi_{PLL}]^T$. The input variable to the PLL is $\mathbf{u}_2 = [v_{Oa} \ v_{Ob} \ v_{Oc}]^T$ and the output, which includes the dq transformation, is $\mathbf{y}_2 = [\theta \ v_{Od} \ v_{Oq}]^T$.

With the above definitions, the state-space model of PLL loop can then be written in the form $\dot{\mathbf{x}}_2 = \mathbf{A}_2 \mathbf{x}_2 + \mathbf{R}_2(\mathbf{x}_2, \mathbf{u}_2)$ as follows

$$\dot{\mathbf{x}}_2 = \begin{bmatrix} 0 & K_T^{PLL} \\ 0 & 0 \end{bmatrix} \begin{bmatrix} 0 \\ \Phi_{PLL} \end{bmatrix} + \begin{bmatrix} K_p^{PLL} v_{Oq} \\ v_{Oq} \end{bmatrix} \quad (7)$$

where v_{Oq} is given in (4) and is a multiplication of state θ with input v_{Oabc} .

2.3 State-space model of the power controller

The power controller is not a closed-loop controller but instead is an open-loop conversion of a power reference into a current reference, given the prevailing voltage at the point of connection. The current is then controlled in closed-loop as given in the next section. This sub-system is nonlinear because of the need to divide power by voltage to obtain current and it contains higher-order dynamics because of the filter employed. The sub-system is illustrated in Fig. 3.

The desired output currents are calculated using the following pair of equations

$$i_{Od}^* = \frac{v_{Od} P^* - v_{Oq} Q^*}{v_{Od}^2 + v_{Oq}^2} \quad i_{Oq}^* = \frac{v_{Oq} P^* + v_{Od} Q^*}{v_{Od}^2 + v_{Oq}^2} \quad (8)$$

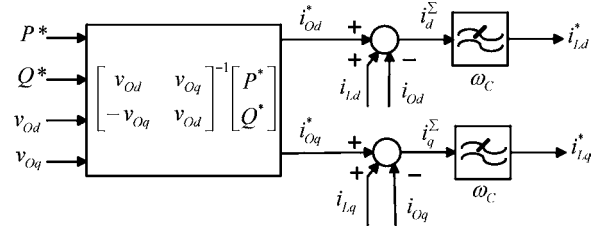


Fig. 3 Power controller [22]

It is actually the filter inductor current that is controlled and so an adjustment must be made to the reference to account for the capacitor current. This is performed using the measured inductor current and measured output current

$$\begin{aligned} i_d^\Sigma &= i_{Od}^* + i_{Cd} = i_{Od}^* + (i_{Ld} - i_{Od}) \\ i_q^\Sigma &= i_{Oq}^* + i_{Cq} = i_{Oq}^* + (i_{Lq} - i_{Oq}) \end{aligned} \quad (9)$$

These current references might contain harmonic and noise terms as a consequence of distortion of the voltage at the point of connection. These are removed from the reference by a low-pass filter. Because of the relatively low cut-off frequency required of this filter it can make an important contribution to the dynamics of the system and should be included in the model. A second-order Butterworth low-pass transfer function is taken as a representative of the filter likely to be used. Equation (10) shows the references for the current controllers, i_{Ld}^* and i_{Lq}^* , formed by the product of a generic filter and the terms in (9)

$$\begin{aligned} i_{Ld}^* &= \frac{\omega_c^2}{s^2 + \sqrt{2}s\omega_c + \omega_c^2} i_d^\Sigma, \\ i_{Lq}^* &= \frac{\omega_c^2}{s^2 + \sqrt{2}s\omega_c + \omega_c^2} i_q^\Sigma \end{aligned} \quad (10)$$

Equation (10) can be re-arranged as in (11) and then re-written in integro-differential form as in (12)

$$\begin{aligned} i_{Ld}^* s + i_{Ld}^* \sqrt{2}\omega_c + i_{Ld}^* \frac{1}{s} \omega_c^2 &= \frac{1}{s} \omega_c^2 i_d^\Sigma \\ i_{Lq}^* s + i_{Lq}^* \sqrt{2}\omega_c + i_{Lq}^* \frac{1}{s} \omega_c^2 &= \frac{1}{s} \omega_c^2 i_q^\Sigma \end{aligned} \quad (11)$$

$$\begin{aligned} \frac{di_{Ld}^*}{dt} &= \omega_c^2 \int (i_d^\Sigma - i_{Ld}^*) dt - \omega_c \sqrt{2} i_{Ld}^* \\ &= \omega_c^2 q_{3d} - \omega_c \sqrt{2} i_{Ld}^* \\ \frac{di_{Lq}^*}{dt} &= \omega_c^2 \int (i_q^\Sigma - i_{Lq}^*) dt - \omega_c \sqrt{2} i_{Lq}^* \\ &= \omega_c^2 q_{3q} - \omega_c \sqrt{2} i_{Lq}^* \end{aligned} \quad (12)$$

Equations (8), (9) and (12) together describe the power controller. They can be assembled into state-space form by introducing new variables for the integrals in (12). Since these are integrations of current, the new variables are denoted as charges: $q_{3d} = \int (i_d^\Sigma - i_{Ld}^*) dt$ and $q_{3q} = \int (i_q^\Sigma - i_{Lq}^*) dt$. The state-variable is then

$\mathbf{x}_3 = [i_{Ld}^* \ i_{Lq}^* \ q_{3d} \ q_{3q}]^T$ and the input vector is $\mathbf{u}_3 = [P^* \ Q^* \ v_{Od} \ v_{Oq} \ i_{Ld} \ i_{Lq} \ i_{Od} \ i_{Oq}]^T$. However, because of the form of (8), the inputs cannot be included as linear terms into the state-space model of the system; and therefore this model takes the form $\dot{\mathbf{x}}_3 = \mathbf{A}_3\mathbf{x}_3 + \mathbf{R}_3(\mathbf{u}_3)$. The full equation is as follows

$$\dot{\mathbf{x}}_3 = \begin{bmatrix} -\sqrt{2}\omega_c & 0 & \omega_c^2 & 0 \\ 0 & -\sqrt{2}\omega_c & 0 & \omega_c^2 \\ -1 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 \end{bmatrix} \begin{bmatrix} i_{Ld}^* \\ i_{Lq}^* \\ q_{3d} \\ q_{3q} \end{bmatrix} + \mathbf{R}_3(\mathbf{u}_3) \quad (13)$$

where $\mathbf{R}_3(\mathbf{u}_3)$ is the nonlinear function of the input variables

$$\mathbf{R}_3(\mathbf{u}_3) = \begin{bmatrix} 0 \\ 0 \\ i_{d\sum}^* \\ i_{q\sum}^* \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ \frac{v_{Od}P^* - v_{Oq}Q^*}{v_{Od}^2 + v_{Oq}^2} + i_{Ld} - i_{Od} \\ \frac{v_{Oq}P^* + v_{Od}Q^*}{v_{Od}^2 + v_{Oq}^2} + i_{Lq} - i_{Oq} \end{bmatrix} \quad (14)$$

The output vector for the power regulator is composed of the two reference currents $\mathbf{y}_3 = [i_{Ld}^* \ i_{Lq}^*]$.

2.4 State-space model of the current controller

The current controller indicated in Fig. 1 is implemented as a pair of PI controllers together with cross-axis decoupling terms and feed-forward terms for the connection voltage. Because the inverter represents (on average) a gain equal to the DC link voltage, it is desirable to include a division by the DC link voltage so that the inverter remains a normalised gain term, even if the DC link voltage varies. The controller block diagram is shown in Fig. 4.

From Fig. 4, the following equations can be obtained

$$v_{Id}^* = v_{Od} - \omega L i_{Lq} + K_P^{d2} i_{Ld}^{err} + K_I^{d2} \int i_{Ld}^{err} dt \quad (15)$$

$$v_{Iq}^* = v_{Oq} - \omega L i_{Ld} + K_P^{q2} i_{Lq}^{err} + K_I^{q2} \int i_{Lq}^{err} dt \quad (16)$$

The frequency (ω) is taken from the PLL and is treated as an input, here.

Again, new variables are introduced to obtain the state-space model: $q_{Ld}^{err} = \int i_{Ld}^{err} dt$ and $q_{Lq}^{err} = \int i_{Lq}^{err} dt$. The state and input vectors of the current controller are given

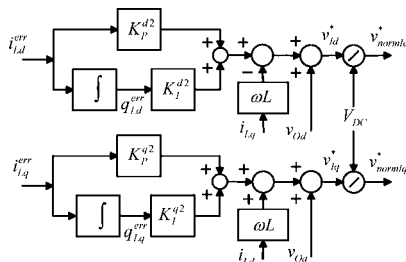


Fig. 4 Model for the current controller

by (17) and (18), respectively.

$$\mathbf{x}_4 = [q_{Ld}^{err} \ q_{Lq}^{err}]^T \quad (17)$$

$$\mathbf{u}_4 = [i_{Ld}^{err} \ i_{Lq}^{err} \ i_{Ld} \ i_{Lq} \ v_{Od} \ v_{Oq} \ \omega]^T \quad (18)$$

The state-space model is in standard linear form, $\dot{\mathbf{x}}_4 = \mathbf{A}_4\mathbf{x}_4 + \mathbf{B}_4\mathbf{u}_4$, but because the two new states of this sub-system do not involve internal feedback, the \mathbf{A} matrix is zero. The model is given in (19)

$$\dot{\mathbf{x}}_4 = \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} q_{Ld}^{err} \\ q_{Lq}^{err} \end{bmatrix} + \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 \end{bmatrix} \times \begin{bmatrix} i_{Ld}^{err} \\ i_{Lq}^{err} \\ i_{Ld} \\ i_{Lq} \\ v_{Od} \\ v_{Oq} \\ \omega \end{bmatrix} \quad (19)$$

The output equation is in standard form except for the division by the DC link voltage

$$\mathbf{y}_4 = \frac{1}{v_{DC}} (\mathbf{C}\mathbf{x} + \mathbf{D}\mathbf{u} + \mathbf{S}(\mathbf{u}))$$

$$\mathbf{y}_4 = \frac{1}{v_{DC}} \left(\begin{bmatrix} K_T^{d2} & 0 \\ 0 & K_T^{q2} \end{bmatrix} \begin{bmatrix} q_{Ld}^{err} \\ q_{Lq}^{err} \end{bmatrix} \right.$$

$$+ \begin{bmatrix} K_P^{d2} & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & K_P^{q2} & 0 & 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} i_{Ld}^{err} \\ i_{Lq}^{err} \\ i_{Ld} \\ i_{Lq} \\ v_{Od} \\ v_{Oq} \\ \omega \end{bmatrix} + \begin{bmatrix} -\omega L i_{Lq} \\ \omega L i_{Ld} \end{bmatrix} \Bigg) \quad (20)$$

2.5 State-space model of the inverter

The inverter itself is a switch-mode circuit and, for the purpose of this model development, it is assumed that the switching frequency is sufficiently high that the switching action itself does not affect the evolution of the states, and the state-space averaging can be applied. In other words, the switching frequency ripple of states such as the inductor currents can be ignored without significantly affecting the accuracy with which the average inductor current is predicted. It is expected that the passive low-pass filter at the output of the inverter will have been chosen to attenuate switching frequency ripple to acceptable levels. As switching frequencies of commercial DG inverters continue to improve, this approximation becomes better. There may be some high-power inverters with low switching frequencies (<1 kHz) where the physical size and the cost of the filter have been constrained and where the state-space averaging assumptions are not strictly valid.

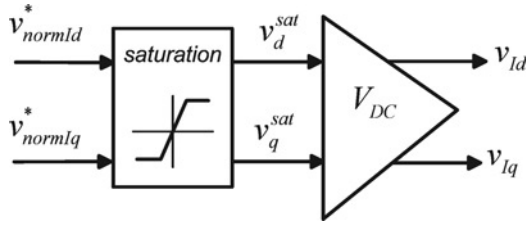


Fig. 5 Simplified model of the inverter

With the switching action of the inverter neglected, the model of the inverter can be simplified to a saturated voltage gain, as shown in Fig. 5.

Saturation arises because there is a finite DC-link voltage, which in turn produces a magnitude-constrained AC voltage at the output. Because the saturation function is a nonlinear element, it is not included in the linear differential equations. Voltage saturation is, however, an important characteristic of VSI and it should be modelled in time-step simulations, used to verify results from linearised s -domain models. Saturation can be included in time-step simulations with if-then-else commands, as shown in Table 1.

The voltage gain model of the VSI in state-space variables is simply

$$\dot{\mathbf{x}}_5 = [0] \quad (21)$$

The output is $\mathbf{y}_5 = [v_{ld} \ v_{lq}]$

2.6 State-space model of LC filter and coupling impedance

Although the switching frequency ripple has been dismissed from the model, the passive low-pass filter must still be included in it. The selection of the inductors and capacitors of this filter involves some design compromise and the filter will have important dynamic effects down to quite low frequencies. This sub-system includes both the coupling inductance that is always present and any additional filter components. The coupling impedance might in reality be a coupling inductor or the leakage impedance of a coupling transformer. If this is indeed a coupling inductor, the LC filter becomes a LCL filter. How to design the output filters is out of the scope of this paper; detailed analyses of these filters can be found in [24, 25].

The LC filter (assumed symmetric) in the abc reference frame is shown in Fig. 6.

The equations describing phase a of the filter are presented in (22)–(24).

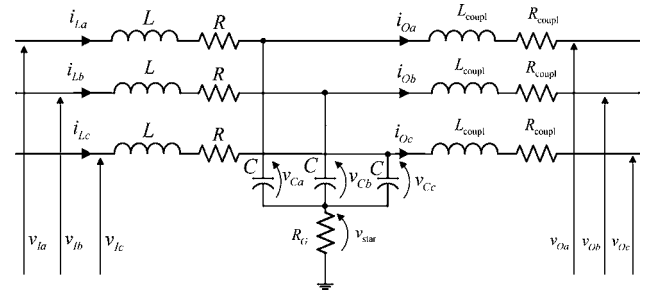


Fig. 6 LC filter and load

$$v_{la} = i_{La}R + L \frac{di_{La}}{dt} + v_{Ca} + v_{star} \quad (22)$$

$$v_{Oa} = v_{Ca} + v_{star} - L_{coupl} \frac{di_{Oa}}{dt} - i_{Oa}R_{coupl} \quad (23)$$

$$C \frac{dv_{Ca}}{dt} = i_{La} - i_{Oa} \quad (24)$$

$$v_{star} = 3R_G(i_{La} - i_{Oa}) \quad (25)$$

To create a general filter model, the star-point of the capacitors is connected to ground by an impedance. Selecting a low value impedance would model a four-wire filter and a high-value impedance would model a three-wire filter. The input, output and state vectors of the filter in dq form are

$$\mathbf{u}_6 = [v_{ld} \ v_{lq} \ v_{Io} \ v_{Od} \ v_{Oq} \ v_{Oo} \ \omega]^T$$

$$\mathbf{y}_6 = [i_{Ld} \ i_{Lq} \ i_{Lo} \ i_{Od} \ i_{Oq} \ i_{Oo}]^T$$

$$\mathbf{x}_6 = [i_{Ld} \ i_{Lq} \ i_{Lo} \ v_{Cd} \ v_{Cq} \ v_{Co} \ i_{Od} \ i_{Oq} \ i_{Oo}]^T$$

The matrices \mathbf{A}_6 , \mathbf{B}_6 and \mathbf{R}_6 in the state-space model $\dot{\mathbf{x}}_6 = \mathbf{A}_6\mathbf{x}_6 + \mathbf{B}_6\mathbf{u}_6 + \mathbf{R}_6(\mathbf{x}_6, \mathbf{u}_6)$ are found to be

$$\mathbf{A}_6 = \begin{bmatrix} \frac{-R}{L} & 0 & 0 & \frac{-1}{L} & 0 \\ 0 & \frac{-R}{L} & 0 & 0 & \frac{-1}{L} \\ 0 & 0 & \frac{-3R_G - R}{L} & 0 & 0 \\ \frac{1}{C} & 0 & 0 & 0 & 0 \\ 0 & \frac{1}{C} & 0 & 0 & 0 \\ 0 & 0 & \frac{1}{C} & 0 & 0 \\ 0 & 0 & 0 & \frac{1}{L_{coupl}} & 0 \\ 0 & 0 & 0 & 0 & \frac{1}{L_{coupl}} \\ 0 & 0 & 0 & 0 & 0 \end{bmatrix} \quad (26)$$

$$\mathbf{B}_6 = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ \frac{-1}{L} & 0 & 0 & \frac{3R_G}{L} \\ 0 & \frac{-1}{C} & 0 & 0 \\ 0 & 0 & \frac{-1}{C} & 0 \\ 0 & 0 & 0 & \frac{-1}{C} \\ 0 & \frac{-R_{coupl}}{L_{coupl}} & 0 & 0 \\ 0 & 0 & \frac{-R_{coupl}}{L_{coupl}} & 0 \\ \frac{3R_G}{L_{coupl}} & 0 & 0 & \frac{-3R_G - R_{coupl}}{L_{coupl}} \end{bmatrix}$$

Table 1: Computer-based model of saturation

Saturation
if $(v_{normld}^*)^2 + (v_{normlq}^*)^2 \geq \frac{1}{2}$
$\theta = \tan^{-1} \frac{v_{normlq}^*}{v_{normld}^*}$
$v_d^{sat} = \frac{1}{\sqrt{2}} \cos(\theta)$
$v_q^{sat} = \frac{1}{\sqrt{2}} \sin(\theta)$
else $v_d^{sat} = v_{normld}^*$
$v_q^{sat} = v_{normlq}^*$
End

$$B_6 = \begin{bmatrix} \frac{1}{L} & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & \frac{1}{L} & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & \frac{1}{L} & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & \frac{-1}{L_{\text{coupl}}} & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & \frac{-1}{L_{\text{coupl}}} & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & \frac{-1}{L_{\text{coupl}}} & 0 & 0 \end{bmatrix}$$

$$R_6 = \begin{bmatrix} \omega i_{Lq} \\ -\omega i_{Ld} \\ 0 \\ \omega v_{Cq} \\ -\omega v_{Cd} \\ 0 \\ \omega i_{Oq} \\ -\omega i_{Od} \\ 0 \end{bmatrix} \quad (27)$$

The output matrix is

$$C_6 = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix} \quad (28)$$

3 Complete model

Fig. 7 is a re-drawing of Fig. 1, but with the state variables of each sub-system identified ready to form the overall model of a current-controlled inverter (CCI) that was envisaged in (1). The inputs of the system can readily be assembled into the input vector (\mathbf{u}) given by (29).

$$\mathbf{u}_{CCI} = [V_{DC} \quad v_{Oa} \quad v_{Ob} \quad v_{Oc} \quad P^* \quad Q^*]^T \quad (29)$$

The state variables also need to be combined into a single state vector as given by (30). The first two state variables, θ and Φ_{PLL} , are contributed by the PLL system. Four state variables arise from the two second-order Butterworth filters in the power controller: i_{Ld}^* , i_{Lq}^* , q_{3d} and q_{3q} . Variables q_{Ld}^{err} and q_{Lq}^{err} are the result of the integrators in

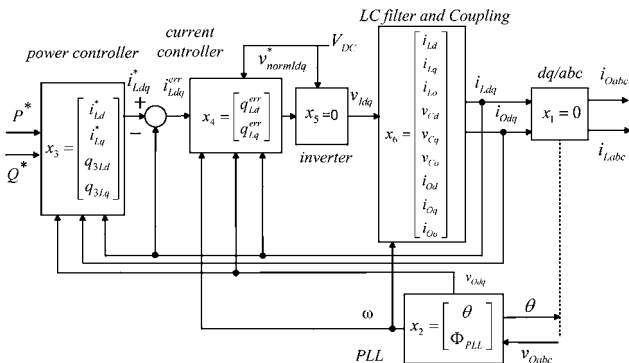


Fig. 7 Topology of a CCI with the state vectors of each sub-system

the current controller. The states, i_{Ld} , i_{Lq} , i_{Lo} , v_{Cd} , v_{Cq} , and v_{Co} , appear due to the reactive elements of LC filter and, finally, i_{Od} , i_{Oq} and i_{Oo} are contributed by the coupling reactance

$$\mathbf{x}_{CCI} = [\theta \quad \Phi_{PLL} \quad i_{Ld}^* \quad i_{Lq}^* \quad q_{3Lq} \quad q_{3Lq} \quad q_{Ld}^{err} \quad q_{Lq}^{err} \quad i_{Ld} \quad i_{Lq} \quad i_{Lo} \quad v_{Cd} \quad v_{Cq} \quad v_{Co} \quad i_{Od} \quad i_{Oq} \quad i_{Oo}]^T \quad (30)$$

Matrix A_{CCI} for the whole system is obtained by combining together the A matrices of the PLL, the power controller, the current controller and the LC filter. This is because these four sub-systems are the only ones with the state variables in their models. Equation (31) illustrates how the matrices are combined

$$A_{CCI} = \begin{bmatrix} [A_2] & 0 & 0 & 0 \\ 0 & [A_3] & 0 & 0 \\ 0 & 0 & [A_4] & 0 \\ 0 & 0 & 0 & [A_6] \end{bmatrix} \quad (31)$$

There are several simplifications to be made to the resulting matrix A_{CCI} to make it more compact. The inductor currents and their reference values occur in several places in the model such as in the error terms of the current controllers and as state variables in the power regulator. Furthermore, they are integrated to provide further state variables. The terms in A_3 and A_4 and in R_3 and B_4 can be simplified on combination to A_{34} and R_{34} as shown in (32)

$$\dot{\mathbf{x}}_{34} = \begin{bmatrix} -\sqrt{2}\omega_c & 0 & \omega_c^2 & 0 & 0 & 0 \\ 0 & -\sqrt{2}\omega_c & 0 & \omega_c^2 & 0 & 0 \\ -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} i_{Ld}^* \\ i_{Lq}^* \\ q_{3d} \\ q_{3q} \\ q_{Ld}^{err} \\ q_{Lq}^{err} \end{bmatrix} + R_{34}(\mathbf{u}_{34}) \quad (32)$$

$$R_{34}(\mathbf{u}_{34}) = \begin{bmatrix} 0 \\ 0 \\ \frac{v_{Od}P^* - v_{Oq}Q^*}{v_{Od}^2 + v_{Oq}^2} + i_{Lq} - i_{Od} \\ \frac{v_{Oq}P^* - v_{Od}Q^*}{v_{Od}^2 + v_{Oq}^2} + i_{Ld} - i_{Oq} \\ -i_{Ld} \\ -i_{Lq} \end{bmatrix} \quad (33)$$

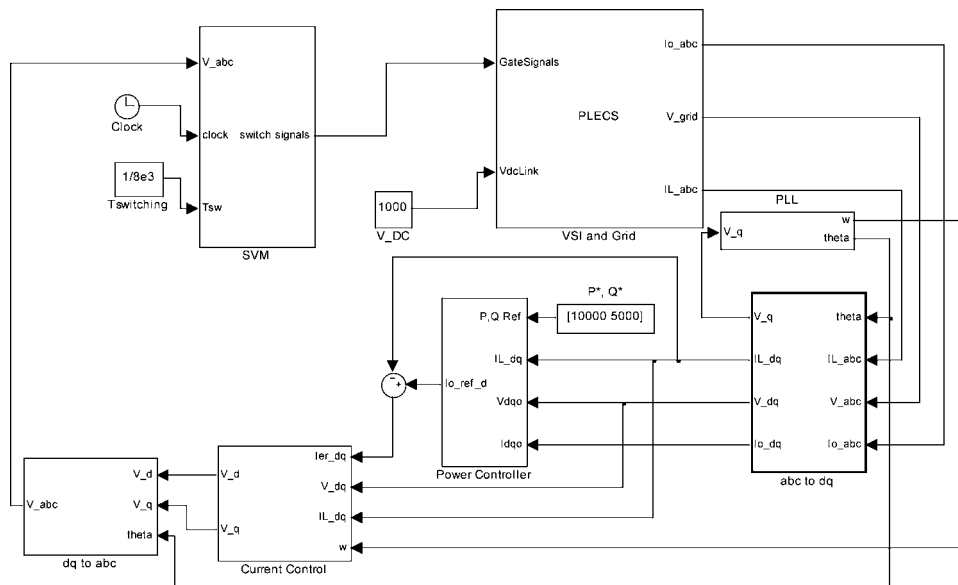
Furthermore, examination of $R_{34}(\mathbf{u}_{34})$ reveals that i_{Ldq} and i_{Odq} are states of one of the other sub-systems and so the terms associated with them should appear in the A_{CCI} matrix of the complete system. Incorporating these modifications into (31) gives the complete matrix A_{CCI} defined in (34) for the CCI. The nonlinear dependencies found in the inverter system model, such as the products of states, and the input terms are collected in $R_{CCI}(\mathbf{x}_{CCI}, \mathbf{u}_{CCI})$ as summarised in (35). An important nonlinearity to consider is in the calculation of the terms v_{ld} and v_{lq} , which includes the possible application of the saturation limit.

Table 2: Simulation setup

parameter	value
nominal phase voltage	240 V
grid frequency	50 Hz
coupling impedance	$(0.131 + j\omega 0.96 \times 10^{-3}) \Omega$
DC bus voltage	1000 V
filter	
inductance	1.35 mH
capacitance	50 μ F
resistance	0.056 Ω
switching frequency	8 kHz
reference active power	10 kW
reference reactive power	5 kVar
current	
proportional gain in	1
controller	
branch d K_P^d	
integral gain in branch	460
d K_I^d	
proportional gain in	1
branch q K_P^q	
integral gain in branch	460
q K_I^q	
PLL	
proportional gain K_P	2.1
Integral gain K_I	5000

4.2 Component-level comparator model

Fig. 8 shows the overall Simulink model of the grid-connected inverter. The ‘PLL’, ‘Power Controller’ and ‘Current control’ correspond to Figs. 2–4, respectively. The block ‘abc–dq’ implements the dq transformation given in (4), and the block ‘space vector modulation (SVM)’ contains a MATLAB script, which produces the power transistor gate signals from 2-axis voltages in stationary reference frame (v_α and v_β) using the SVM technique. (The SVM process and the transistor switching action were explicitly excluded from the the state-space model.) The gate signals are then applied to an inverter circuit created in the PLECS extension to Simulink as shown in Fig. 9.

**Fig. 8** Simulink model of current controlled VSI with its ancillary circuits

5 Comparison of results

5.1 Initial transient

At start up, there is an initial transient as the controllers act to establish the requested power flow. Both models were given power references of $P^* = 10$ kW, $Q^* = 5$ kVar. The inductor currents in abc form are shown in Fig. 10a. The upper graph shows the results from the PLECS/Simulink model and the lower graph shows those from the state-space model. It is clear that both the initial transient and steady-state sinusoidal currents present the same evolution in time, in each case, but that the Simulink model has a high-frequency ripple superimposed on the basic current waveform. This is the ripple arising from the switching action of the inverter and was explicitly excluded from the state-space model.

The inductor currents are internal states and will not normally be used as outputs of the model in a system study. Fig. 10b shows the output currents, that is, the currents that flow in the grid connection (again in abc form); here the PLECS/Simulink results show very little switching frequency ripple because the LC filter has attenuated these components. This justifies the argument presented in Section 2.5 that at the connection point, the switching ripple is relatively small and in most cases it can be neglected. The lower graph in Fig. 10b confirms that the state-space model gives an accurate representation of the system. The output powers from both models are presented in Fig. 11 and again show good agreement.

5.2 Step change in load

A series of further tests were conducted to illustrate the type of study that the state-space model facilitates. The response of the inverter to changes in power reference is an important performance criterion. Here it was tested with a series of step changes. The initial reference values of the active and reactive powers were 10 kW and 5 kVar. The active power was stepped up to 20 kW at 0.1 s, and stepped down to 10 kW at 0.35 s. The reactive power was stepped down to 2 kVar at 0.2 s, and stepped up to 5 kVar at 0.45 s. The

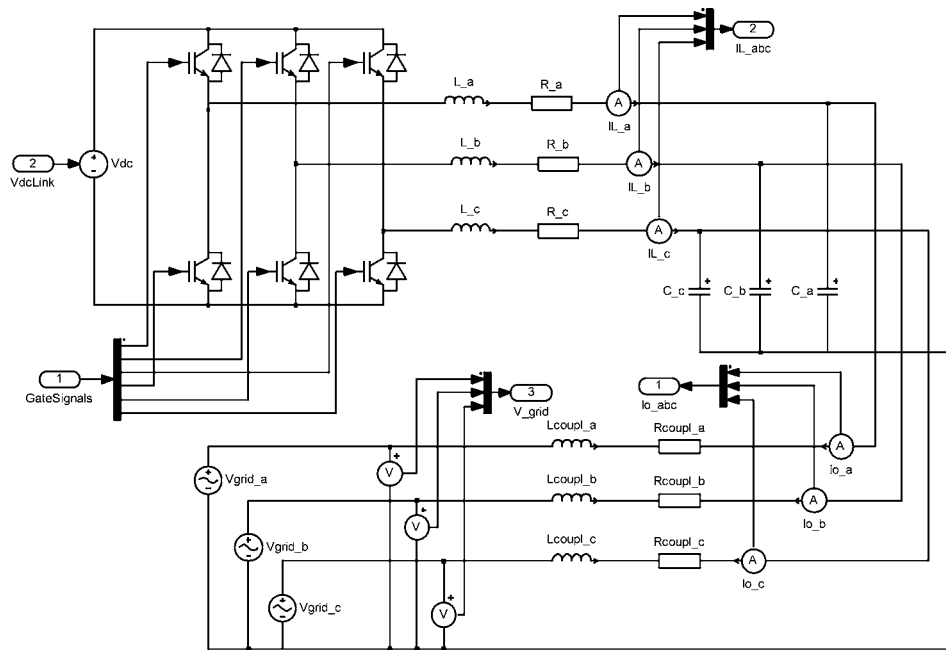
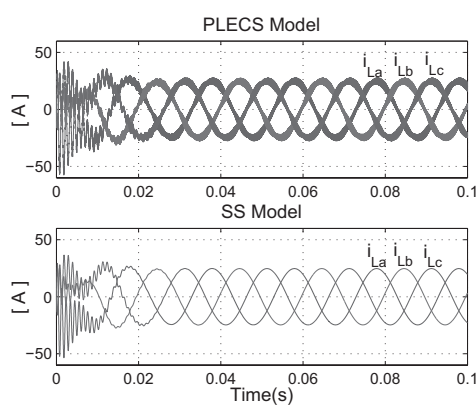
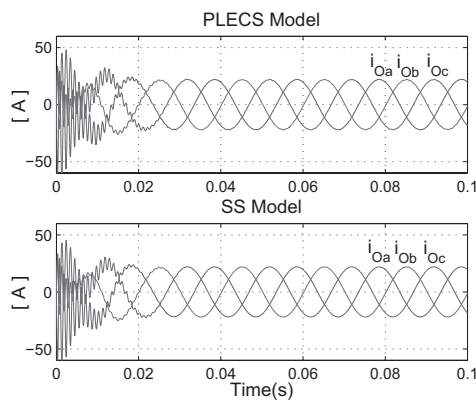


Fig. 9 PLECS model of inverter, LC filter and coupling impedance



a



b

Fig. 10 Behaviour during the initial transient in the abc reference frame

a Inductor currents
b Output currents

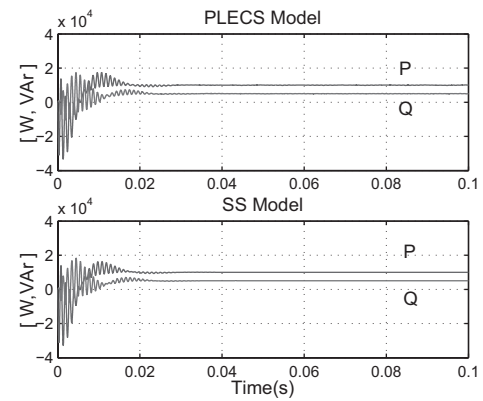


Fig. 11 Output power

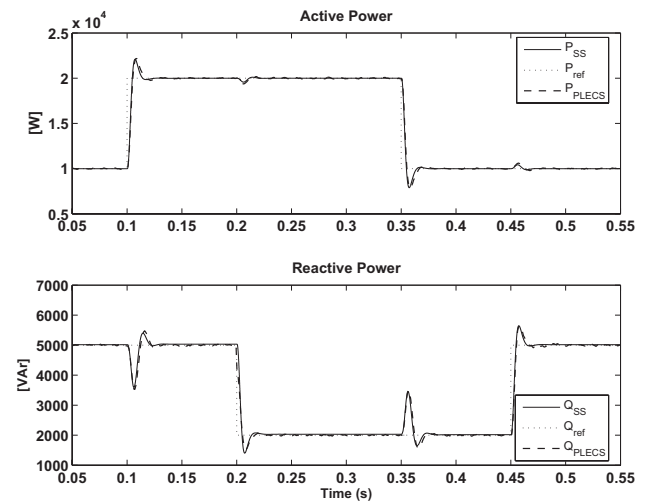


Fig. 12 Reference and actual active and reactive power

resulting output powers of the system together with the reference powers are shown in Fig. 12 (the initial 0.05 s containing the start-up transient is not shown). It can be seen that the response to the changes in reference values is fast and

without significant overshoot, but that there is some coupling between the active and reactive power (e.g. the step change of P at 0.1 results in a negative excursion in Q), which might be tackled through changes to the current controller.

6 Conclusions

An appropriate level of detail is needed in a model for each purpose to which it is put. A state-space model has been presented that enables transient stability of a grid connected inverter to be studied. The model is in state-space form and specifically avoids simulation of switching frequency effects. It has been shown to capture the important dynamics of the passive output filter, the current controllers, the filter in the power reference to current reference conversion and the controller that establishes phase-locking. This phase-locked and current-controlled form of inverters are common for DG connected to relatively strong grids. The model has been built up in stages, or subsystems, so that it can be related to practical implementations of inverters. The state-space form allows coupling of this model to standard models of distribution grids and coupling of several inverters in one system. The state vector has a similar form to the state vector of standard models of rotating machines. Thus, full dynamic models of complex systems can be built and stability of the system assessed through either time-domain technique or, after linearisation, frequency-domain techniques. The model can also be coupled to models of the original DC energy source through the DC-link port provided. Participation analysis will allow identification of appropriate inverter features that influence oscillatory modes of a system. Through experience with various inverter implementations it will be possible to establish general rules for enhancing the stability of networks that include inverter-interfaced DG.

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