

Carrier Phase Shift Method of SPWM for Concurrent Wired and Wireless Power Transfer Systems

Enes Ayaz, Ogün Altun, and Ozan Keysan

Abstract—This paper presents an approach for concurrent power transfer to wired and wireless systems using just a single inverter. The approach utilizes a novel carrier phase-shift (CPS) method that independently controls the inverter output voltages at the fundamental and switching frequencies. This proposed method can be a cost-effective solution to wireless power transfer (WPT) systems used in contactless slip rings (CSR), which transfer power to auxiliary loads such as sensors, radars, and IoT devices. There are two separate converters in conventional CSR systems: one is for the motor drive, and the other is for the WPT system. It is proposed that the switching harmonics of the motor drive can also be utilized to excite the WPT system while the low-frequency component can still be used to drive the motor. In order to control these independently, the CPS method is introduced. The proposed method is investigated analytically for sinusoidal-PWM (SPWM). Then, an experimental setup consisting of a 3-phase 3-wire GaN-based inverter and a 3-phase motor is built. Experimental results show that the WPT and motor systems are operated concurrently, and their powers are controlled independently by the proposed method.

Index Terms—Wireless power transfer, inductive power transfer, motor drive, concurrent power transfer, dual-band power transfer, carrier phase shift.

LIST OF ABBREVIATIONS

ω_{rl}	Lower resonant frequency of WPT [rad/sec].
$\hat{V}_{sh}(m_a)$	Line-to-line voltage at the higher sideband frequency for modulation index [V _{peak}].
$\hat{V}_{sl}(m_a)$	Line-to-line voltage at the lower sideband frequency for modulation index [V _{peak}].
$\hat{V}_{sw}(m_a)$	Line-to-line voltage at the switching frequency for modulation index [V _{peak}].
ω	Operation frequency of WPT [rad/sec].
ω_c	Carrier frequency [rad/sec].
ω_{rh}	Higher resonant frequency of WPT [rad/sec].
ω_r	Resonant frequency of WPT [rad/sec].
$\phi_{c(A,B,C)}$	Carrier phases [°].
ϕ_{CPS}	The amount of carrier phase shift [°].
ϕ_o	Fundamental phases [°].
A_{INV}	Inverter line-to-line voltage gain to DC-link.
A_{WPT}	WPT AC-AC voltage gain.
C_{Rx}	Rx capacitance [F].
C_{Tx}	Tx capacitance [F].
CV	Constant voltage.
f_o	Fundamental frequency [Hz].

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f_s	Switching frequency [Hz].
f_{sh}	Higher sideband ($f_s - 2f_o$) [Hz].
f_{sl}	Lower sideband ($f_s - 2f_o$) [Hz].
J_k	kth order Bessel function.
J_0	Zeroth order Bessel function.
k	Coupling factor.
L_{Rx}	Rx inductance [H].
L_{Tx}	Tx inductance [H].
M	Mutual inductance [H].
m_a	Modulation index.
P_{rated}	Output power of WPT system [W].
Q_{RX}	Quality factor of receiver side including load.
R_L	Load resistance [Ω].
S	Switching function.
V_{DC}	DC-link voltage [V].
V_D	Equivalent centered harmonic drive (input) voltage of WPT system [V_{RMS}].
V_{OUT}	Output voltage of WPT system [V].
V_{RX}	Output voltage of WPT system [V_{RMS}].
Z_{RX}	Receiver side impedance of WPT [Ω].
Z_{TX}	Transmitter side impedance of WPT [Ω].

I. INTRODUCTION

Wireless power transfer (WPT) systems have been commonly used in many applications such as EV chargers [1], consumer electronics [2], wearable devices [3], and contactless slip rings [4]. They provide several advantages like operation under harsh environments, increased mobility, etc. Compared to other WPT methods such as capacitive power transfer (CPT), WPT in radio frequency (RF), and WPT in ultrasonic; inductive power transfer (IPT) is the most common technique, which provides a moderate transfer distance, high power, and better efficiency [5]. IPT systems are primarily composed of a high-frequency converter, a set of transmitter (Tx), and receiver (Rx) coils with compensation elements and rectifier circuits. The compensation circuits create a resonant structure that amplifies the frequencies near the resonant frequency and attenuates the other frequencies, which means that they provide a power transfer at a single frequency, although high-frequency converters generate a square wave with high-order harmonics.

Multi-frequency operation rather than single frequency to increase the power of single-load [6] or transfer power concurrently to multiple loads [4], [7]–[15] are studied in the literature. Thus, the number of switches decreases with

the same VA ratings, transmitted power increases, and the utilization rate of DC-link voltage increases. In [6], a single inverter is used with multi-resonant circuits amplifying the fundamental and the third harmonic of the square wave. The third harmonic component increases the transmitted power without increasing the VA rating of the semiconductor devices. However, it is not possible to independently regulate the third harmonic component in such a system. In [7], a dual-band WPT system is introduced to make Tx compatible with two different WPT standards. However, in these systems, two separate sets of coils and drivers are used, allowing design flexibility but increasing system complexity and cost. In [8], a single-inverter-based dual-frequency WPT system is proposed using the programmed PWM method. The system can regulate the output voltage for multiple receivers. The programmed PWM provides that one WPT frequency is equal to the effective switching frequency, which reduces the switching losses. Nevertheless, the programmed PWM method is computationally complex and requires switching angle calculations using offline algorithms, which is not feasible in dynamic systems. In [10], multi-frequencies are achieved by comparing superimposed sinusoidal reference signals with high-frequency triangular carrier signals. In addition, a phase shift can be given between the superimposed reference signals to increase the utilization rate of the DC-link voltage [11]. However, in these methods, the switching frequency is higher than the operating frequencies of the WPT system, which increases the switching losses. In [12], multi-frequency components are achieved by a multi-level inverter (MLI) with a switching frequency lower than its two-level alternatives. However, this system uses a higher number of switching components.

Apart from the aforementioned studies, multi-frequency systems can also be implemented to combine wireless and wired power transfer concurrently since they could co-exist in dynamic applications [16], [17]. One of these applications is a contactless slip ring (CSR) used in many areas, such as the field excitation of wound rotor synchronous motor, radar systems, robotic arms, and IoT devices [18]–[20]. In [21], the field winding is excited by a contactless slip ring, and its power rating is just 2% of the motor rating. In [22], motor onboard sensor systems such as LIDARs, sensors, and cameras are excited via a wireless power transfer system, and its power rating is given as 20% of the utilized motor power rating. In [23], a contactless slip ring is proposed to excite auxiliary systems with 10 % of the motor-rated power. In these CSR systems, an inverter is readily available to drive the actuator, and the power transfer to moving frames is granted via a WPT system, consisting of a high-frequency converter and a set of Tx-Rx coils.

In this study, a motor and an auxiliary system are driven concurrently by a single converter, as shown in Fig. 1.a. It is proposed that the already existing inverter of the drive can be used as a multi-frequency inverter; thus, the high-frequency converter of the WPT system can be eliminated, reducing the cost and complexity. Since the power rating of an auxiliary system is lower than the motor, the existing motor drive can also be used as a single inverter of a multi-frequency system. Furthermore, conventional motor drives

use several pulse width modulation (PWM) techniques (such as sinusoidal PWM, space vector PWM, and discontinuous PWM) to adjust the speed and torque of the motor. However, independent control of the WPT system cannot be achieved only using these techniques. In this paper, a novel carrier phase shift (CPS) method is proposed, which provides independent control of the dual-band output voltage.

The rest of the paper is organized as follows. Section II presents the modulation methods and the proposed CPS method. Section III delivers the design stage of the WPT system. Section IV explains the independent control of the motor and the WPT system. Section V gives experimental results to validate the theoretical calculations. Section VI compares the proposed system with existing studies in the literature. Section VII discusses the implementation of the proposed system to conventional industrial drives.

II. SYSTEM PROPOSAL AND MODULATION METHOD

A 3-phase 3-wire ($3\Phi\text{-}3W$) motor and a WPT system for auxiliary rotating loads are driven by a single inverter with sinusoidal PWM (SPWM) as a proof-of-concept. The circuit diagram of the system and the expected voltage and current waveforms are shown in Fig. 1.b. In this section, the analytical modeling of applied SPWM will be investigated. Then, the carrier phase shift (CPS) method is proposed, and the effect of this method on the magnitude of the dual-band output will be examined. Finally, the equivalent centered harmonic approach to control the amount of CPS is presented.

A. Analytical analysis of SPWM

SPWM is established by comparing a high-frequency triangular carrier signal and a fundamental (modulating) signal. This fundamental signal is the reference signal of the motor, and the operating frequency of WPT is cultivated using the first harmonic of the carrier signal. The analytical model of SPWM can be obtained as given in (1), using double Fourier analysis.

$$\begin{aligned} S = & \frac{1}{2} + \frac{m_a}{2} \cos(\omega_o t + \theta_o) \\ & + \frac{2}{\pi} \sum_{i=1}^{i=\infty} J_o\left(i\frac{\pi}{2} m_a\right) \sin\left(i\frac{\pi}{2}\right) \cos\left(i(\omega_c t + \theta_c)\right) \\ & + \frac{2}{\pi} \sum_{i=1}^{i=\infty} \sum_{k=-\infty}^{k=\infty} \left(\begin{array}{c} \frac{1}{i} J_k\left(i\frac{\pi}{2} m_a\right) \sin\left((i+k)\frac{\pi i}{2}\right) \\ \cos\left(i(\omega_c t + \theta_c) + k(\omega_o t + \theta_o)\right) \end{array} \right) \end{aligned} \quad (1)$$

There are four main components for each leg: DC, fundamental, switching harmonics, and sidebands. The magnitudes of switching harmonics and their sidebands cannot be adjusted independently, and they change along with the modulation index, which is used to control the magnitude of the fundamental frequency. Moreover, the phases of these components vary for each leg of the inverter, and they have positive, negative, or zero sequences, as shown in Table I. The normalized line-to-line voltage harmonics are given in Fig. 2.

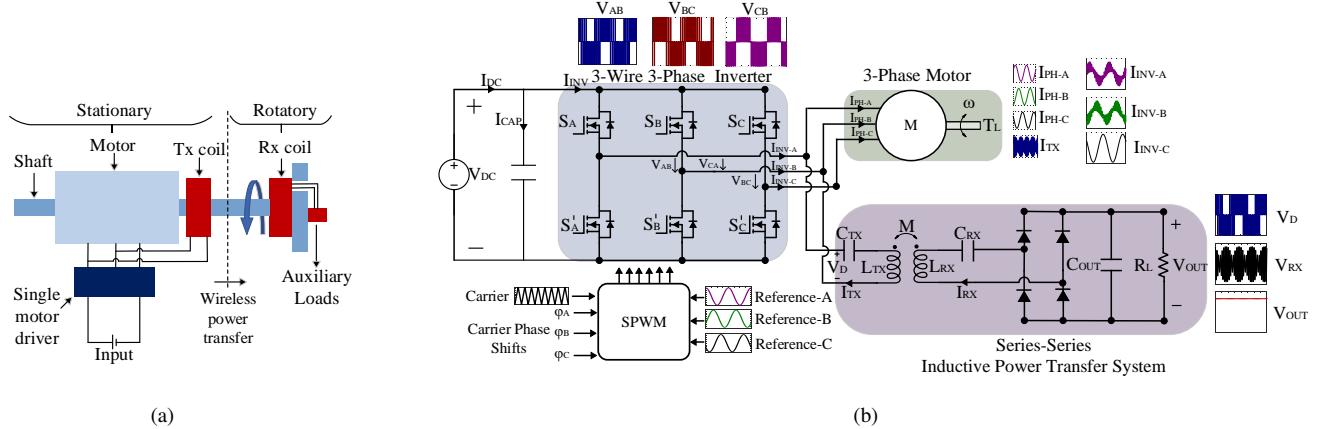


Fig. 1. a) The proposed single-inverter multi-frequency motor drive and WPT system to energize IoT devices or sensors on the rotating frames. b) The circuit diagram of a single inverter system, which drives concurrently 3W-3Φ motor and WPT system.

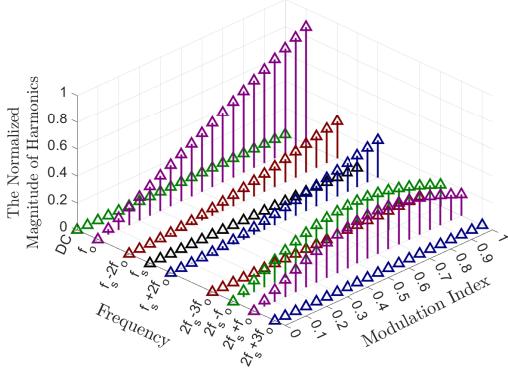


Fig. 2. The harmonic distribution of a line-to-line voltage among different modulation indices without carrier phase shift. (Their peak voltages per DC-link voltage are presented.)

TABLE I

THE SEQUENCES OF THE FUNDAMENTAL FREQUENCY, SWITCHING HARMONIC, AND ITS SIDEBAND HARMONICS

Frequency	Leg A	Leg B	Leg C	Sequence
f_o	0°	120°	-120°	Positive
$f_s - 2f_o$	0°	120°	-120°	Positive
f_s	0°	0°	0°	Zero
$f_s + 2f_o$	0°	-120°	120°	Negative

Since the switching harmonic is zero-sequence, it disappears in a line-to-line connection, which means that the line-to-line connected WPT system is excited only by the sideband components. Additionally, the magnitude of these sideband components cannot be controlled independently. Therefore, in order to regulate the WPT power, a DC-DC converter can be added to the receiver for post-regulation, which increases cost and complexity. Furthermore, this regulation method cannot guarantee to transfer power under each modulation index since sideband components converge to zero as m_a comes up to zero. In other words, the transferred power of the WPT system is decreasing while m_a is nearing zero. This diminished power is undesirable since the auxiliary loads may require the rated power for each operating condition of the motor.

B. The Proposed Carrier Phase Shift (CPS) Method

Normally, the power transmitted to the WPT system cannot be controlled independently for every m_a value due to the non-controllable magnitude of sidebands and the disappearance of the switching frequency in the line-to-line connection. It is proposed that these problems can be solved by giving a phase shift between carrier signals of two legs. As can be seen from the double Fourier analysis in (1), phases of switching frequency and its sidebands change as a function of the phase of the carrier signal. In this study, for the coherency, the WPT system will be connected between legs A and B, and a carrier phase shift will be given to leg B, as given in (2).

$$\text{Carrier phases} \begin{cases} \phi_{cA} = 0 \\ \phi_{cB} = \phi_{CPS} \\ \phi_{cC} = 0 \end{cases} \quad (2)$$

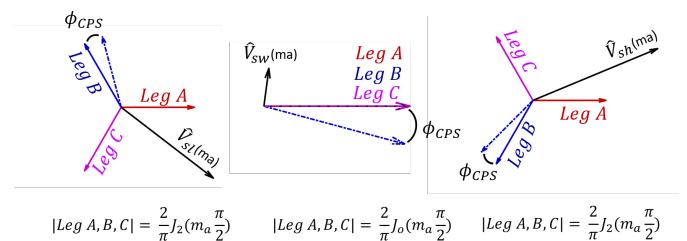


Fig. 3. Phasor diagram of the switching frequency and sideband harmonics.

The phasor diagram of the switching frequency and sideband harmonics for each leg are represented in Fig. 3. The magnitudes of these components can be calculated as in (1) by taking ($i = 1$), ($i = 1, k = -2$), and ($i = 1, k = 2$). The harmonic components between phase A and phase B, the magnitudes of which can be controlled via phasor subtractions, can be controlled by the carrier phase shift of phase B. Thus, the magnitudes of these components can be calculated as given in (3, 4, 5).

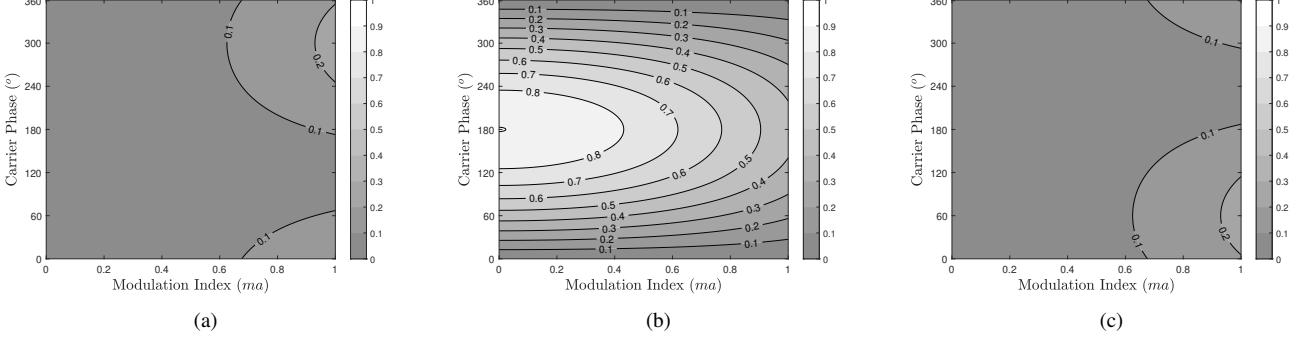


Fig. 4. Normalized inverter output voltages $|V_l-l/V_{DC}|$. (a) Lower sideband of the switching frequency (f_{sl}). (b) Switching frequency (f_s). (c) Higher sideband of the switching frequency (f_{sh}).

$$\hat{V}_{sl}(ma) = \frac{2}{\pi} J_2 \left(m_a \frac{\pi}{2} \right) \sqrt{1 - \cos(\phi_{CPS} + 120^\circ)} \quad (3)$$

$$\hat{V}_{sw}(ma) = \frac{2}{\pi} J_0 \left(m_a \frac{\pi}{2} \right) \sqrt{1 - \cos(\phi_{CPS})} \quad (4)$$

$$\hat{V}_{sh}(ma) = \frac{2}{\pi} J_2 \left(m_a \frac{\pi}{2} \right) \sqrt{1 - \cos(\phi_{CPS} - 120^\circ)} \quad (5)$$

The magnitudes are calculated for variable carrier phase shifts and modulation indices, as shown in Fig. 4. A challenge is to determine the amount of the phase shift, which must guarantee constant power transfer for any modulation indices. Since the magnitudes of switching frequency harmonic and sidebands change differently, a combined equivalent magnitude value is required to select the amount of phase shift.

C. Equivalent centered harmonics approach

Considering the quality factor of WPT systems, the gain of the switching frequency and its sideband harmonics are nearly equal since their frequencies are close enough. Instead of investigating each component in the time domain, we can build an equivalent drive voltage assuming all components have the same frequency. These components generate power, which is dissipated on the R_L . The same dissipated power on the R_L can be produced by an equivalent drive voltage (V_D). Using the power equality in (6), (V_D) is calculated as given in (7).

$$P_{RL} = \frac{V_D^2}{R_L} = \frac{V_{sl}^2}{R_L} + \frac{V_{sw}^2}{R_L} + \frac{V_{sh}^2}{R_L} \quad (6)$$

$$V_D = \sqrt{\left(V_{sl}^2 + V_{sw}^2 + V_{sh}^2 \right)} \quad (7)$$

Using the equivalent center harmonic approach, a normalized drive voltage is shown for various carrier phase shifts and modulation indices in Fig. 5. For each modulation index, we can guarantee the normalized gain between 0.28 and 0.45 by adjusting the carrier phase shift. However, if a higher gain is required, the modulation index of the motor drive should be restricted to achieve this gain for all conditions, which narrows the operation region of the motor. Therefore, the normalized

gain is selected considering the DC-link voltage, motor modulation index requirements, output voltage, and output power of the WPT system.

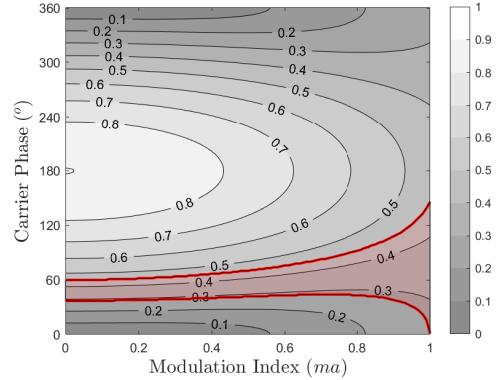


Fig. 5. Normalized inverter output voltages $|V_l-l/V_{DC}|$ using the center harmonic approach. The constant drive voltage gains, which can be achieved in any modulation index, are colored red.

III. WPT SYSTEM PARAMETERS SPECIFICATION

As a proof of concept, the WPT system is implemented with a 100 V DC-link and switching frequency up to 100 kHz. The system is designed to energize auxiliary systems, so constant voltage (CV) output is required. The output voltage and rated power of the WPT system are given in Table II. The circuit diagram of the series-series compensated WPT system is shown in Fig. 6.

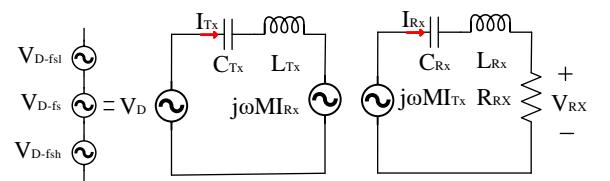


Fig. 6. The first harmonic approach circuit diagram of the WPT system.

TABLE II
THE INPUT-OUTPUT SPECIFICATIONS OF THE WPT SYSTEM

Ratings	
DC-link Voltage (V_{DC})	100 V
Modulation Index (m_a)	0 – 1
Switching frequency (f_s)	< 100kHz
Output Voltage (V_{OUT})	15 V
Rated Power (P_{rated})	30 W

Design Parameters	
Receiver voltage (V_{Rx})	16.7 V _{RMS}
Gain of the WPT System (A_{WPT})	0.5
WPT Input Voltage (V_D)	33.4 V _{RMS}
Normalized Drive Voltage	0.334

Since the output voltage is 15 V, for SS-compensated system, the receiver voltage is 16.7 V_{RMS} as can be calculated in $V_{RX} = V_{OUT} \frac{2\sqrt{2}}{\pi}$. The motor operates under the full range of modulation index (0-1); thus, the normalized drive voltage should be selected between 0.28 and 0.45 to avoid modulation restriction. The gain of the WPT system is selected as 0.5, which means that the input voltage should be 33.4 V_{RMS}, and the normalized drive voltage becomes 0.334. For SS compensation, the frequency of the CV operation (or as known as load-independent voltage) is different from the resonant frequency (w_r), and there are two operating frequencies (ω_{rl}, ω_{rh}). The receiver voltage (V_{RX}) and voltage gain (A_{WPT}) of the SS compensated system are given in (8) and (9).

$$V_{RX} = \frac{j\omega M I_{Tx}}{Z_{Rx}} R_L = \frac{j\omega M V_D}{\omega^2 M^2 + Z_{Tx} Z_{Rx}} R_L \quad (8)$$

$$A_{WPT} = \left| \frac{V_{RX}}{V_D} \right| = \frac{\omega M}{\omega^2 M^2 + Z_{Tx} Z_{Rx}} R_L \quad (9)$$

In order to calculate the frequencies providing load-independent voltage gain, the differential equation of $\frac{dA_{WPT}}{dR_L} = 0$ should be solved [24]. The load-independent voltage operating frequencies are found as in (10) according to selected coupling factor (k) and angular resonant frequency (w_r).

$$\omega_{rl,rh} = \omega_r \sqrt{\frac{1}{1 \pm k}} \quad (10)$$

The minimum load resistance that delivers the rated power can be calculated as in (11).

$$R_L = \frac{V_{RX}^2}{P_{rated}} \quad (11)$$

Receiver inductance is calculated using the load resistance, operating frequency that is selected as ω_{rh} , and the chosen quality factor of Rx (Q_{Rx}) as provided in (12).

$$Q_{Rx} = \frac{\omega_{rh} L_{Rx}}{R_L} \quad (12)$$

The load-independent voltage gain is presented in (13), and the Tx inductance is found using (14).

$$A_{WPT(\omega_{rh})} = \left| \sqrt{\frac{L_{Rx}}{L_{Tx}}} \right| \quad (13)$$

$$L_{Tx} = \frac{L_{Rx}}{A_{WPT(\omega_{rh})}^2} \quad (14)$$

Then, the mutual inductance is calculated as in (15).

$$M = k \sqrt{L_{Tx} L_{Rx}} \quad (15)$$

Finally, the compensation capacitances are adjusted as in (16).

$$C_{Tx,Rx} = \frac{1}{\omega_r^2 L_{Tx,Rx}} \quad (16)$$

The initial parameters and the derived parameters are given in Table III. The operating frequency is selected 85 kHz, which provides a frequency control region up to 100 kHz. Q_{RX} is selected between 2-10 as a rule of thumb [25], and the coupling coefficient (k) is between 0 and 1. In this study, Q_{RX} and k are selected as 2.9 and 0.4, respectively.

TABLE III
WPT SYSTEM PARAMETERS

Initial/Choosen Parameters	Values	Derived Parameters	Values
P_{rated}	30 W	R_L	9.3 Ω
V_D	33.4 V _{RMS}	L_{Rx}	50 μH
V_{OUT}	15 V	L_{Tx}	200 μH
f_{rh}	85 kHz	M	40 μH
Q_{Rx}	2.9	f_r	65.85 kHz
k	0.4	C_{Tx}	29.2 nF
V_{RX}	16.7 V _{RMS}	C_{Rx}	117 nF
A_{WPT}	0.5		

IV. CONTROL SCHEME OF WPT SYSTEM

The carrier phase shift is introduced to keep the V_D constant under varying modulation indices. It is observed that the carrier phase shift has narrow control region since the normalized gain of V_D per unit DC-link voltage is restricted between 0.28 and 0.45 to operate under any modulation index. Since the system requires a constant voltage output, and the WPT system operates at the frequency of the load-independent voltage, a look-up table can be implemented to keep V_D constant. However, minor variations in the output voltage due to parasitic effects or disturbances can occur. Therefore, additional frequency detuning control can be implemented to regulate V_D . The overall control scheme is presented in Fig. 7.

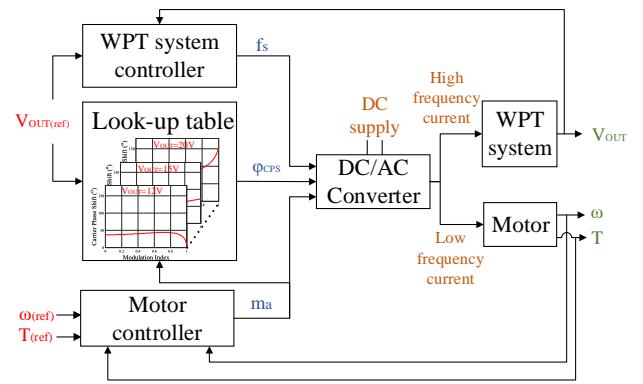


Fig. 7. Control algorithm of the combined carrier phase shift and frequency detuning method.

V. EXPERIMENTAL VALIDATION

An experimental setup is established consisting of a GaN-based 3Φ-3W inverter and an AC motor to validate the proposed method. The experimental setup is shown in Fig. 8. The AC motor is loaded using a DC generator, and the WPT system is connected to its shaft.

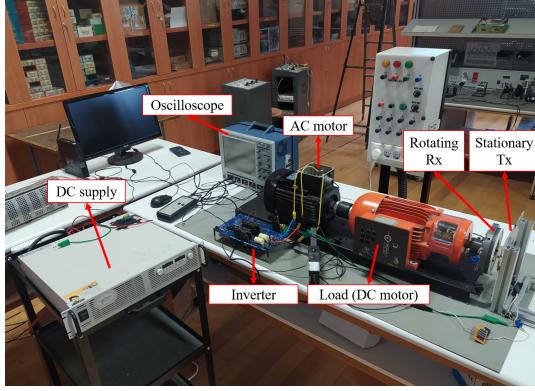


Fig. 8. Experimental Setup.

A. Multi-frequency Inverter Test

In this part, the inverter is driven by the SPWM modulation technique with and without the proposed CPS method. Thus, the variation in the magnitudes of the fundamental frequency, switching frequency, and its sidebands are investigated.

The inverter output voltages are shown in Fig. 9 for the phase shift of $\phi_B = 0^\circ$ and $\phi_B = 47.5^\circ$. The harmonic distributions of the normalized inverter output voltage are compared in Table IV for analytical and experimental results, which have a good agreement with an error band of less than 10%. It can be observed that the carrier phase shift method controls the magnitude of the switching frequency component and its sidebands while the magnitude of the fundamental component stays constant.

TABLE IV

THEORETICAL AND EXPERIMENTAL RESULTS OF INVERTER OUTPUT HARMONIC DISTRIBUTION FOR $\phi_{CPS} = 0^\circ$ AND $\phi_{CPS} = 47.5^\circ$

		$A_{INV} (\hat{V}_l/V_{dc})$			
		at f_o	at f_l	at f_s	at f_h
$m_a = 0.6$	Theoretical	0.519	0.113	0	0.113
	Experimental	0.532	0.110	0.011	0.115
	Error	2.5%	2.6%	-	1.7%
$m_a = 0.6$ $\phi_{CPS} = 47.5^\circ$	Theoretical	0.519	0.130	0.405	0.077
	Experimental	0.537	0.127	0.414	0.084
	Error	3.4%	2.3%	2.2%	9.1%

B. Concurrent Operation of motor and WPT

The motor and WPT system are driven standalone and concurrently. CPS, f_s , and m_a are adjusted to 50° , 82 kHz , and 0.8 . Besides, the fundamental frequency of the motor is 20 Hz . Firstly, the motor is operated standalone, where the motor draws 1.58A as presented in Fig. 10.a. Then, only the WPT system is connected, and its output voltage, Tx current, and Rx current are measured as given in Fig. 10.b. Finally, the motor and WPT system are run concurrently, and it is observed that the systems draw and supply almost the same current and

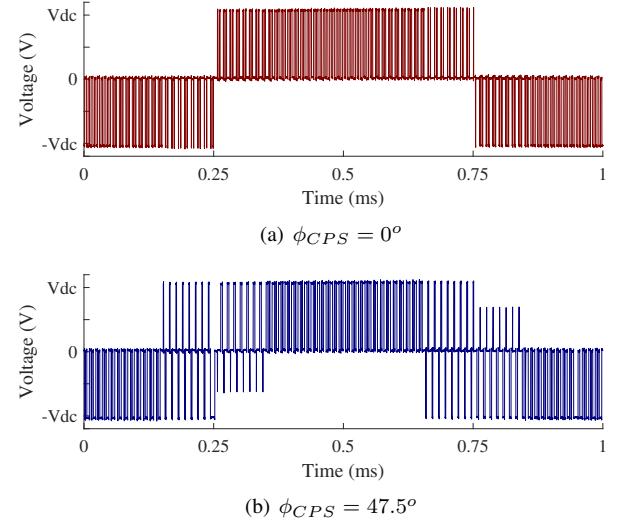


Fig. 9. Inverter output voltage (V_{l-l}) (or called as drive voltage V_D) at $m_a = 0.6$ for $\phi_{CPS} = 0^\circ$ and $\phi_{CPS} = 47.5^\circ$

voltage as shown in Fig. 10.c. It is observed that the output voltage differs by 1.7%. These minor changes stem from the varying effective duty cycle for the different converter currents due to the dead time duration. It is also observed that a low-frequency fluctuation exists in the output voltage, which will be discussed in Section VII.

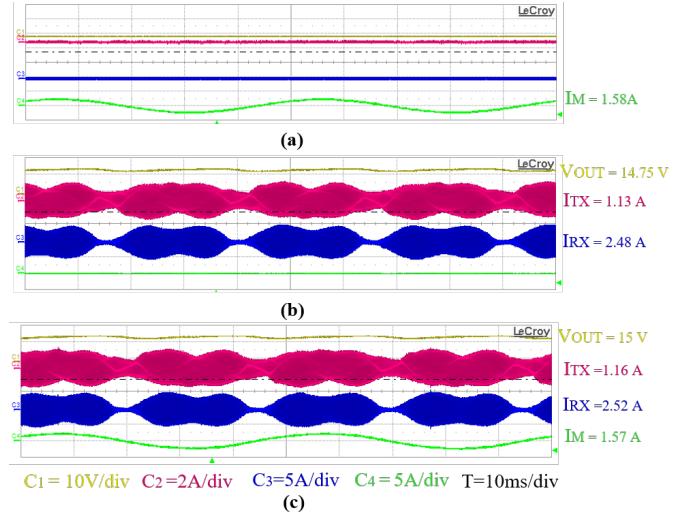
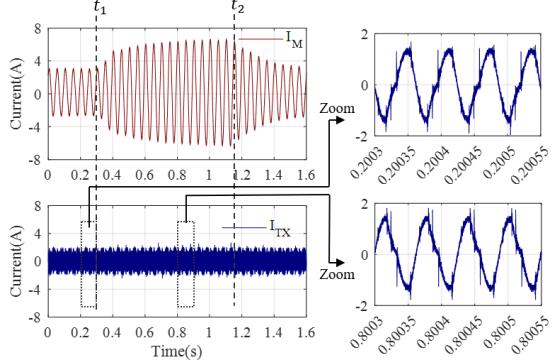


Fig. 10. The waveforms of the DC output voltage, Tx current, Rx current, and motor current. a) Standalone motor operation. b) Standalone WPT system operation. c) Concurrent operation.

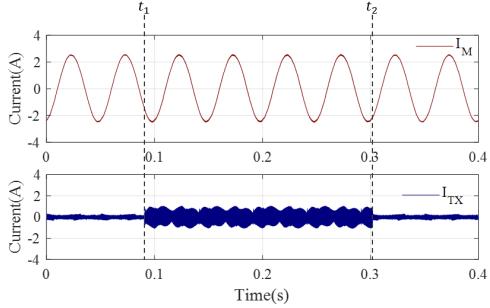
C. Transient Load Changes of the Motor and the WPT System

In this part, the effect of the load changes on the WPT system and motor is investigated. Firstly, the mechanical load of the AC motor is varied, while other parameters such as switching frequency and modulation index are kept constant. It is observed that the WPT system is not disturbed by the load changes, as shown in Fig 11.a. Then, the load of the WPT system is altered using the same procedure. As expected, currents of the AC motor do not change, as shown in Fig. 11.b.

Unless the modulation index or switching frequency varies, load changes of a system do not affect the operation of the other system. Furthermore, in Fig. 11, it is observed that the Tx current has some high-order harmonics (3rd and 5th harmonics of the switching frequency). Since the mathematical model is developed using only switching frequency and its sideband harmonics, the output voltage deviates from the desired value if the theoretic carrier phase shift is applied. The combined carrier phase shift and frequency detuning control method, which will be revisited in the following part F, can compensate for this deviation.



(a) At t_1 , the motor load torque is increased, and at t_2 , the motor load torque returns to the former value.



(b) At t_1 , The WPT load resistance is decreased, and at t_2 , WPT load resistance returns to the former value.

Fig. 11. Transient load changes. a) Motor load changes. b) IPT load changes.

D. DC Output Reference Change and Fundamental Frequency Variation

In this part, the effects of DC output reference change and fundamental frequency variation are investigated. Firstly, the carrier phase shift is adjusted to 44.5° to achieve a 15 V DC output (f_s is 80 kHz, f_o is 10Hz, and m_a is 0.36). The DC reference is decreased to 8 V by adjusting carrier phase shift to 23°, and then increased to 15 V DC. The DC output voltage, motor, Tx and Rx currents are shown in Fig. 12. It is observed that the motor current is not affected by this change. Secondly, a resistive-inductive (RL) load is connected to the motor drive, and the fundamental frequency is swept from 0 to 50 Hz. The carrier phase shift is adjusted to 44.5° to achieve a 15 V DC output (f_s is 80 kHz, and m_a is 0.36). The current of the RL load and DC output voltage is shown in Fig. 13. It is observed that the DC output voltage is constant while the fundamental frequency is changing.

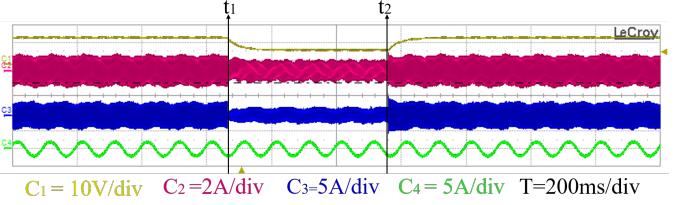


Fig. 12. The current and voltage waveforms for DC reference changes.

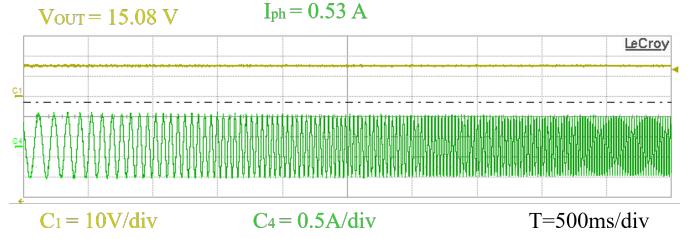


Fig. 13. The output voltage and phase current under identical m_a and CPS for fundamental frequency variation between 0 to 50 Hz

E. Efficiency and Drive Losses

In the proposed system, the motor drive also delivers high-frequency current components drawn by the WPT system in addition to the motor current. These components increase the thermal stress on the drive. In order to investigate this stress, the input and output powers of the driver are measured for the motor's standalone and concurrent operations, which are presented in Table V. Since the WPT system has a lower power rating, the drive loss is just increased by 12% compared to standalone operation. However, the high-frequency current only circulates between two legs of the converter. Therefore, this 12% increase is shared between these two legs, which means an 18% increase in their individual losses. Therefore, even though the system is still in the thermally safe region, the design of cooling and heatsink placement can be improved to minimize this thermal imbalance.

TABLE V
DRIVE LOSSES AND EFFICIENCY MEASUREMENTS

Standalone Motor Operation			
Input Power	Motor Power	Drive Losses	Drive Efficiency (%)
273 W	248 W	25 W	90.8

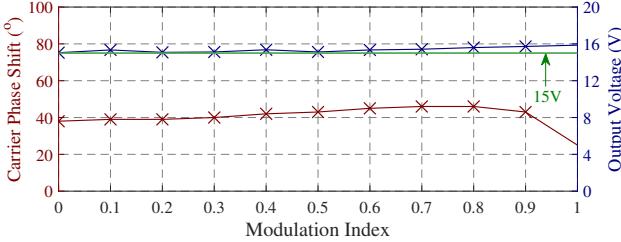
Concurrent Operation of the Motor and the WPT system

Input Power	Motor Power	WPT Power	Drive Losses	Drive Efficiency (%)
312 W	255 W	29 W	28 W	91.0

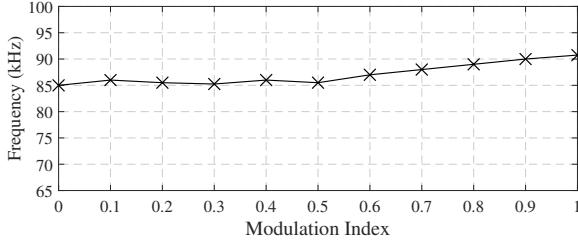
F. Embedding of the Look-up Table and Control of Frequency Detuning

The system should keep the output voltage constant for varying modulation indices. The desired output voltage is 15 V, and the switching frequency is brought to 85 kHz, providing 0.5 of A_{WPT} . Considering the mathematical model given in (7) and in Fig. 5, the carrier-phase-shift values are calculated to keep the output voltage constant, then it is implemented with a look-up table in experimental setup. Fig. 14.a shows

the measured output voltages for these calculated carrier-phase shifts. It is observed that the output voltages are deviated from the desired value due to high-order harmonics, parasitic effects such as line resistance and inductance, or nonlinearities such as dead time. The frequency detuning method can be used to compensate for these differences. The switching frequencies to reach the desired output voltage are achieved experimentally, as shown in Fig. 14.b.



(a) Output voltages and the applied carrier phase shifts for varying modulation indices.



(b) Switching frequencies to reach 15 V output for varying modulation indices.

Fig. 14. Embedding look-up table and frequency detuning method.

VI. COMPARISON WITH EXISTING STUDIES

Existing multi-frequency WPT systems in the literature are given in Table VI. The proposed method provides independent control of the dual-band output voltage. Unlike [8], it is not required completely offline calculation where it is not possible to apply dynamic applications like motor drive systems. Although a look-up table is embedded in the proposed system to control the switching frequency and its sideband components, conventional online modulation techniques still achieve the low-frequency modulated component. Besides, online frequency detuning control can finely adjust the switching frequency and its sideband components.

In the proposed system, only low-frequency modulated motor's current is obtained by SPWM, and the WPT system directly utilizes the switching harmonics. Therefore, the required switching frequency of the proposed WPT system is lower than the studies given in [10], [11]. If WPT operates at the modulated frequency rather than the switching frequency, the switching losses increase to achieve the same coil sizes. Thus, directly utilizing the switching frequency is significant to make the system feasible. In [6], switching harmonics are directly utilized, but, unlike the proposed system, it does not have an independent control method, and just a single load is supplied. In [7], two converters are used to achieve a dual channel using switching harmonics, while the proposed method uses only a single converter. In [12], multi-frequency components are obtained by increasing the level of the converter; therefore, a

higher number of switches is used than the proposed method, which increases the cost and complexity.

TABLE VI
COMPARISON WITH EXISTING STUDIES IN THE LITERATURE.

	Transfer Channels	Converter Numbers (Tx)	Offline Algorithm	Operating Frequency
[6]	1	1-2LC	Not Required	$\geq f_s$
[7]	2	2-2LC	Not Required	f_s
[8]	2	1-2LC	Required	$\leq f_s$
[10]	2	1-2LC	Not Required	$< f_s$
[11]	4	1-2LC	Not Required	$< f_s$
[12]	2	1-MLI	Not Required	$\geq f_s$
This Work	2	1-2LC	Not required*	$= f_s$

2LC : Two level converter, MLI : Multi level inverter.

* : A partially look-up table is required.

VII. DISCUSSION

A. Current Modulation Techniques of Conventional Drives

Many modulation techniques such as Sinusoidal PWM (SPWM), Space Vector PWM (SVPWM), and Discontinuous PWM (DPWM) are used in industrial motor drives. Each modulation technique has its advantages. For example, while SVPWM effectively utilizes the DC link voltage, DPWM provides reduced switching losses. Although the proposed system is implemented with SPWM, it can also be applied with other modulation techniques. The change in the magnitude of the switching frequency over modulation indices is presented in Fig. 15 for SPWM and SVPWM. It is observed that the tendency of the switching harmonic magnitude of SVPWM is similar to SPWM, and it can be controlled in line-to-line connection via the proposed CPS, as presented before.

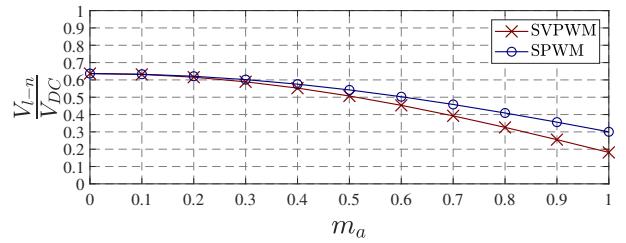


Fig. 15. The normalized inverter voltage ($\frac{V_{l-n}}{V_{DC}}$) of the switching harmonics over modulation indices (m_a).

B. Carrier Phase Shift Impacts on the Motor

In the conventional SPWM method, the switching frequency components are not observed in line-to-line voltages, and the magnitudes of the sideband harmonics increase by the modulation index. However, in the proposed method, the switching component is controlled by the carrier phase shift and so is also observed in line-to-line voltages. The effective voltages, which are calculated as in (7), are plotted in Fig. 16 for with and without carrier phase shift method. It is observed that the effective voltage harmonics in the proposed system are greater than the conventional SPWM until m_a reaches 0.9. However, the harmonics attenuate in motor phase currents thanks to large motor phase inductances. Since the voltage harmonic at the

fundamental frequency (low-frequency modulated signal of the SPWM) is not affected by the carrier phase shift and high-frequency components are filtered out, the motor operation and efficiency do not significantly change with the proposed method. However, if a low-inductance motor is used or the switching period is not short enough compared to the motor's time constant (depending on motor phase inductances and resistances), the voltage harmonics also produce harmonics in motor phase currents. In this condition, introducing carrier-phase shift generates high-frequency current ripples, which creates torque ripple and decreases the efficiency. Therefore, the switching frequency should be selected considering the motor electrical time constant to avoid current harmonics due to the carrier phase shift.

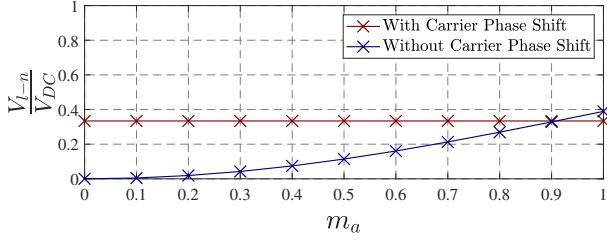


Fig. 16. The effective inverter voltage ($\frac{V_{I-n}}{V_{DC}}$) for the switching harmonic and its sidebands over modulation indices (m_a).

C. Low-Frequency Fluctuation on the Output Voltage

Rectified sideband harmonic components generate low-frequency ripples (at the double-fundamental and quad-fundamental frequencies) even though they contribute to the DC output voltage. The WPT system cannot filter out these sideband harmonic components since they are close to the switching frequency. Besides, the magnitude of the low-frequency ripples increases for a higher modulation index since sideband components become more dominant with the increased modulation index. The variations of low-frequency current components with changing modulation indices are shown in Fig. 17.

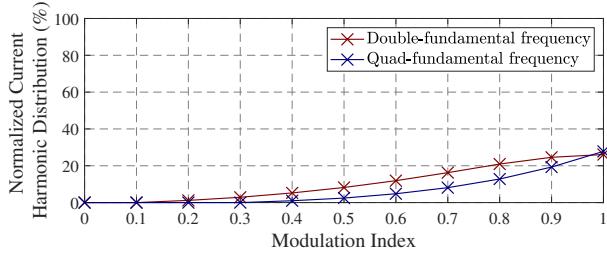


Fig. 17. The ratio of the low-frequency harmonic components in the rectifier current over modulation indexes (m_a).

$$\Delta V = \Delta t_2 \frac{\Delta I_2}{C_{OUT}} + \Delta t_4 \frac{\Delta I_4}{C_{OUT}} \quad (17)$$

These current ripples can be easily filtered out in the output voltage by adding output capacitance. Peak-to-peak voltage fluctuation can be calculated as in (17), where C_{OUT} is the output capacitance, Δt_2 and Δt_4 are the periods of double-fundamental and quad-fundamental frequencies, ΔI_2 and ΔI_4 are the peak-to-peak values at $m_a = 1$ under rated load. Therefore, the voltage fluctuation can be brought to the desired value by adding appropriate output capacitance.

D. Effect of Switching Frequency and Type of Semiconductors

Several factors should be considered while selecting the switching frequency. Firstly, a higher switching frequency shrinks the coil size but increases eddy losses, which can be reduced with Litz wires. Secondly, the inverter voltage harmonics create motor current ripples, which can be decreased by increasing the switching frequency. Since the proposed carrier phase shift method also has a negative effect on voltage harmonics, the switching frequency should be selected carefully. However, the upper limit for the switching frequency is usually dictated by the motor drive. In conventional silicon (Si)-based motor drives, the switching frequency is usually around 20 kHz [26]. At these frequencies, Tx-Rx coils get bulky, and motor current ripple (especially for high-speed motors with higher fundamental frequency) increases. Therefore, implementing the proposed system with conventional Si-based motor drives is challenging. On the other hand, wide bandgap (WBG) based motor drives (such as silicon carbide (SiC) or gallium nitride (GaN)) are becoming more popular with recent advancements in semiconductor technology. They can provide higher switching frequencies (up to several hundred kHz) without sacrificing efficiency [27], [28]. Thus, with WBG-based motor drives, a sweet spot in the switching frequency can be found that makes the proposed method feasible by reducing the coil size and current ripple.

VIII. CONCLUSION

This paper proposes a concurrent dual-frequency power transfer method for wired and wireless systems using just a single inverter. The proposed method can reduce the system cost and size for applications that require power transfer to rotating frames, such as rotating sensors, radars, and auxiliary systems. This study also presents a novel carrier phase shift (CPS) method to regulate the power transferred to the WPT systems. Therefore, active converters on the Rx side are not required, which reduces the complexity and weight of the system on the rotating frame. However, a few concerns reveal in selecting switching frequency. On the one hand, the switching frequency should be high enough to avoid the impact of the CPS method on the motor phase currents and make WPT coils' size feasible. On the other hand, the upper limit of the switching frequency is dictated by the motor drive due to losses. WBG-based motor drives, which are the trend in industrial motor drives, help to find a sweet spot in the switching frequency considering all these concerns. In order to validate the proposed system, an experimental setup with a GaN-based drive was established. It was acquired that the switching harmonic and the fundamental frequency is controlled independently by the proposed CPS method. However, it was observed that the measured output voltage deviates from theoretical calculations due to high-order harmonics and the effect of parasitics or nonidealities, so the frequency detuning method is proposed to compensate for these deviations. It is also acquired that low-frequency ripple at the output exists due to sideband harmonics, and this ripple can easily be filtered out using proper output capacitance.

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