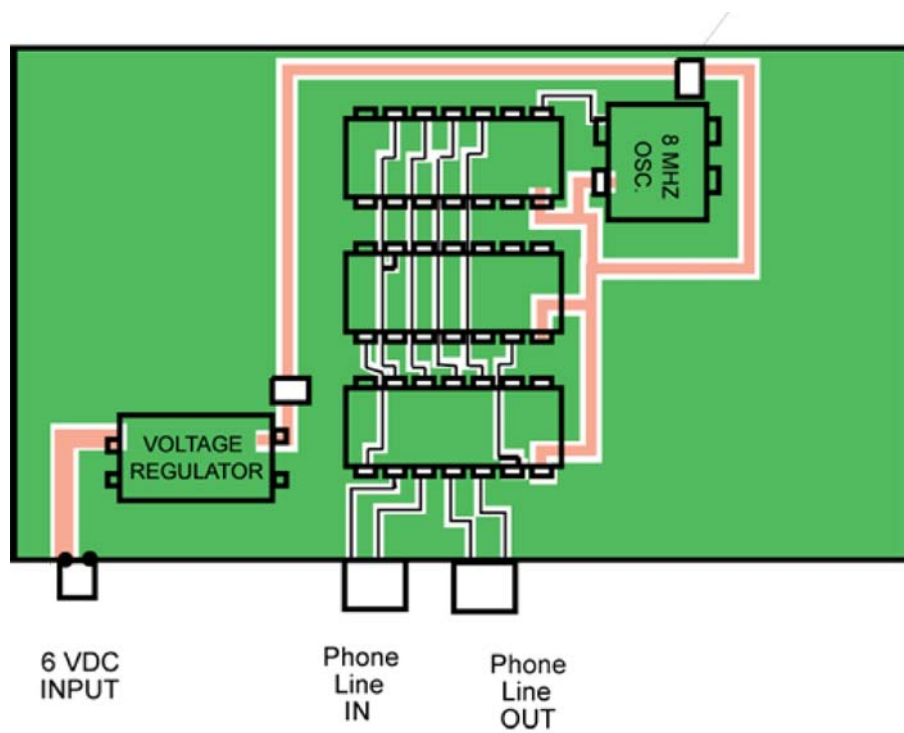


Figure 20 From 3 D Electromagnetic Modeling Of Parasitics
And



Learnemc Pcb Layout

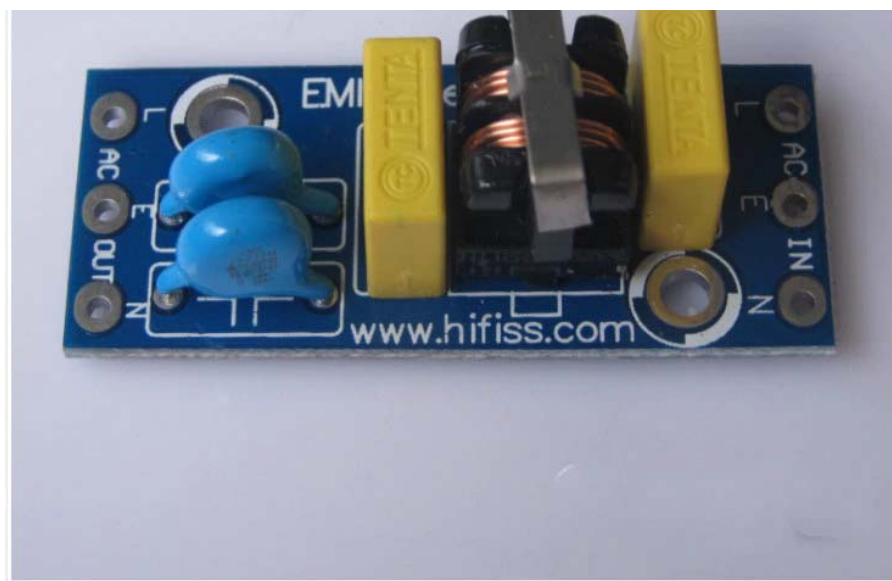
PCB Design for Real-World EMI Control

Bruce R. Archambeault

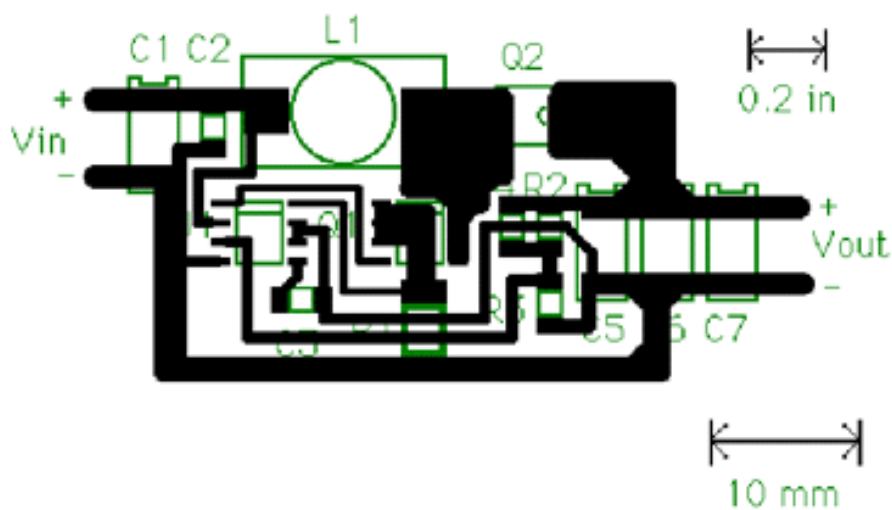


Contributions by James L. Drowniak

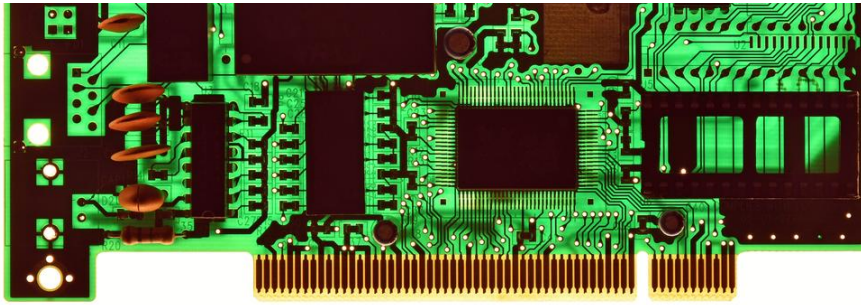
Pcb Design For Real World Emi Control The Springer
International



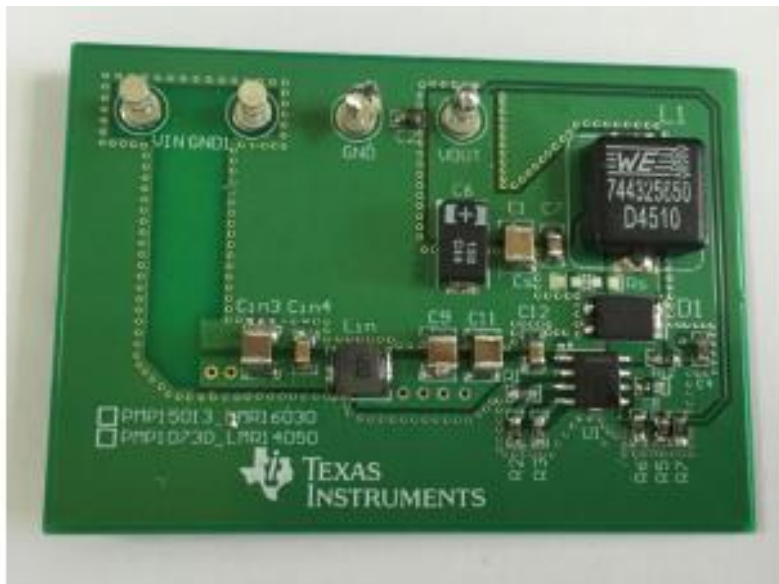
Yimaker 5pcs Electromagnetic Interference Filters Emi Filter
Pcb



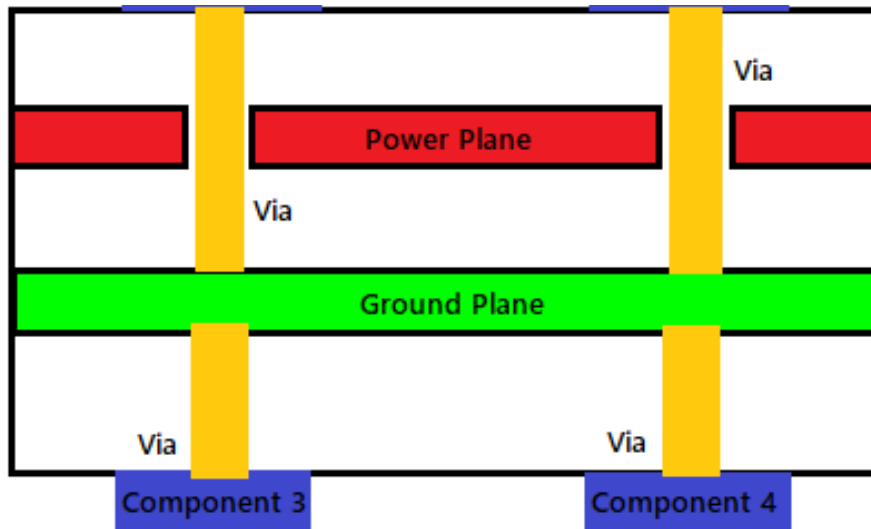
Good Physical Layout Takes Black Magic Out Of Power Supply
Design



7 Tips And Pcb Design Guidelines For Emi And Emc Sierra Circuits

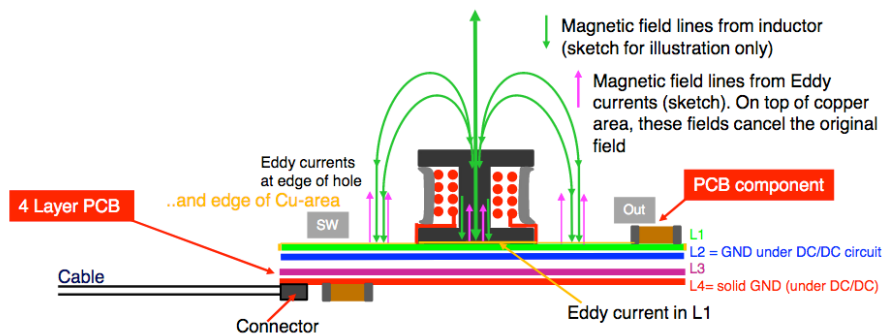


Pmp15013 Cispr 22 Emi Optimized Reference Design Featuring The



Pcb Layout Design Tips Grounding Considerations

Onelectrontech



- + No AC magnetic field on bottom side of PCB
- + No magnetic coupling into bottom side components
- + Reduced Magnetic field coupling in cable
- + Reduced coupling in adjacent PCBs
- + AC Magnetic fields only on top side of PCB
- + inner layer should be clean

- Bottom side of PCB CLEAN
- EMC filter can be placed effectively here

- Losses in Cu due to Eddy currents
- Increased parasitic capacity of coil
- Reduced effective inductance
- Eddy currents under coil in L1 and at edge of Cu-area

Pcb For Low Emi Dc Dc Converters Working Design Ele Times



[Http Www Ieca Inc Com Images X2y Filter Evaluation And Pcb Design Guide Pdf](http://www.leca-inc.com/Images/X2y_Filter_Evaluation_And_Pcb_Design_Guide_Pdf)



[Https Www Analog Com Media En Technical Documentation Application Notes An139f Pdf](https://www.analog.com/media/en/technical_documentation/application_notes/an139f.pdf)

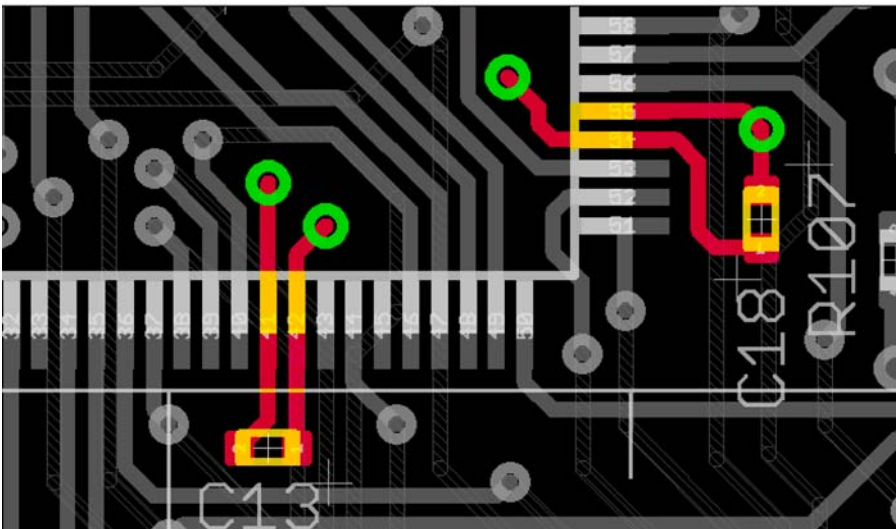
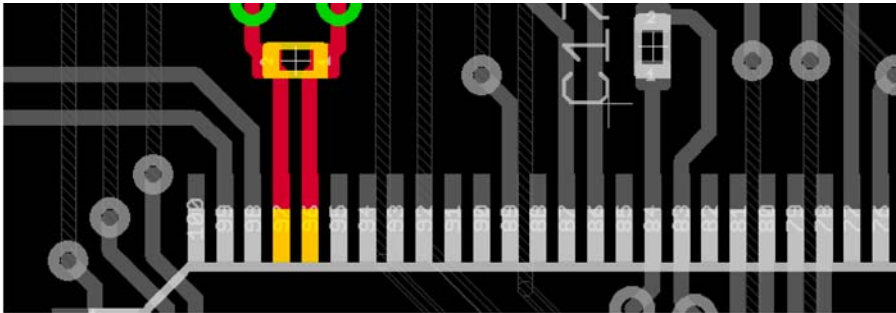


[https://inst.eecs.berkeley.edu/Ee192/Sp18/Files/Pcb Layout An 1031 Pdf](https://inst.eecs.berkeley.edu/Ee192/Sp18/Files/Pcb%20Layout%20An%201031.pdf)

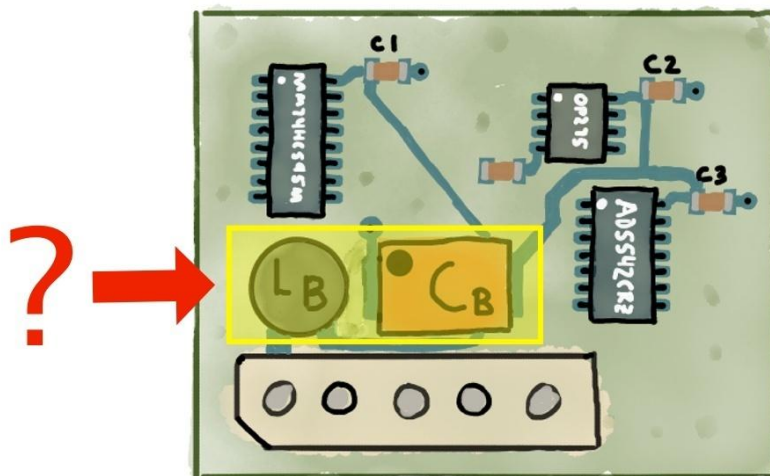


[Pcb Layout For Emc Power Supply Design Tutorial Section 3 1](#)

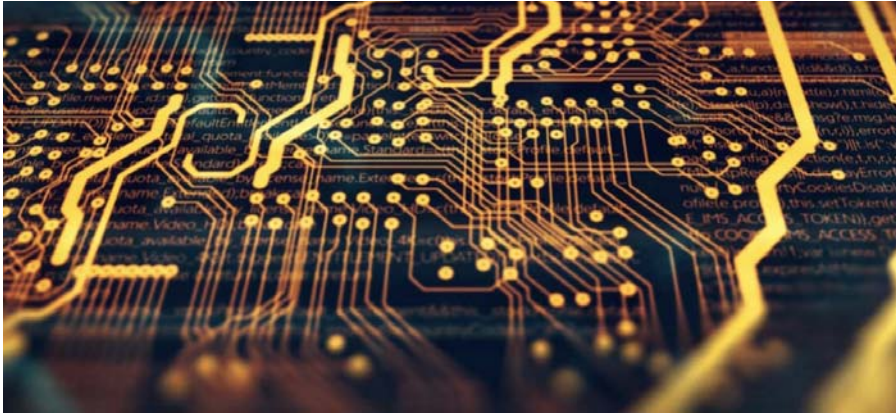
PCB CIRCUITS

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Decoupling Caps Pcb Layout Electrical Engineering Stack Exchange



Power Supply Filter Design For Pcb Tempo Automation



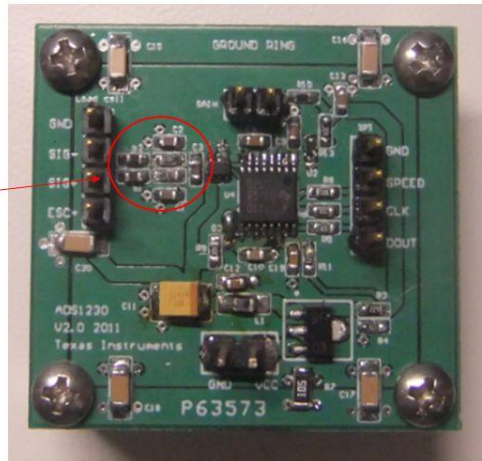
Cmos Is Different Pcb Design For Both Low Noise And Low Emi



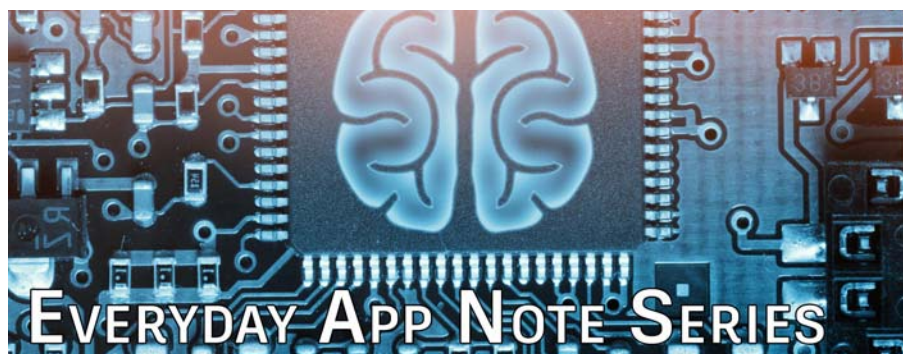
PCB Layout (SMD)

- Ground Plane on Bottom-side

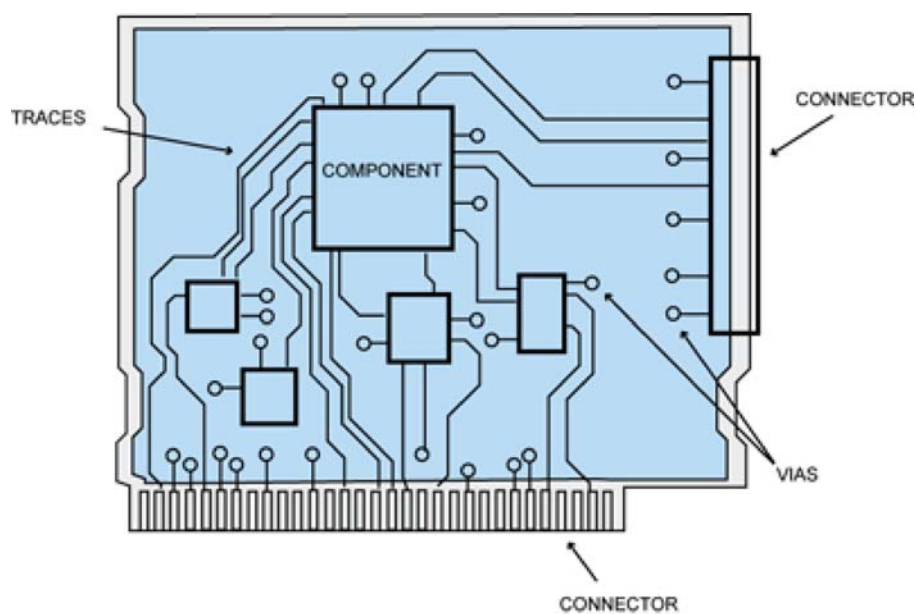
Filter:
 $R=1\text{Kohm}$, $C3=2.2\text{uf}$, $C1, C2=0.22\text{uf}$,
2 Beads



Design And Layout Guidelines Ppt Download



Reduce Emi Pcb Design Guidelines Everyday App Note Eagle
Blog



Learnemc Pcb Layout

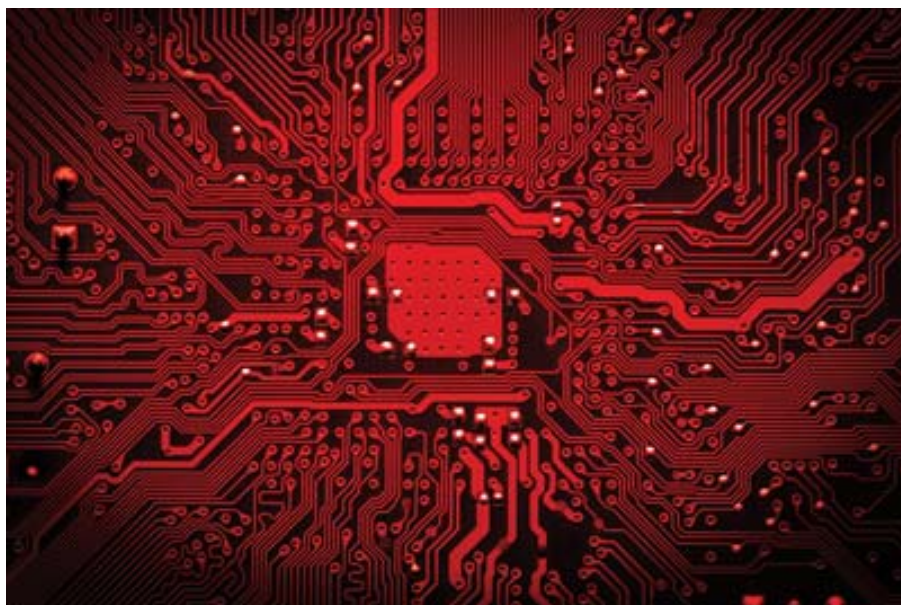


Emi Filter Design

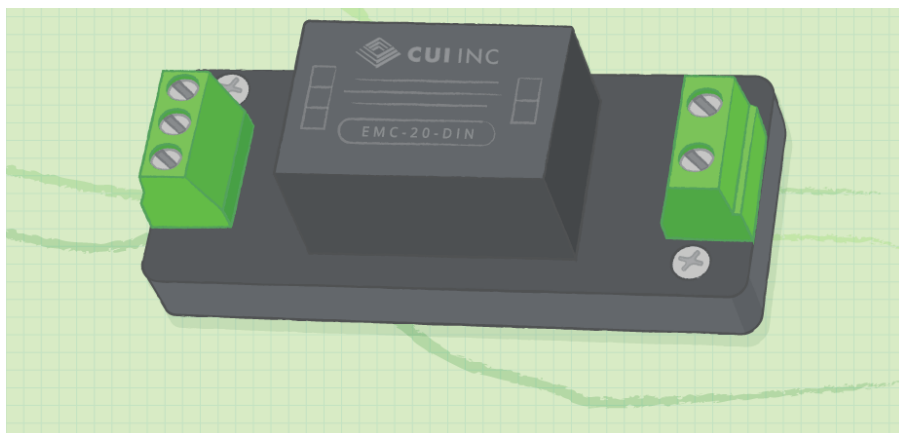


https://www.st.com/resource/en/application_note/dm00403849-layout-recommendations-for-the-design-of-boards-with-st25r3911bst25r391x-devices-stmicroelectronics.pdf

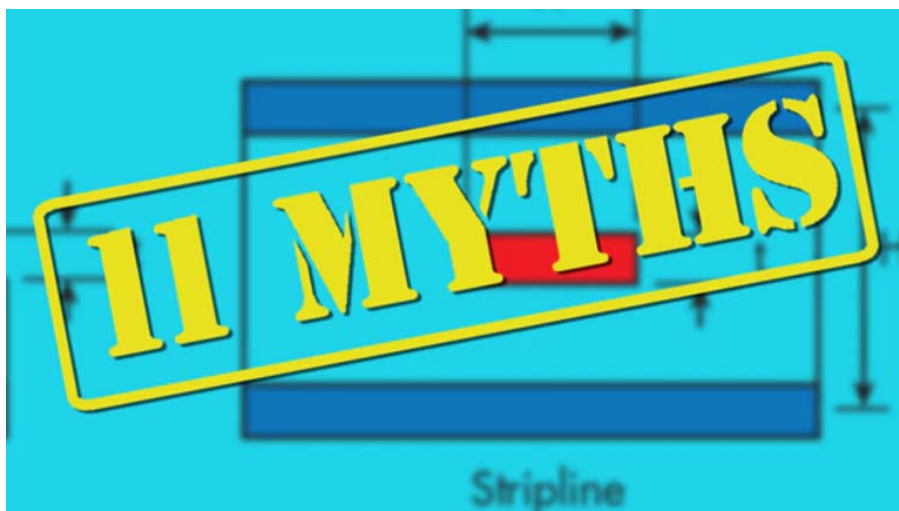
Pdf Emi Filter Design Part I Conducted Emi Generation Mechanism



Emi And Signal Integrity How To Address Both In Pcb Design In



How To Select An Emi Power Filter Cui Inc



11 Myths About Emi Emc Electronic Design