Meaning DeFinition is an interrated circuit which Com Perform avithmetic and 199ial Micro-Processor operations- need to memory and is an intervaled circuit which consist of CPU and other components like Micro-Confroher BOM and BAM and I/o Ports Simple used to control ambedded statem 578 tem Controlled by specien Rupose" Computer states which designed to Embedded Statems Perform one or fe w dedicated Functions Conside by definition of a Me Chatronic me chamican Part that has to Perform Certian motions and an ST Stems electronic part that adds intelligence to the system Processor Com Process n-bit Processor of Josta last actime bota larger than n-bit has to be broken into n-bit Pieces to be Processed Micro Contraktor Micro - Processor Micro-Contraller Gendral Put Pose Specific Purpose and include a Micko Processor in side Ilo Ports Contain no BAM, no Bom, no I/O on The chip it sof Ad vontage Ad wont spe: its systems are versatile ending ideal For application in which cos The designer to decide on the and space are critical Ports to Fit the tasks Disad Vent-ge Disadvent Je: The designer con't and any its systems are bullsier and externed memory, I/o Ports or timers to it to Eit more ad Vanced tasks sune stellen Si Ve because BAM Bomand Ilo abded extornally

Compare between Won-Norman Us Har very - Al chitecture Osingto Comman memory space whose Program instructions and data one stored Con- Mamon @ there is a sille douta bus retines both instructions and fater Harvard avonitecture @ se Parete memory area for instractions and another are for John 2 one bus connects the CPu to the BAM and another connects the Cru to Bom

as By simple and Mastrate the types of Bom EPBOM (Programable Bom)
Lower Com burn information into Le information com be burned into PROM only once for every bit is fuse So it's Campo OTP DEPROM CE rasable Programable Bom)
to Programmed and erased thous ands of time La refused by [UV] of [UV-EPBOM]
La not Possible to evase Particular byte of distain EPBOM the Whole Joya is delta BI EEPBOM [electrically evasable Programable Bom]

to evased by electrical signal

to you can solect the bitle to be evased

wit's cost is higher than un-EPBOM to high de hoty low coast, non volotile, fast HI Flash Memory s electrically reprogramable

CGBBY Simple of at Must keep the Tipes BAM 28 tallic BAM made of Elif Flors
to Storage Cells are made of Elif Flors
to it has medium Power consumtion.
To volodile when the Power is lost the data will be lost
to each Cell requies at least 6 transisters. DSBAM ESTATIC BAM) Lahas more conflex design 25 OBAM [Ofnomic BAM] to using adaitor to store sata. by volatile. Is need to refreshing due to change lossage (3) NUBAM Enon Volatilo BAM) to SBAM with buttory backup. 4 allows the CPu to Bood and write to it 6 uses internal 1: thium buttor as a back up Ly another type Combines SRAM and EEPBOM Bom - Che dosent have the capolitity
to write to it

Any be writen to by an obtened
delice such as burner "

The volice writedle Erose morror astament shed."

17/00	Voldile	wifealor	Eyase	Matellaso	Cost (ANBIA	s Aced .
SBAM	yes	yes	Byte	bolimber	ellensive	
DRAM	yes	Yes	Byte	antimites	moderade	modovate
musked Both	No	No	NIA	NIA	in offensib	East
PROM		once	NIA		modelato"	
EPBom		yes	entire	1: mi to	numbered	Fast
EEPBom	No	yes	BHe	pinipl	L. S. P. S. S. S.	East Veet
Flash	No	yes	sector	limited	moderate	Fast Rend SI = W coxite
MBAR	-	yes			la lengi 16	