

Execution

1 load.

$\downarrow \begin{matrix} R \\ 0 \\ \text{Load} \end{matrix} \frac{\begin{matrix} M \\ 3 \\ 2 \end{matrix}}{R_0} \leftarrow M[3^2]$

2 load value

$\downarrow \begin{matrix} R \\ 1 \\ \text{Load} \end{matrix} \frac{6A}{R_1} \leftarrow 6A$

3 store.

$\downarrow \begin{matrix} R \\ 2 \\ \text{Store} \end{matrix} \frac{\begin{matrix} M \\ IF \end{matrix}}{R_2} \rightarrow M[IF]$

4 MOVE

$\downarrow \begin{matrix} R_1 \\ R_2 \\ \text{MOVE} \end{matrix} \frac{R_1}{R_2} \rightarrow R_2$

$R_1 \boxed{10}$

$R_2 \boxed{20}$
10

①

5 ADD.

5 2^R R₀
ADD $R_2 \leftarrow R_1 + R_0$

6 $\rightarrow X$

7,8,9 $\rightarrow A, \rightarrow$

1 \Rightarrow Jump.

B 3^R 2^M 1

Jump to M[21]

if $R_3 = [R_0]$

(2)

$B \underset{=} \underline{2} \overset{M}{F}$

Jump to $M[\underline{3F}]$ if $R_2 = R$

Conditional Jump.
if $R? = R_0$

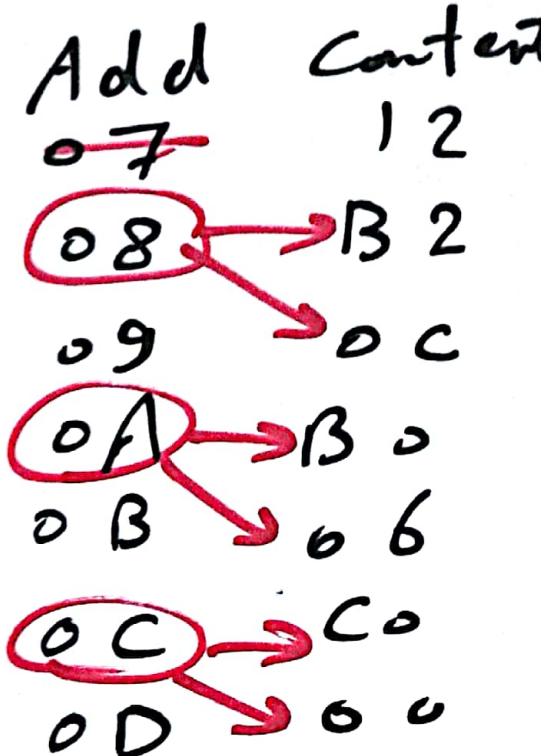
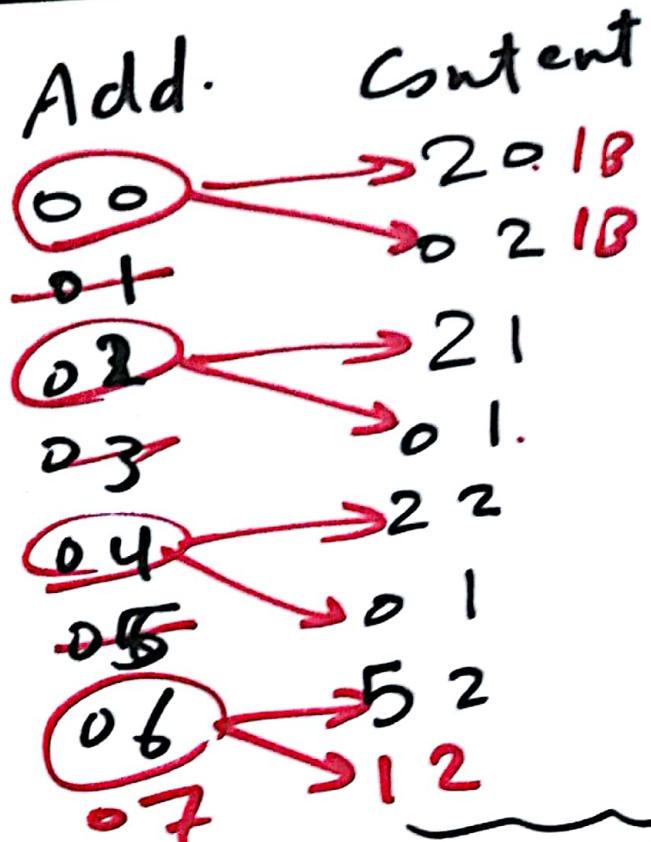
$\rightarrow B \underset{=} \underline{0 A I}$

Jump to $M[AI]$ if
 $R_0 = R_0$

unconditional Jump.

③

EX:



* Instruction Table

Start Add	inst.	Description
00	20 02	load R0 ← 02
02	21 01	load R1 ← 01
04	22 01	load R2 ← 01
06	52 12	Add R2 ← R1 + R2
08	B2 0C	Jump to M[0C] if <u>R2=R</u>
0A	B0 06	Jump to M[06]
0C	C0 00	Halt

(4)

Face Table

$$\frac{R_0}{02} \quad \frac{R_1}{01} \quad \frac{R_2}{00}$$

St.	T Face
00	$R_0 = 0^2$
02	$R_1 = 0^1$
04	$R_2 = 0^1$
06	$R_2 = R_1 + R_2 = 0^2$ if $R_2 = R_0 \checkmark$
08 soc	Halt.

a) What abit pattern will be in R_2 ? $R_2 = (02)_{16}$
 $= 0000\text{.}0000$

b) How many times will the instruction at address $\underline{\underline{06}}$ be executed?
 one time

(5)

~~E^{x.}~~ Suppose the memory cells at address A4 to B1 contains the following program:

<u>Add.</u>	<u>Content</u>
AA	
A4	20
AS	00
AC	21
A7	03
A8	22
A9	01
AA	B1
AB	B0

<u>Add.</u>	<u>Content</u>
AC	50
AD	02
AE	B0
AF	AA
BA	C0
B1	00

*Instruction Table

St.	Instruction	Description
A4	2000	load $R_0 \leftarrow 00$
A6	2103	load $R_1 \leftarrow 03$
A8	2201	load $R_2 \leftarrow 01$
AA	$B1 \underline{B0}^M$	Jump to $M[B_0]$ if $R_1 = R_0$
Ac	5002	Add $R_0 = R_0 + R_2$
AE	$B0 \underline{AA}^M$	unconditional Jump.
B_0	0000	Halt / Stop.

7

* TFace Table

$$\frac{R_0}{00} \quad \frac{R_1}{03} \quad \frac{R_2}{01}$$

St.	TFace	
A4	$R_0 = 00$	03
A6	$R_1 = 03$	
A8	$R_2 = 01$	
AA④	if $\tilde{R}_1 = \tilde{R}_0$ X	
AC	$R_0 = \tilde{R}_0 + \tilde{R}_2 = 01$	
AE	✓ Jump to M[AA]	
AA④	if $\tilde{R}_1 = \tilde{R}_0$ X	
AC	$R_0 = \tilde{R}_0 + \tilde{R}_2 = 02$	
AE	✓ Jump to M[AA]	
AA④	if $\tilde{R}_1 = \tilde{R}_0$ X	
AC	$R_0 = \tilde{R}_0 + \tilde{R}_2 = 03$	
AE	✓ Jump to M[AA]	
AA④	if $\tilde{R}_1 = \tilde{R}_0$ ✓ Jump to M[B]	
B0	Stop	④

→ Assuming the program execution starts address A4 answer the following questions?

- a) What is the content of R0 after the first time the instruction at address AA is executed? $R_0 = 00$
- b) What is the content of R0 after the second execution of the instruction at address AA? $R_0 = 01$
- c) How many times the instruction at address AA is executed before the machine halts?
four times.

→ Suppose the memory cell at address E4 to F3 contain the following program:-

Add.	Content
E4	20
E5	00
E6	21
E7	04
E8	22
E9	01
EA	B1
EB	F2
EC	50
ED	02

Add.	Content
ED	02
EE	53
EF	01
FA	B0
FI	EA
F2	C0
F3	00

b

Instruction Table

st.	inst.	Description.
E4	2000	load $R_0 \leftarrow 0^0$
E6	2104	load $R_1 \leftarrow 0^4$
E8	2201	load $R_2 \leftarrow 0^1$
EA	B1 F2	Jump to $M[F_2]$ if $R_{10} < 0$
Ec	5002	Add $R_0 \leftarrow R_0 + R_2$
EE	5301	Add $R_3 \leftarrow R_0 + R_1$
F0	B0 EA	unconditional Jump ✓
F2	C000	halt / stop.

11

Trace Table

R_0	R_1	R_2	R_3
00	04	05	05
01	05	06	06
02	05	07	07
03	04	08	08

St.

Trace.

E4

$$R_0 = 00$$

04
05
06
07
08

E6

$$R_1 = 04$$

E8

$$R_2 = 01$$

EA

$$\text{if } R_1 = R_0 \quad X$$

EC

$$R_0 = R_0 + R_2 = 01$$

EE

$$R_3 = R_0 + R_1 = 05$$

F0

$$\checkmark \quad \text{if } R_1 = R_0 \quad X$$

SEA

$$R_0 = R_0 + R_2 = 02$$

EE

$$R_3 = R_0 + R_1 = 06$$

F0

$$\checkmark \quad \text{if } R_1 = R_0 \quad X$$

EA

$$R_0 = R_0 + R_2 = 03$$

EE

$$R_3 = R_0 + R_1 = 07$$

F0

$$\checkmark \quad \text{if } R_1 = R_0 \quad X$$

SEA

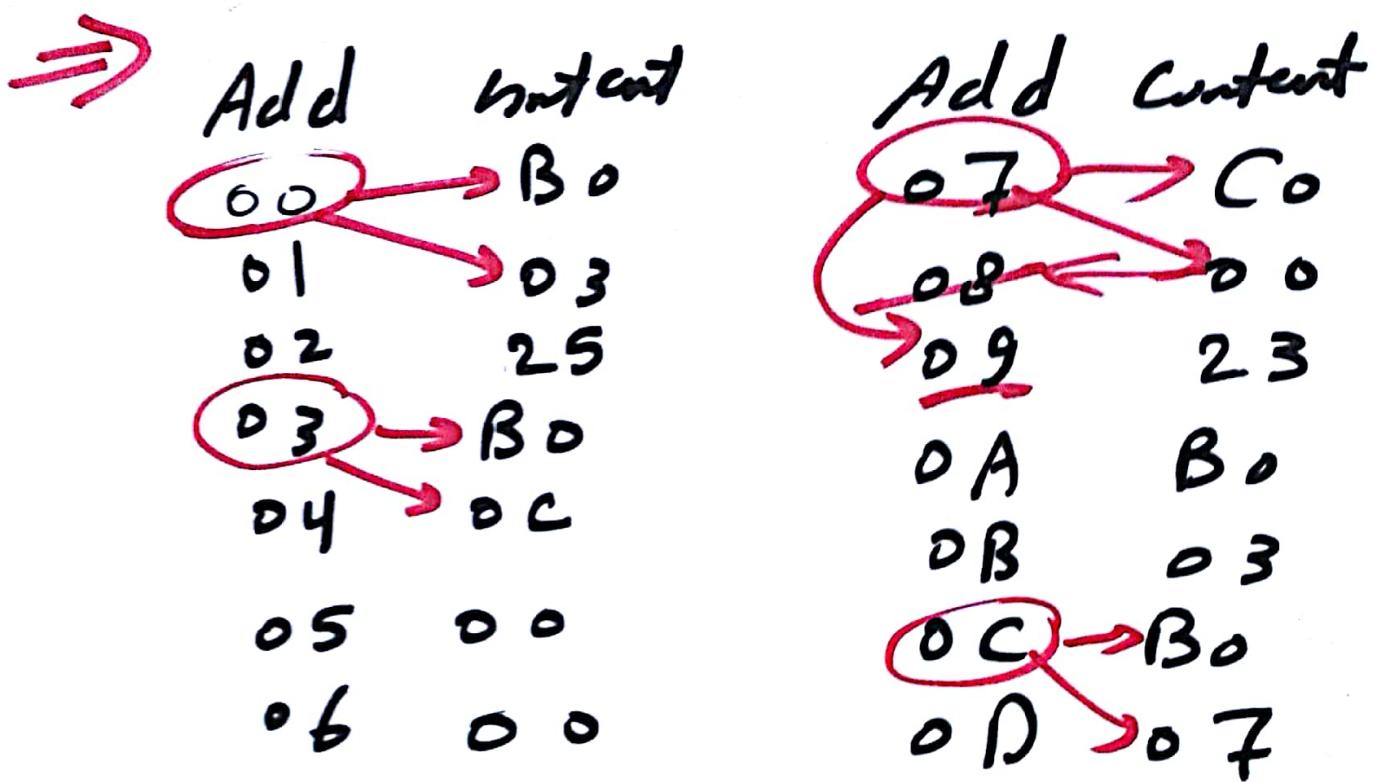
$$R_0 = R_0 + R_2 = 04$$

EE

$$R_3 = R_0 + R_1 = 08$$

SEA

$$\xrightarrow{\text{if } R_0 = R_0} F_2 \xrightarrow{\text{Halt 1st. P.}} 12$$



- How many instructions will be executed after the machine halts?
four instructions.
- ⇒ What abit Pattern will be in the Program Counter when the machine halts?

09

a) What is the content of R₀ after the first time the inst. at EA is executed?

$$R_0 = 00$$

b) What is the content of R₀ after the second time the inst. at EA is executed?

$$R_0 = 01$$

c) How many times the inst. at address EA is executed before the machine halts?

5 times.

d) What is the final value of R₃ after the machine halts?

$$R_3 = 08.$$

instruction Table

st.	inst.	Meaning
00	<u>B003</u>	un Conditional Jump Jump to M[03]
03	<u>B00C</u>	Jump to M[0C]
0C	<u>B007</u>	Jump to M[07]
07	<u>C000</u>	halt /stop.

Logical Operations:

AND

, OR, XOR

AND \rightarrow \wedge

<u>AND</u>	0	0	1	1
	0	1	0	1
	$\frac{0}{0}$	$\frac{0}{1}$	$\frac{0}{0}$	$\frac{1}{1}$

OR

<u>OR</u>	0	0	1	1
	0	1	0	1
	$\frac{0}{0}$	$\frac{1}{1}$	$\frac{0}{1}$	$\frac{1}{1}$

XOR

0	1	0	1
0	1	0	1
$\frac{0}{0}$	$\frac{1}{0}$	$\frac{0}{1}$	$\frac{1}{1}$

$0 = \text{متساوية}$
 $1 = \text{ مختلفة}$

(15)

EX

A AND

$$\begin{array}{r} 1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 1 \\ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 0 \\ \hline 1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 0 \end{array}$$

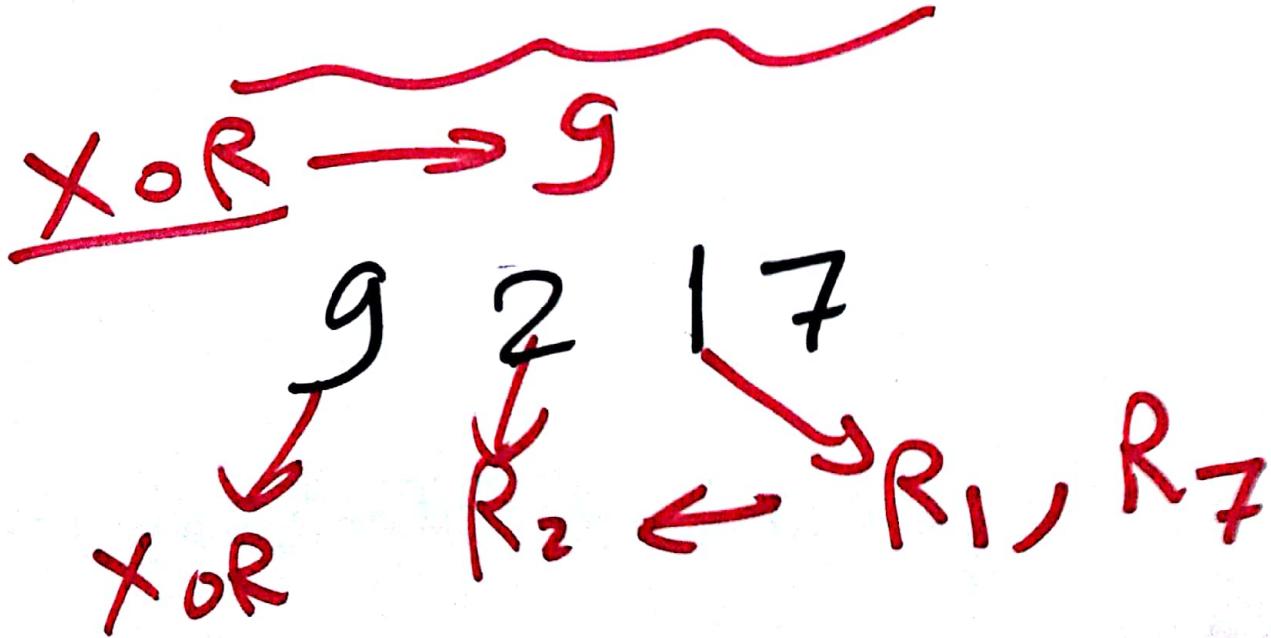
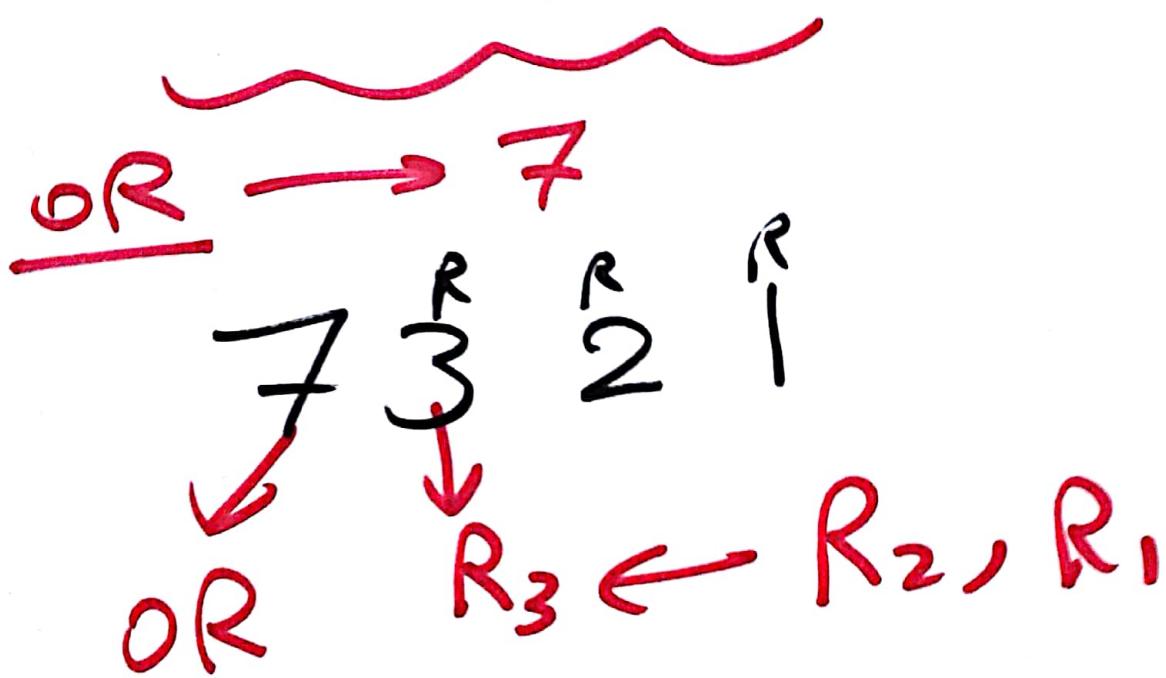
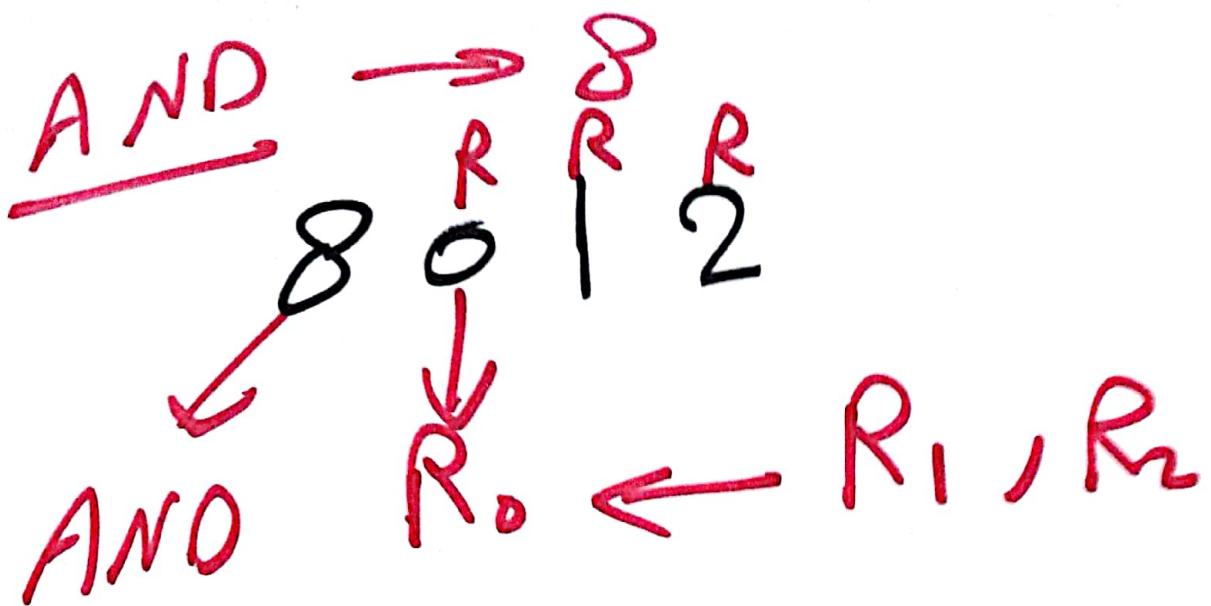
OR

$$\begin{array}{r} 1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 1 \\ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 0 \\ \hline 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \end{array}$$

XOR

$$\begin{array}{r} 1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 1 \\ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 0 \\ \hline 0 \ 0 \ 0 \ 0 \ 1 \ 1 \ 0 \end{array}$$

(16)



(F)

Given $\Rightarrow \downarrow$ B7

① 10A6	load	$R_0 \leftarrow M[A6]$
② 21E7	load	$R_1 \leftarrow E7$
③ 8201	AND	$R_2 \leftarrow R_0, R_1$
④ 2142	load	$R_1 \leftarrow 42$
⑤ 7321	OR	$R_3 \leftarrow R_2, R_1$
⑥ 2181	load	$R_1 \leftarrow 81$
⑦ 9031	XOR	$R_0 \leftarrow R_3, R_1$
⑧ 30A6	Store	$R_0 \rightarrow M[A6]$
⑨ C000	halt / stop.	

\Rightarrow if the contents
of memory cell

$M[A6] = \underline{\underline{B7}}$

\rightarrow write the content of
 $M[A6]$ at the end
of the execution. 18

TFace Table

$$\frac{R_0}{B7} \\ 66$$

$\frac{R_1}{E7}$ 42	$\frac{R_2}{A7}$ 81
------------------------	------------------------

st TFace.

① $R_0 = B7$

② $R_1 = E7$

③ $R_2 = R_0 \text{ AND } R_1$
 $B7 \quad E7$

$$\begin{array}{r} \text{AND} \\ \hline 0 & 1 & 1 & 0 & 1 & 1 & | \\ 1 & 1 & 0 & 0 & 1 & 1 & | \\ \hline 1 & 0 & 1 & 0 & 0 & 1 & 1 \end{array}$$

$\rightarrow A \quad 7$

④ $R_1 = 42$

⑤ $R_3 = R_2 \text{ OR } R_1$
 $A7 \quad 42$

$$\begin{array}{r} \text{OR} \\ \hline 1 & 0 & 1 & 0 & 0 & 1 & | \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 \\ \hline 1 & 1 & 0 & 0 & 1 & 1 & | \end{array} \leftarrow E7$$

⑥ $R_1 = 81$

⑦ $R_0 = R_3 \text{ Xor } R_1$
 $E7 \quad 81$

$$\begin{array}{r} \text{Xor} \\ \hline 1 & 1 & 0 & 0 & 0 & 1 & | \\ 0 & 0 & 1 & 0 & 0 & 0 & 1 \\ \hline 0 & 1 & 1 & 0 & 0 & 1 & 0 \end{array}$$

66

19

⑧ } $M[A_6] = R_0 = 66$
⑨ } halt.

20

logical masks / filters

logical operation

AND

$\begin{array}{cccccc} X & X & X & X & Y & Y \\ | & | & | & | & \text{mask} \\ \hline F & & & & 0 & \end{array}$

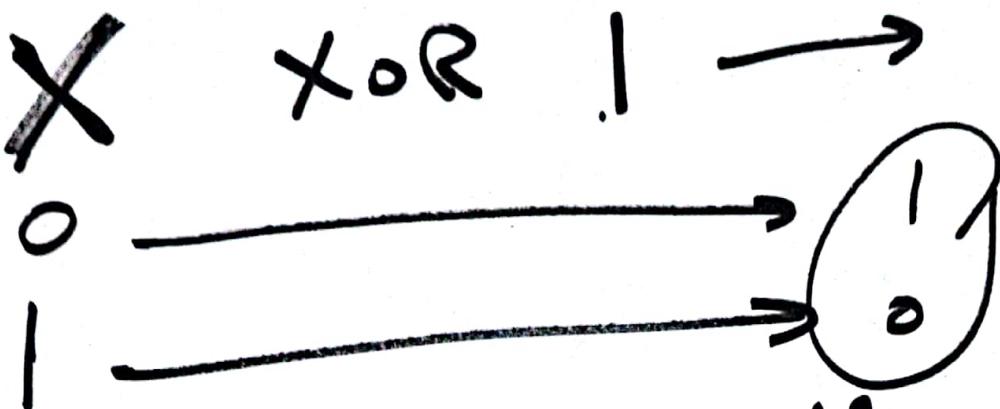
$\begin{array}{cccccc} X & X & X & X & 0 & 0 & 0 \\ \hline \text{Most Significant} & & & & & & \end{array}$

Least
Significant
bits.

$\begin{array}{cccccc} X & X & X & X & Y & Y & Y \\ | & | & | & | & | & | & \\ \hline \text{OR} & 0 & 0 & 1 & 1 & 1 & 0 & 0 \\ \hline X & X & 1 & 1 & 1 & 1 & Y & Y \end{array}$

21

XoR



I' 's Complement

X XoR I \rightarrow X' ^{dash}
Prim
Complement

XoR I $0 0 0 0 0 1$

~~$X' X' X' X' Y Y Y Y$~~
 $X' X' X' X' Y Y Y Y$ ²²

AND \rightarrow 0 ↗

OR \rightarrow 1 ↗

XOR \rightarrow 1's comp.
with 1



23

A^{NP}

X	X	X	X	Y	Y	Y	Y
1	1	1	0	0	1	1	1

OR

X	X	X	0	0	Y	Y	Y	
0	R	0	1	1	0	0	1	0

x_{OR}

X	1	1	0	0	1	1	Y
1	0	0	0	0	0	0	1

x'

X'	1	1	0	0	1	1	Y'
1	1	1	0	0	1	1	Y'

x'

X'	1	1	0	0	1	1	Y'
1	1	1	0	0	1	1	Y'

24

$X \quad X \quad X \quad X \quad Y \quad Y \quad Y \quad Y$

OR $0 \quad | \quad 0$

$X \quad | \quad Y$

$X \oplus R$ $| \quad 0 \quad 0 \quad | \quad | \quad | \quad 0 \quad 0 \quad |$

$X \quad | \quad | \quad 0 \quad 0 \quad | \quad | \quad Y$

$OR \Rightarrow 1$

هذا هو المعاين ١٥٠ بـ ١ ①

OR مع
 $(X \oplus R)$ بـ $\left(\begin{matrix} 0 \\ 1 \end{matrix} \right)$
أو $\left(\begin{matrix} 1 \\ 0 \end{matrix} \right)$

25

26

$\begin{array}{cccccc} X & X & X & X & Y & Y & Y \\ \text{AND} & | & 0 & 0 & 0 & 0 & 1 \end{array}$

$\begin{array}{ccccccc} & & & & & & \\ \hline X & 0 & 0 & 0 & 0 & 0 & y \end{array}$

$\begin{array}{ccccccc} \text{XOR} & | & | & | & 0 & 0 & | & | & | \\ \hline \end{array}$

$\begin{array}{ccccccc} X & 1 & 1 & 0 & 0 & 1 & 1 & y' \end{array}$

26

27