

Devices Connected/Referenced

AD8276	Low Power, Wide Supply Range, Low Cost Unity Gain Difference Amplifier
AD8603	Micropower, Low Noise, CMOS Operational Amplifier

High Precision, Low Cost Current Sources Using the [AD8276](#) Difference Amplifier and the [AD8603](#) Op Amp

CIRCUIT FUNCTION AND BENEFITS

Current sources are widely used in industrial, communication, and other equipment for sensor excitation and machine-to-machine communication, etc. For example, the 4 mA-to-20 mA loop is widely used in process control equipment.

Programmable current sources can be built using a DAC, amplifier (op amp or difference amplifier), and matched resistors. Low value current sources can be integrated into low output current sources or amplifiers. For example, the [AD8290](#) is an instrumentation amplifier with a single integrated current source, and the [AD7794](#) is a high resolution Σ - Δ ADC with two integrated current sources. For high currents, external MOSFETs or transistors will generally be required.

Current sources using the low power [AD8276](#) difference amplifier and the [AD8603](#) op amp are affordable, flexible, and small in size. Performance characteristics such as initial error, temperature drift, and power dissipation are excellent.

CIRCUIT DESCRIPTION

The current source circuit is shown in Figure 1. Reference voltage, V_{REF} , is applied to the noninverting input of the [AD8276](#). This voltage controls the amount of output current, I_O . The inverting input of the [AD8276](#) is connected directly to ground. There are four laser-trimmed, 40 k Ω resistors inside the [AD8276](#) that are connected to the input pins, the REF pin, and the SENSE pin. The output of the [AD8276](#) is used to drive a transistor if a high current output is needed.

If the resistors are perfectly matched, the input voltage, V_{REF} , appears across R_1 , thereby producing a constant load current, I_O , which is equal to V_{REF}/R_1 .

The [AD8603](#) op amp is used in the feedback loop of the circuit and was chosen because of its low bias current (maximum 1 pA) and offset voltage (less than 50 μ V). The low bias current makes it possible to interface to a high impedance load without introducing significant offset errors. The [AD8603](#) low temperature drift specification (4.5 μ V/ $^{\circ}$ C maximum) allows operation over a wide temperature range, and the amplifier also features low noise and rail-to-rail inputs and outputs.

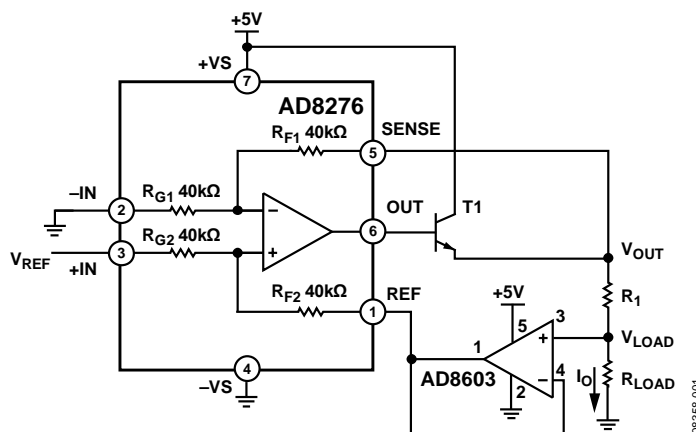


Figure 1. Current Source Using the [AD8276](#) Difference Amplifier and the [AD8603](#) Op Amp (Simplified Schematic)

The value of the output current, I_O , can be calculated by using the equation

$$I_O = \frac{V_{REF} \left(\frac{R_{F2}}{R_{G2}} + \frac{R_{F1}}{R_{G1}} \times \frac{R_{F2}}{R_{G2}} \right)}{R_1 \left(1 + \frac{R_{F2}}{R_{G2}} \right) + R_{LOAD} \left(\frac{R_{F2}}{R_{G2}} - \frac{R_{F1}}{R_{G1}} \right)} \quad (1)$$

Because the [AD8276](#) has very tight resistor matching, $R_{F1}/R_{G1} = R_{F2}/R_{G2} = 1$, and Equation 1 can be simplified as

$$I_O = \frac{V_{REF}}{R_1} \quad (2)$$

Equation 1 shows that the primary errors of the circuit in Figure 1 are due to the internal resistor matching, the tolerance of R_1 , and the tolerance of the load resistance. The [AD8276](#) (B-grade) maximum gain error is 0.02%. The [AD8276](#) (A-grade) maximum gain error is 0.05%. Overall accuracy of 0.02% is possible with the circuit.

At the same time, the accuracy of R_1 is critical, so it should have 0.1% tolerance or better. This error can be removed by calibration.

The amount of output current, I_O , available from the circuit is limited by the op amp input range, the difference amplifier output range, and the difference amp SENSE pin voltage range.

Based on Figure 1, three conditions have to be met:

1. $V_{LOAD} = I_O \times R_{LOAD}$ must be within the [AD8603](#) op amp input range.
2. $V_{OUT} = I_O \times (R_{LOAD} + R_1)$ must be within the [AD8276](#) SENSE pin voltage range: $2(-V_S) - 0.2$ V to $2(+V_S) - 3$ V.
3. $I_O \times (R_{LOAD} + R_1) + V_{BE}$ must be within the [AD8276](#) output voltage range: $-V_S + 0.2$ V to $+V_S - 0.2$ V.

The [AD8276](#) rail-to-rail output feature and the ability to operate on a 2.5 V to 36 V power supply allow a wide range of output current.

The [AD8276B](#) offset voltage drift of 2 μ V/°C maximum and gain drift of 1 ppm/°C maximum yield low temperature drift and wide temperature operation. The specifications for the [AD8276A](#) are 5 μ V/°C and 5 ppm/°C, respectively.

Both the [AD8276](#) (8-lead MSOP) and the [AD8603](#) (5-lead TSOT-23) are in small packages, thereby minimizing the board area required by the circuit.

The external current source transistor, T1, should have a V_{CB} breakdown voltage higher than the [AD8276](#) supply voltage. The transistor maximum collector current should be higher than the expected output current with suitable headroom, and the transistor power dissipation limits must be observed. Low cost transistors, such as the 2N3904, 2N4401, 2N3391, and MPSA06 are recommended.

The [AD8276](#) can drive output currents of 15 mA or less without the need for the external transistor or MOSFET.

Testing results under room temperature based on the [AD8276A](#), [AD8603](#), and 2N3904 are shown in Figure 2. R_1 is 50 Ω with 0.1% tolerance. It is obvious that the actual output complies with the calculated results. On the scale shown, the measured results are indistinguishable from the ideal results because they are within 0.5% of each other with the average of less than 0.1% limited by the R_1 's tolerance.

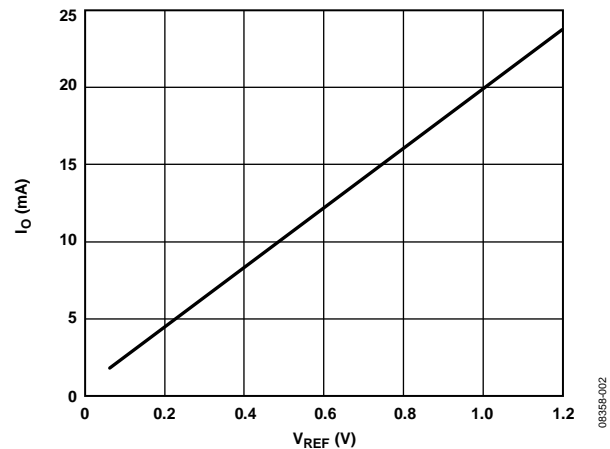


Figure 2. Test Results for Current Source Using the [AD8276A](#), [AD8603](#), and 2N3904 ($R_1 = 50 \Omega$, $R_{LOAD} = 100 \Omega$, $V_S = +5$ V, $T_A = 25^\circ\text{C}$)

As with any high accuracy circuit, proper layout, grounding, and decoupling techniques must be employed. See [Tutorial MT-031, Grounding Data Converters and Solving the Mystery of AGND and DGND](#) and [Tutorial MT-101, Decoupling Techniques](#) for more details.

COMMON VARIATIONS

If higher power supplies are needed for higher value output current, the [OP1177](#), [AD8661](#), and [AD8663](#) can be used. The important specifications are power supply range, bias current, offset voltage, input voltage range, and temperature drift.

If a fixed current source is required, V_{REF} can be supplied by a voltage reference such as the [ADR36x](#) family.

The [ADR82x](#) family integrates a voltage reference and an op amp and can operate on a power supply up to 36 V. This provides an additional space saving option.

If a dual-current source is needed, the [AD8607](#) and the [AD8277](#) are good choices.

If programmable current sources are needed, use a precision 14-bit or 16-bit DAC to generate the reference voltage, V_{REF} . The [AD5560](#), [AD5060](#) (single), and [AD5663R](#) (dual) are suitable for this application.

LEARN MORE

Kitchen, Charles and Lew Counts. *A Designer's Guide to Instrumentation Amplifiers (3rd Edition)*. Analog Devices.

MT-031 Tutorial, *Grounding Data Converters and Solving the Mystery of "AGND" and "DGND."* Analog Devices.

MT-035 Tutorial, *Op Amp Inputs, Outputs, Single-Supply, and Rail-to-Rail Issues*. Analog Devices.

MT-061 Tutorial, *Instrumentation Amplifier Basics*. Analog Devices.

MT-068 Tutorial, *Difference and Current Sense Amplifiers*, Analog Devices.

MT-101 Tutorial, *Decoupling Techniques*. Analog Devices.

Data Sheets

[AD8276 Data Sheet](#)

[AD8277 Data Sheet](#)

[AD8603 Data Sheet](#)

[AD8607 Data Sheet](#)

[AD8661 Data Sheet](#)

[AD8663 Data Sheet](#)

[ADR361 Data Sheet](#)

[ADR821 Data Sheet](#)

[OP1177 Data Sheet](#)

REVISION HISTORY

8/13—Rev. 0 to Rev. A

Changes to Circuit Description Section.....2

7/09—Revision 0: Initial Version

(Continued from first page) Circuits from the Lab circuits are intended only for use with Analog Devices products and are the intellectual property of Analog Devices or its licensors. While you may use the Circuits from the Lab circuits in the design of your product, no other license is granted by implication or otherwise under any patents or other intellectual property by application or use of the Circuits from the Lab circuits. Information furnished by Analog Devices is believed to be accurate and reliable. However, Circuits from the Lab circuits are supplied "as is" and without warranties of any kind, express, implied, or statutory including, but not limited to, any implied warranty of merchantability, noninfringement or fitness for a particular purpose and no responsibility is assumed by Analog Devices for their use, nor for any infringements of patents or other rights of third parties that may result from their use. Analog Devices reserves the right to change any Circuits from the Lab circuits at any time without notice but is under no obligation to do so.