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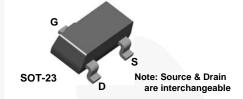


March 2015

# MMBF5460 / MMBF5461 / MMBF5462 P-Channel General-Purpose Amplifier

## **Description**

This device is designed primarily for low level audio and general-purpose applications with high impedance signal sources. Sourced from process 89.



## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MMBF5460	6E	SOT-23 3L	Tape and Reel
MMBF5461	61U	SOT-23 3L	Tape and Reel
MMBF5462	61V	SOT-23 3L	Tape and Reel

## **Absolute Maximum Ratings**(1), (2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
$V_{DG}$	Drain-Gate Voltage	-40	V
$V_{GS}$	Gate-Source Voltage	40	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## Thermal Characteristics(3)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation	225	mW
P <sub>D</sub>	Derate Above 25°C	1.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	556	°C/W

## Note:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Parameter Conditions		Min.	Тур.	Max.	Unit
Off Chara	acteristics						
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_G = 10 \mu A, V_{DS} = 0$		40			V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0				5.0	nA
		V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0, T <sub>A</sub> = 100°C				1.0	μΑ
	Gate-Source Cut-Off Voltage	$V_{DS} = 15 \text{ V}, I_D = 1.0 \mu\text{A}$	MMBF5460	0.75		6.0	V
$V_{GS(off)}$			MMBF5461	1.0		7.5	
			MMBF5462	1.8		9.0	
V <sub>GS</sub>	Gate-Source Voltage	$V_{DS} = 15 \text{ V}, I_{D} = 0.1 \text{ mA}$	MMBF5460	0.5		4.0	V
		$V_{DS} = 15 \text{ V}, I_{D} = 0.2 \text{ mA}$	MMBF5461	8.0		4.5	
	$V_{DS} = 15 \text{ V}, I_{D} = 0.4 \text{ mA}$ MMI		MMBF5462	1.5		6.0	
On Chara	cteristics						
	Zero-Gate Voltage Drain Current <sup>(4)</sup>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 MMBF546	MMBF5460	-1.0		-5.0	mA
			MMBF5461	-2.0		-9.0	
			MMBF5462	-4.0		-16.0	
Small Sig	nal Characteristics						
	Forward Transfer Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz	MMBF5460	1000		4000	μmhos
9 <sub>fs</sub>			MMBF5461	1500		5000	
			MMBF5462	2000		6000	
g <sub>os</sub>	Output Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz				75	μmhos
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			5.0	7.0	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			1.0	2.0	pF
NF	Noise Figure	$V_{DS} = 15 \text{ V}, V_{GS} = 0, R_G = 1.0 \text{ M}\Omega,$ f = 100 Hz, BW = 1.0 Hz			1.0	2.5	dB
e <sub>n</sub>	Equivalent Short-Circuit Input Noise Voltage	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 100 Hz, BW = 1.0 Hz			60	115	nV/√Hz

#### Note:

4. Pulse test: pulse width  $\leq$  300 ms, duty cycle  $\leq$  2.0%

## **Typical Performance Characteristics**

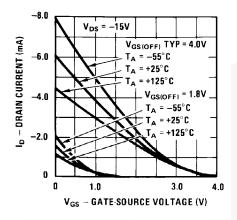


Figure 1. Transfer Characteristics

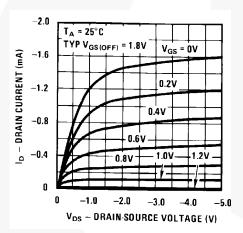


Figure 3. Common Drain-Source

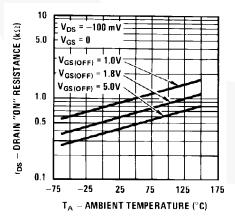


Figure 5. Leakage Current vs. Voltage

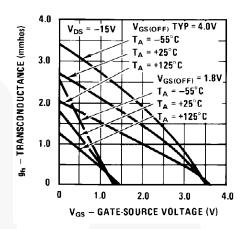


Figure 2. Transfer Characteristics

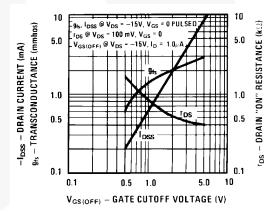


Figure 4. Parameter Interactions

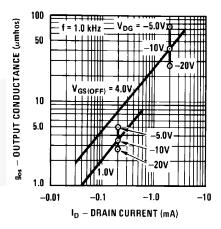


Figure 6. Output Conductance vs. Drain Current

## **Typical Performance Characteristics (Continued)**

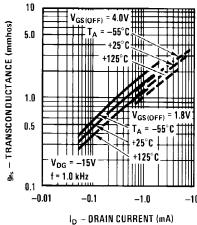
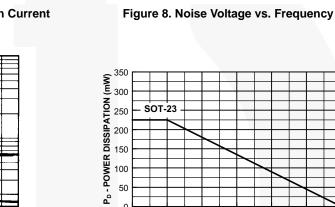


Figure 7. Transconductance vs. Drain Current



25

Figure 10. Power Dissipation vs. **Ambient Temperature** 

50 75 100 **TEMPERATURE (°C)** 

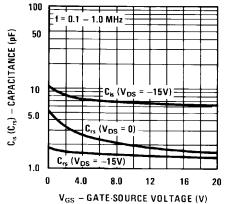
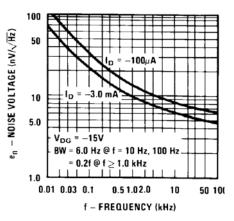


Figure 9. Capacitance vs. Voltage



150

## **Physical Dimensions** 0.95 2.92±0.20 3 1.40 1.30+0.20 2.20 2 0.60 0.37 (0.29) -0.95 ⊕ 0.20 M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A -1.20 MAX 0.10 (0.93) ○ 0.10 M C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. 0.23 B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994.

Figure 11. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE

**PLANE** 

SEATING E) DRAWING FILE NAME: MA03DREV10

0.20 MIN

(0.55)

**DETAIL A** 





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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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