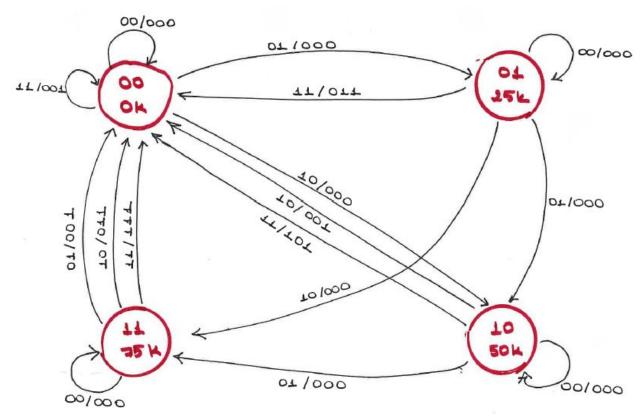
### **CS 303 LAB4 REPORT**

## **State Diagram:**



## **State Table:**

A 1(t)	A 0 (t)	Input 1	Input 0	A 1 (t+1)	A 0 (t+1)	Change 1	Change 0	Output
0	0	0	0	0	0	0	0	0
0	0	0	1	0	1	0	0	0
0	0	1	0	1	0	0	0	0
0	0	1	1	0	0	0	0	1
0	1	0	0	0	1	0	0	0
0	1	0	1	1	0	0	0	0
0	1	1	0	1	1	0	0	0
0	1	1	1	0	0	0	1	1
1	0	0	0	1	0	0	0	0
1	0	0	1	1	1	0	0	0
1	0	1	0	0	0	0	0	1
1	0	1	1	0	0	1	0	1
1	1	0	0	1	1	0	0	0
1	1	0	1	0	0	0	0	1
1	1	1	0	0	0	0	1	1
1	1	1	1	0	0	1	1	1

## **K-Maps and Functions:**

A 0 (t+1)						
A1A0\I1I0	00	01	11	10		
00	0	1	0	0		
01	1	0	0	1		
11	1	0	0	0		
10	0	1	0	0		

A0 (t+1) = A0'.11'.10 + A1'.A0.10' + A0.10'.11'

A 1 (t+1)						
A1A0\I1I0	00	01	11	10		
00	0	0	0	1		
01	0	1	0	1		
11	1	0	0	0		
10	1	1	0	0		

A1 (t+1) = A1'.10'.11 + A0'.A1.11' + A1.10'.11' + A1'.A0.11'.10

Change 1						
A1A0\I1I0	00	01	11	10		
00	0	0	0	0		
01	0	0	0	0		
11	0	0	1	0		
10	0	0	1	0		

C1 = A1.I0.I1

Change 0						
A1A0\I1I0	00	01	11	10		
00	0	0	0	0		
01	0	0	1	0		
11	0	0	1	1		
10	0	0	0	0		

C0 = A0.10.11 + A1.A0.11

Output						
A1A0\I1I0	00	01	11	10		
00	0	0	1	0		
01	0	0	1	0		
11	0	1	1	1		
10	0	0	1	1		

Output = 10.11 + A1.11 + A1.A0.10

# **Screenshot of Schematic Design:**

