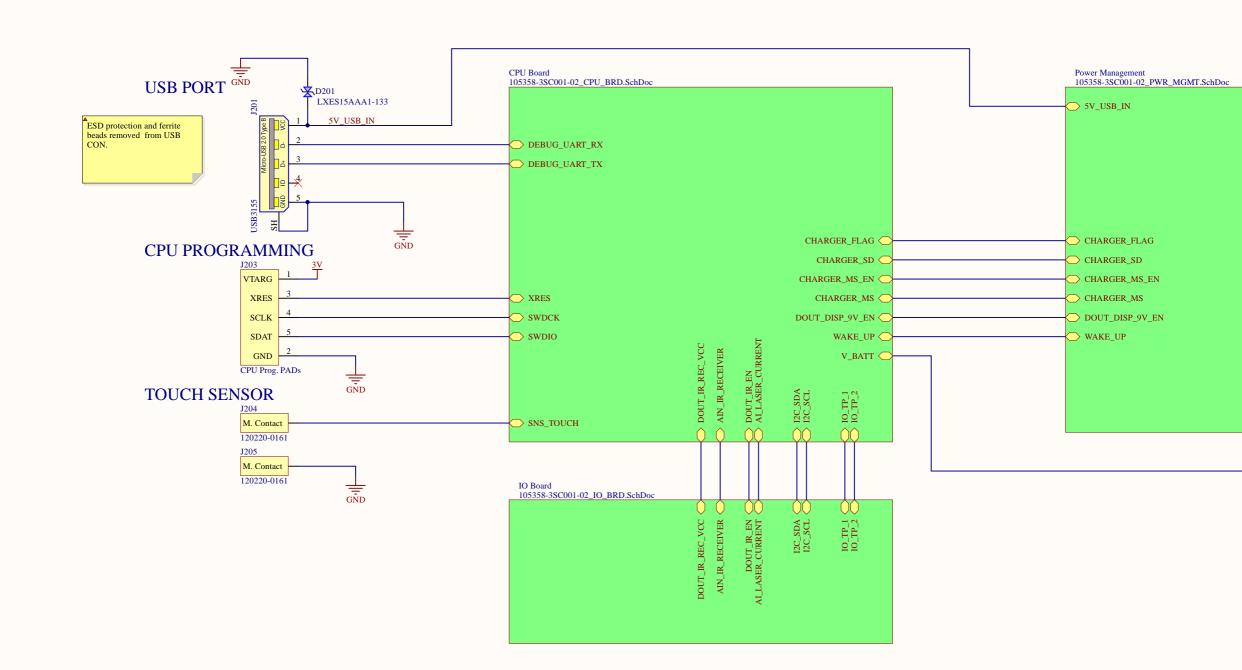
# Electronics Functional Model 1.5 - Mini Schematic version 105358-3SC001-02-A

Block diagram 105358-3SC001-02\_BlockDiagram.SchDoc

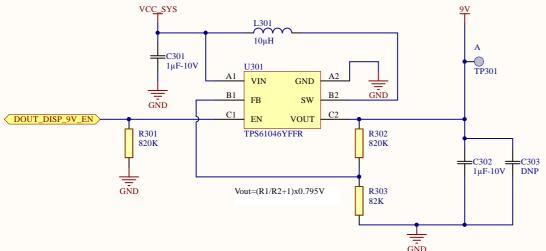
- 2 Block Diagram
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### Block Diagram



### Power Management

### BOOST CONVERTER



## BUCK CONVERTER VCC. SYS U301 VIN SW A1 C302 $4.7\mu F$ C1 VSEL1 GND B2

VSEL3

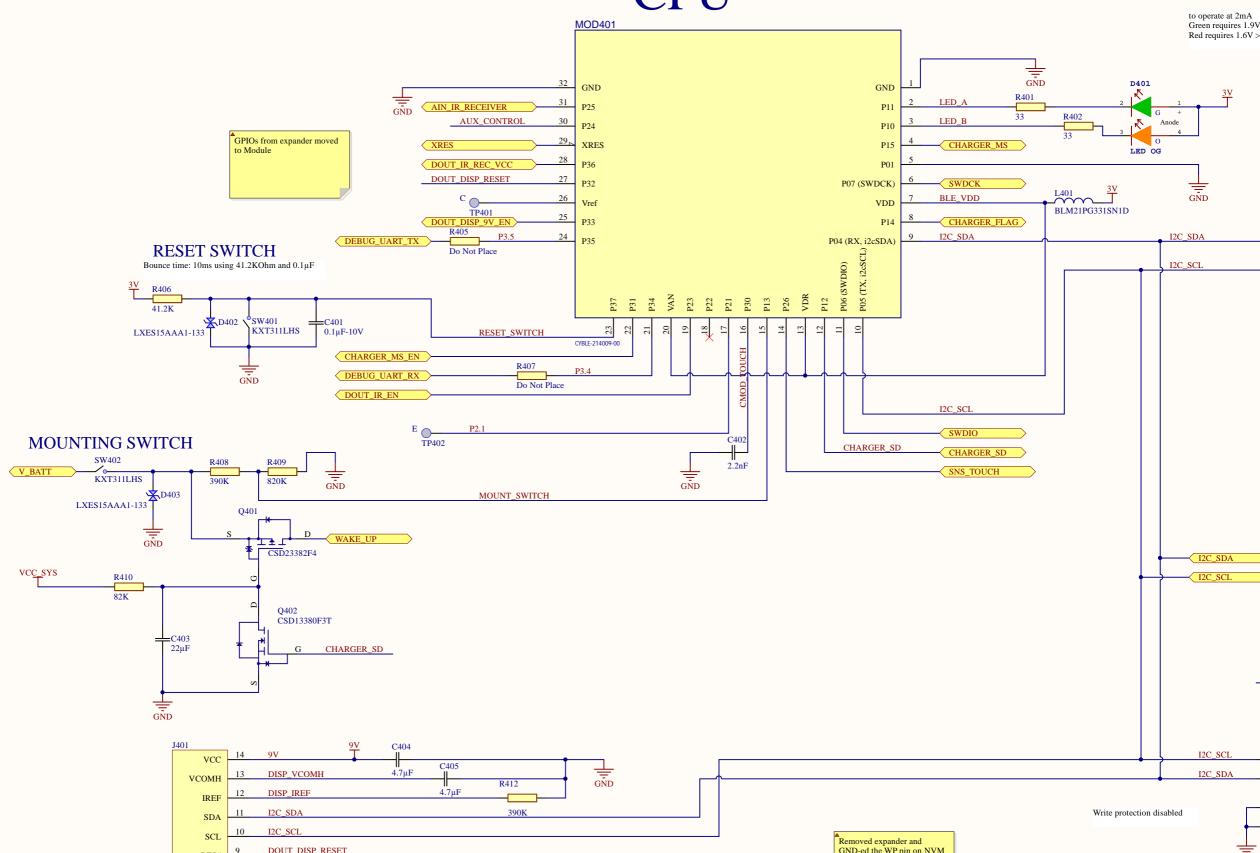
VSEL2

TPS627431YFPR

Note: Removed from Layout Components made Graphical

#### **BATTERY CHARGER** =C304 10μF-16V GND 1μF-10V LDO F5 VCC\_SYS nCHG D5 <u></u> —C307 CEN is internally pulled up, so can be CEN 1μF-10V left not connected × F3 BATMS\_EN × E4 SW1\_OA SW1\_OB R305 820K R304 SW\_SEL1 82K × E2 SW2\_I SW\_SEL2 SW2\_OA BATMS SW2\_OB 200K 150K 41.2K provides about 5mA 5.6K BATSNS 5.6K provide about 35.6mA IPRE BATSNSFV 41.2K SW SEL should be connected to GND C3 D3 if not used (Datasheet page 22) ₩ GND B5 \_C308 4.7μF D2 WAKE-UP STBC03

**CPU** 



### IR Barrier

#### LIGHT BARRIER

