

REVISIONS						
REV	ECO#	DESCRIPTION	DATE	DRAWN BY	APPROVED BY	THE INFORMATION CONT
Α	N/A	First release	2018-12-14	Nancy Bujold	Cedric Godin	EXCEPT AS SPECIFICAL

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NOTES:

1. UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES.
TOLERANCES: 1 DECIMAL PLACE: +/-0.05
2 DECIMAL PLACES: +/-0.01
3 DECIMAL PLACES: +/-0.005
ANGLE: +/- 1 DEG.

2. HOLE SIZES SHOWN ARE FINISHED HOLE SIZES. UNITS: MILS.

3. MANUFACTURE PCB USING THE FOLLOWING FILES: KR13933PCB_revA.GTL — TOP LAYER

KR13933PCB_revA.GTL — TOP LAYER

KR13933PCB_revA.G1 — LAYER 2

KR13933PCB_revA.G2 — LAYER 3

KR13933PCB_revA.GBL — BOTTOM LAYER

KR13933PCB_revA.GTS — SOLDER MASK TOP

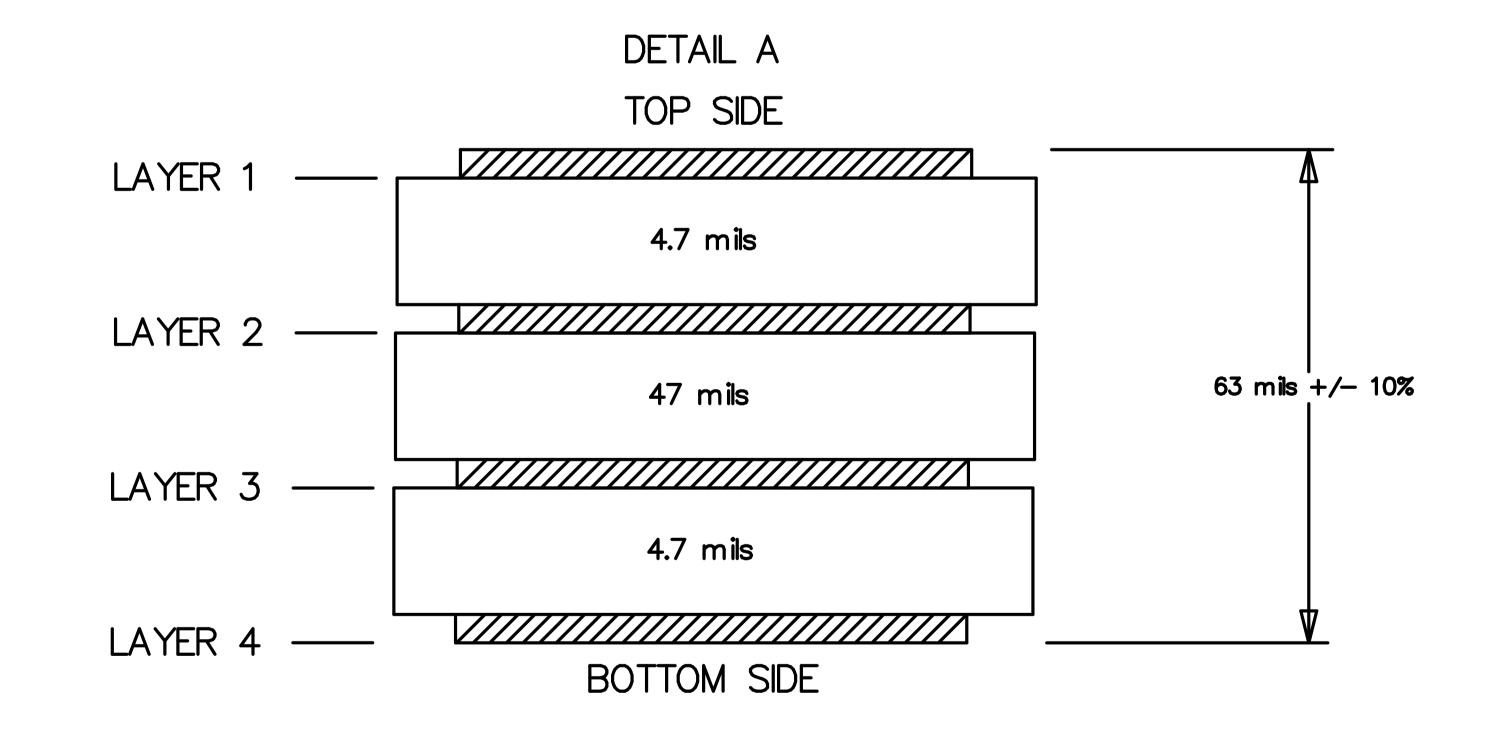
KR13933PCB_revA.GBS — SOLDER MASK BOTTOM

KR13933PCB_revA.GTO — SILKSCREEN TOP

KR13933PCB_revA.GTP — PASTE MASK TOP

KR13933PCB_revA.DRL -NC DRILL

4. IMPEDANCE: — DIFFERENTIAL PAIR: 100 OHM +/-10%
TRACE'S WIDTH OF 6 MILS AND SPACING OF 8 MILS ON TOP LAYER



COPPER THICKNESS TABLE		
LAYERS	THICKNESS (oz)	
1	1 (FINISHED)	
2	0.5	
3	0.5	
4	1 (FINISHED)	

*NOT TO BE VERIFIED AT INCOMING INSPECTION

Symbol	Count	Hole Size	Hole Length	Plated	Hole Type	Via/Pad	Hole Tolerance
¢	51	13.00mil (0.330mm)	_	PTH	Round	Via	+/-3.00mil
×	8	38.00mil (0.965mm)	_	PTH	Round	Pad	+/-3.00mil
0	18	43.00mil (1.092mm)	_	PTH	Round	Pad	+/-3.00mil
∇	2	63.00mil (1.600mm)	_	PTH	Round	Pad	+/-3.00mil
	3	86.61mil (2.200mm)	_	PTH	Round	Pad	+/-3.00mil
\$	2	128.00mil (3.251mm)	_	NPTH	Round	Pad	+/-3.00mil
	84 Total						

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PROJECT: Gen3 L	53K End Effector Re	eference Design				
KR1393	33PCB	REV. A	OR IN PART, AND THE INFORMATION CONTAINED THEREIN CANNOT BE USED OR DIVU WITHOUT THE EXPRESSED WRITTEN CONSENT OF KINOVA.			
DRAWING:	DRAWING: Drill Drawing					
	TOP SIDE VIEW		(C) KINOVA			
DRAWN BY: Nanc	ey Buiold					
VERIFIED BY: Cedr		Project SVN Version	2009-2018			
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