University of Engineering & Technology,

Taxila

Electrical Engineering Department



PROBLEM BASED LEARNING (PBL)

Title:

Antenna Azimuth Position Control System

\mathbf{BY}

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TO

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> Introduction:

An antenna azimuth position control system consists of two potentiometers, a preamplifier, a power amplifier, a motor and a load. The position (direction) of the antenna can be changed via one potentiometer (input transducer). The second potentiometer (output transducer) is used to obtain the actual azimuth position of the antenna. The input potentiometer converts the desired angular position to a voltage, while the differential amplifier provides the error signal between the desired and real antenna position signals. A power amplifier is used to amplify the control signal applied to the motor. The antenna position control is usually achieved by the help of a set of gears.

This project is Problem Based Learning (PBL). We are going to design a compensator to meet our requirements. In this we address the position control of antenna azimuth using lead compensators, lag compensator and proportional, integral and Derivative (PID) controller. The fractional order lead compensator is proposed for enhancing the closed loop performance of azimuth position control of antenna system. From the comparison of the closed loop responses, the proposed lead compensator delivers a superior closed loop performance when compared with PI controller and lead compensator. Here we required best azimuth positioning control with a little settling time, some value of overshoot and no steady-state error is detected. Frequency ranges were utilized for the PID controller in the system, while Ziegler-Nichols was used to tune the PID parameter gains. MATLAB Simulink platform was used for the system simulation The antenna azimuth position control system turns the input command in output position. This system is widely used in antennas, robots and computers disks. Here we present the systems that are managed with azimuth antenna. We're going to show how the system works and how its performance can be improved. The purpose of this system is the input angle to be turned into an output angle of the antenna. The potentiometer converts the angular rotating in voltage. Similar to that, on the output angle rotations are turning into voltage of potentiometer, which is connected to the feedback. Signal and gain are increasing the difference between the input and output voltage. Rest of the system description given below in problem statement section. Here the functional block diagram shown below:

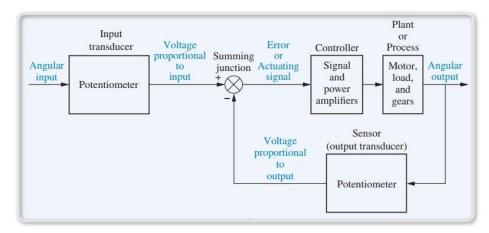
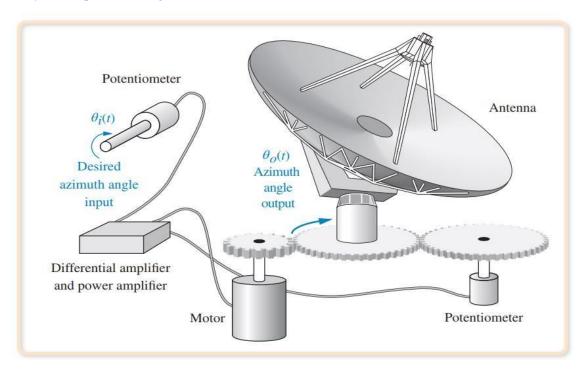


Figure 1 - Layout of the position control system of antenna azimuth

> System Description:

An antenna azimuth position control system consists of two potentiometers, a preamplifier, a power amplifier, a motor and a load. The position (direction) of the antenna can be changed via one potentiometer (input transducer). The second potentiometer (output transducer) is used to obtain the actual azimuth position of the antenna. The input potentiometer converts the desired angular position to a voltage, while the differential amplifier provides the error signal between the desired and real antenna position signals. A power amplifier is used to amplify the control signal applied to the motor. The antenna position control is usually achieved by the help of a set of gears.



> Project Objective:

The purpose of this report is to describe the management system of the antenna position in the frequency range (with transfer functions) and in the state space representation (state time equations), also known as time—domain approach. The results of analytical calculations and programming package MATLAB/Simulink will give the clear explanation of the response of the system. It will emphasize the importance of the results and indicate the possibility of further theoretical and experimental work on the same problem as well as the contributions in this field.

Revised Concepts of Control Systems:

- Open/Close loop system
- Stability and Instability
- Pole-zero analysis
- Root locus plot
- Bode plots
- Gain & Phase Margin
- Lead & Lag Compensators
- PID Controller
- Step & Ramp Response
- Time domain performance
- Rise time, Settling time, Overshoot, Steady state error

> MATLAB Tools:

- Basic commands
- Simulink
- SISO tool

Finally, start to implementation of mathematically modelling in MATLAB, analyzed, made assumptions (hit and trial), and got the required OS%, Ts, Tr, ess, PM and GM. Analysis and designed controller using MATLAB codes, Simulink blocks and SISO tool.

System description, mathematically modelling, Simulations, analysis in different domains, Results & References are given in respective sections

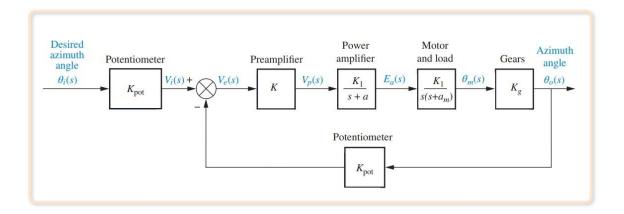


Figure 2: The closed-loop system scheme

Parameter	Kpot	K_1	K_g	K_m	а	a_m
Value	0.318	100	0.2	2	50 + Reg #	2.5

In a group of students, Reg # of smallest Roll number will be taken as the value of "a". For example, group 1 of A1 group consists of three students having Reg # of 09, 49 & 89.

Value of parameter "
$$a=21 +50 = 71$$
"

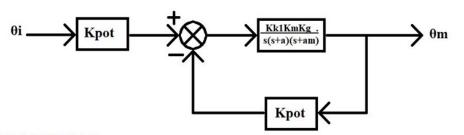
> Methodology

- 1. First we derive the open loop and close loop transfer function.
- 2. Then we obtain open loop root locus, pole zero map and bode plot.
- 3. Then we obtain close loop step response to obtain the time domain parameters like rise time , settling time , peak time , overshoot and steady state etc.
- 4. Then according to given requirements we design lead compensator.
- 5. Also, we design lag compensator.
- 6. Then we connect lead lag compensator in series with system to see the waveform.
- 7. Then we replace lead-lag compensator with PID controller to see the waveform.
- 8. Then we analyze the time domain parameters of compensated waveforms.

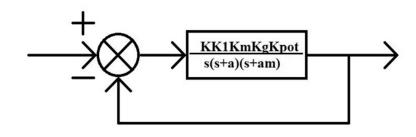
> System Description

Open and Close Loop Transfer Function:

Handwritten:



THEN



OPEN LOOP:

$$O.L = \frac{KK_1K_mK_gK_{pot}}{s(s+a)(s+a_m)}$$

$$S(S^2 + 73.5S + 177.5)$$

$$5^3 + 73.55^2 + 177.55$$

CLOSE LOOP:

$$\frac{\theta_o(s)}{\theta_m(s)} = C.L = \frac{\frac{KK_1K_mK_gK_{pot}}{s(s+a)(s+a_m)}}{1 + \frac{KK_1K_mK_gK_{pot}}{s(s+a)(s+a_m)}}$$

$$C.L = \frac{KK_1K_mK_gK_{pot}}{s(s+a)(s+a_m) + KK_1K_mK_gK_{pot}}$$

 $\frac{12.72 \, \text{K}}{+ 73.5 \, \text{S}^2 + 177.5 \, \text{S} + 12.72 \, \text{K}}$

Code:

Kpot=0.318

K1 = 100

Kg=0.2

Km=2

num=[Kpot*K1*Km*Kg] % K=1

den=[1 73.5 177.5 0]

OpenLoop=tf(num,den) %OpenLoop=GH

Forward=K1*Kg*Km

Gp=tf(Forward,den)

Gp1=feedback(Gp,Kpot)

CloseLoop=Kpot*Gp1 %CloseLoop=Kpot(forward/1+GH)

OpenLoop =

12.72

 $s^3 + 73.5 s^2 + 177.5 s$

Continuous-time transfer function.

CloseLoop =

12.72

 $s^3 + 73.5 s^2 + 177.5 s + 12.72$

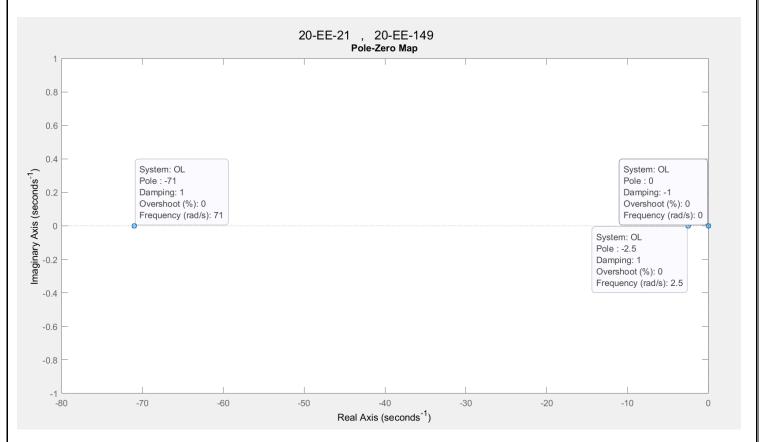
Continuous-time transfer function.

> Uncompensated System Analysis in Time and Frequency Domain

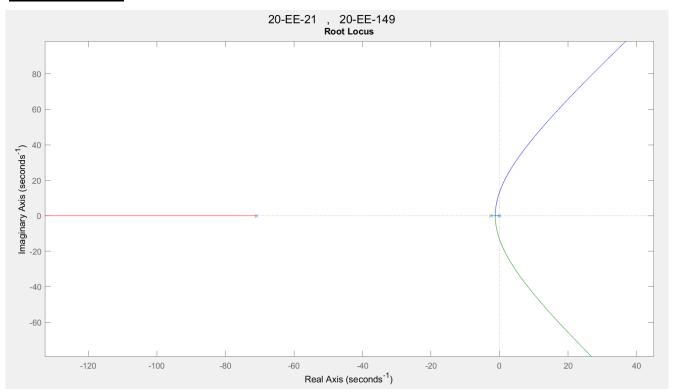
Code:

```
%20-EE-21
%20-EE-149
close all
num = [12.72]
den=[1 73.5 177.5 0]
num1 = [12.72 * 23.8]
OL=tf(num, den)
OL1=tf(num1, den)
figure
pzmap(OL)
suptitle('20-EE-21
                           20-EE-149')
figure(2)
rlocus (OL)
suptitle('20-EE-21
                            20-EE-149')
figure(3)
margin (OL1)
```

Open Loop Pole-Zero Analysis:

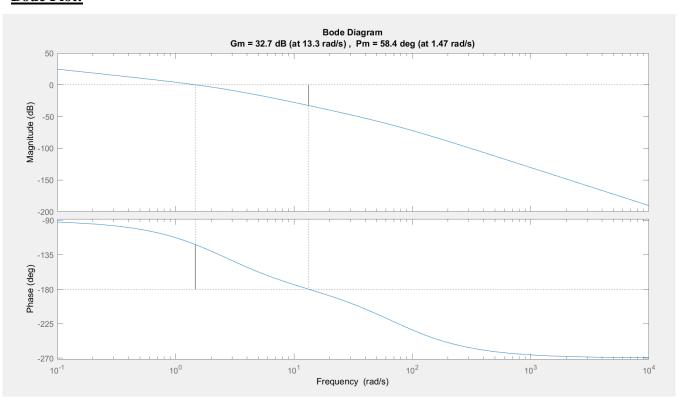


Root Locus Plot:



Note: In root locus plot at 10% overshoot, we have found k=23.8

Bode Plot:

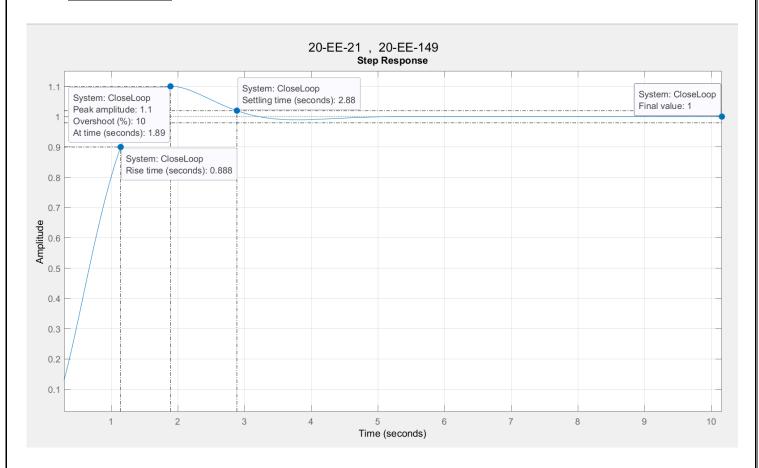


Closed-Loop Step Response:

Code:

%20-EE-21 %20-EE-149 K=23.83 num=[302.736] den=[1 73.5 177.5 302.736] CloseLoop=tf(num,den) figure step(CloseLoop) suptitle('20-EE-21 , 20-EE-149')

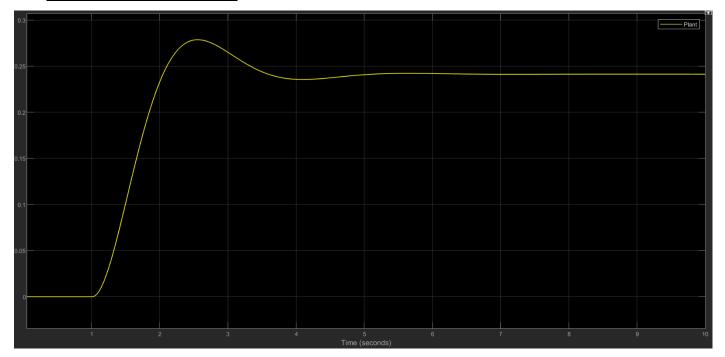
Step Response:



Circuit on Simulink:



Step Response on Simulink:



Comment:

- **System is Stable.**
- ***** Type of Response is Underdamped.

Response	PM & GM	Rise time	Settling time	Overshoot	ess
Uncompensated	GM = 32.7 dB	0.888 s	2.88 s	10 %	0 %
	PM = 58.4 °				

> Compensators Design and Evaluation

Design Requirements:

- ➤ Overshoot of less than 10%
- \triangleright Rise time is half of the uncompensated system or equal to (Tr<=0.5Tr) i.e. Tr<=0.4495 s
- > Settling time should be less than or equal to 2 sec. (Ts<= 2sec.)
- ➤ Phase margin between 45 and 65 degree, and Gain margin of at least 6 dB.
- > There is tenfold improvement in steady state error.

Lead-Lag Compensator Design:

Lead Compensator	,
Tr 6 0.4498	
-/0 O.S = 100/0	
E = 0.5912	
From Ts = 4.	
E Wn	
1.9 = 4	
(0.5912)(Wn)	
": Wn = 4 = 3.56 rad/s	
1-123	
POLES - S1,2= - & Wn I J1- & JWI	
$= -(0.59)(3.56) \pm \sqrt{1 - (0.59)^2} j.b$	yn
z-2.1 ± 2.87j	

$$=> 01 = ?$$

01 : 145.128°

$$=) 02 = ?$$

$$\Rightarrow \theta_3 = ?$$

$$03 = \tan^{-1}\left(\frac{2.87}{68.9}\right)$$

Now,

$$0z - (01 + 02 + 03 + \cdots) = -180$$

$$01 + 03 + 04 = 180^{\circ}$$

 $L_1 = 3.556$

L2 = 2.87

L3 = 68.97

Ly = 5.34

" Kc overall = TLp = LIL3L4

TL2

Kc = 1310.703

→ Kc = 1310.73 = 4,33

302-736

⇒ G(Lead) = 4.33(s+2.5)

S+ 6.61

Log - Compensator:

$$\beta = K_{v-reg}$$
 K_{v-rent}

$$\beta = \frac{1/0.1_{ess-plant}}{1/ess-plant}$$

So,
$$z = 10$$

So,

PID Compensator Design:

PID:-	
As ess=0, let ki=0	
And,	
pm_req = 100 &	
$ \oint m - req = 59.12^{\circ} $	
Let ϕ_m - reg = 60°	
So,	
$W_1 = 8$	
Ts (ton 60)	
As condition says Ts = 2,	
$\rightarrow using T_s = 1.5s$	
$W_1 - 8 = 8$	
1.5(1.73) 2.6	
W1 = 3.1 rad	
Ind:-	
I(GH) = 302.736	-
$\frac{ (GH)_{w=3\cdot 1} = 302\cdot 736}{3\cdot 1j(2\cdot 5 + 3\cdot 1j)(71 + 3\cdot 1j)}$	
= 302.736	
3.1~90 (4/51.11)(71.862)	25)
= 0.347 L-143.61°	

$$\mathcal{O} = -180 + \phi m - (arg G(jw_1)(H(jw_2))$$

Now

16H1

0.342

Ko = SINO Wa (IGHI)

$$K_0 = \frac{\sin(2361)}{(31)(0.342)}$$

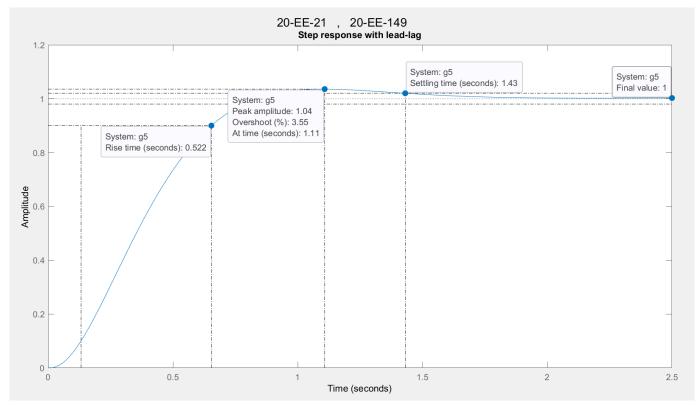
Code:

```
close all
t=[-10:0.01:50]
unitstep=zeros(size(t))
unitstep(t \ge 0)=10
num=[952]
den=[1 73.5 177.5 0]
Gp=tf(num,den)
lead=tf(4.33*[1 2.5],[1 6.61])
lag=tf([1 0.01],[1 0.001])
pid=tf([0.37 2.6],[1])
g0=Gp*pid
g1=feedback(g0,0.318)
g2=g1*0.318
                    %close loop with PID
g3=Gp*lead*lag
g4=feedback(g3,0.318)
g5=0.318*g4
                    %close loop with lead-lag
figure
step(g2) %Step response with PID
title('Step response with PID')
suptitle('20-EE-21 , 20-EE-149')
figure
step(g5)
              %Step response with lead-lag
title('Step response with lead-lag')
suptitle('20-EE-21 , 20-EE-149')
figure
margin(g0*0.318)
                     %Bode plot with PID
figure
margin(g3*0.318)
                   %Bode plot with lead-lag
figure
lsim(unitstep,t,g2) % step response with 10 unit step(PID)
title('Step response with PID with 10u(t)')
suptitle('20-EE-21, 20-EE-149')
figure
lsim(unitstep,t,g5) %step response with 10 unit step(lead-lag)
title('Step response with lead lag with 10u(t)')
suptitle('20-EE-21 , 20-EE-149')
```

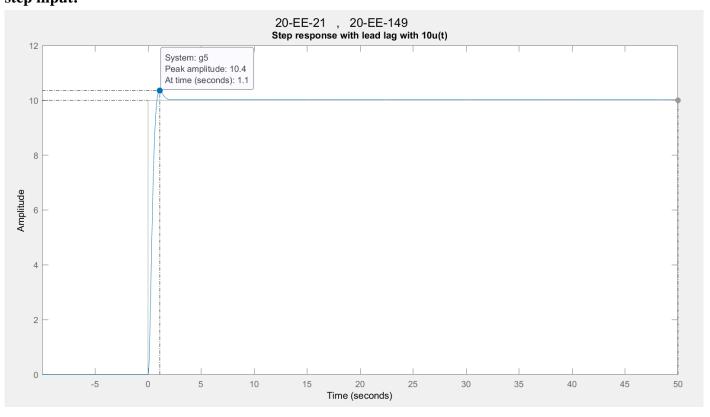
Waveform:

Lead-Lag Compensator:

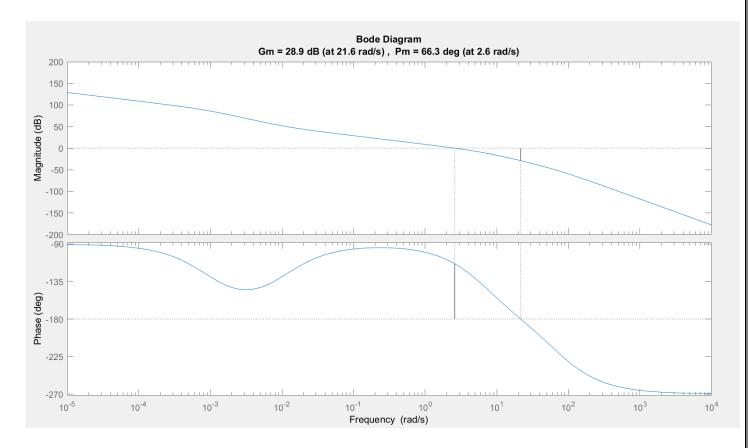
Step input:



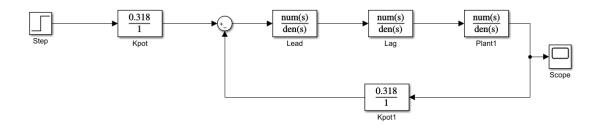
10 step input:



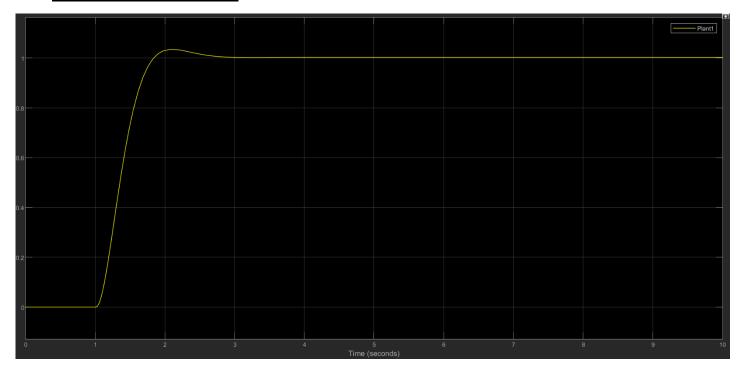
Bode Plot Compensated:



Circuit on Simulink:

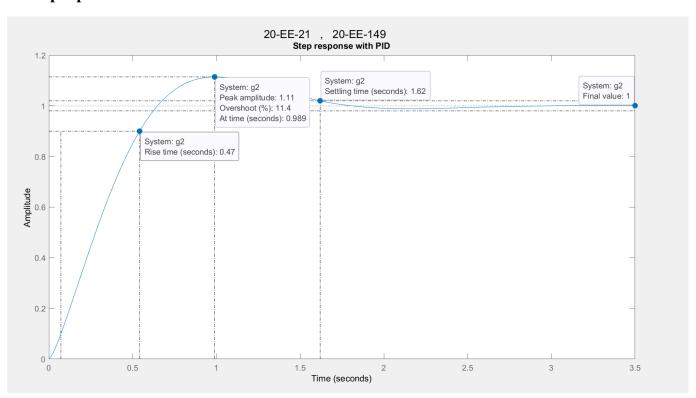


Step response on Simulink:

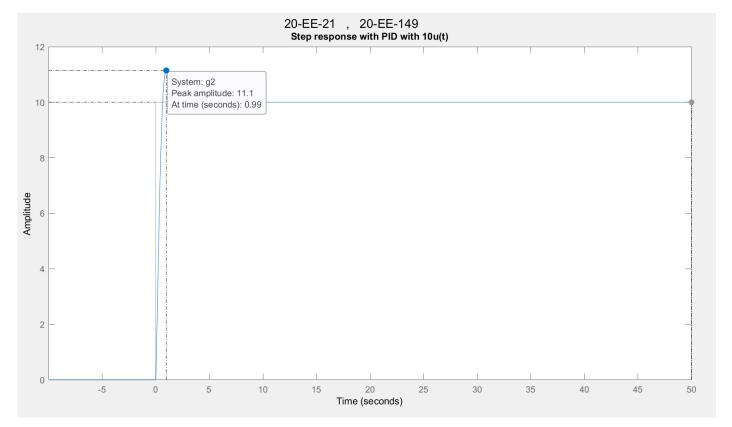


PID Controller:

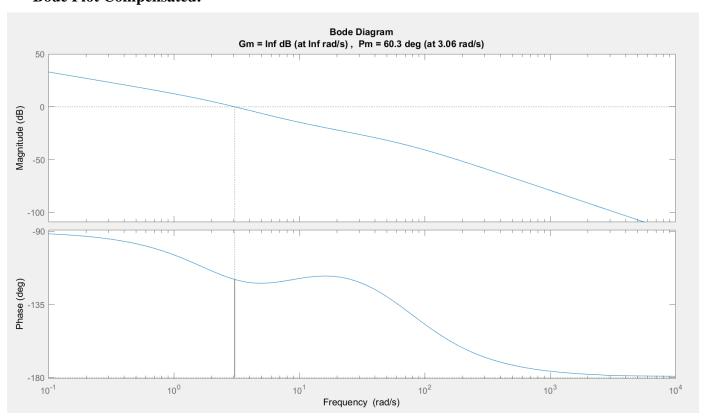
Step input:



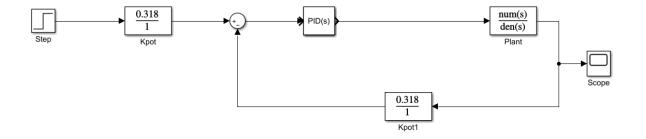
10 step input:



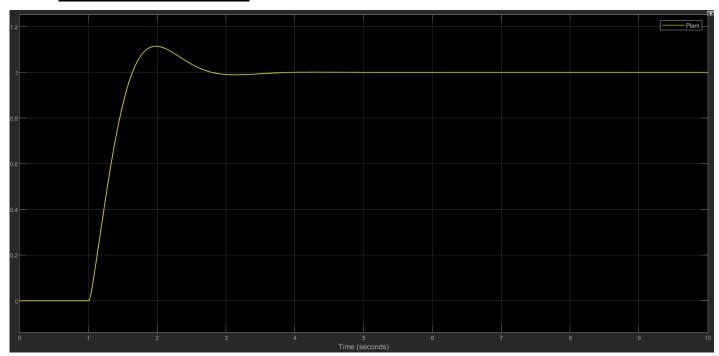
Bode Plot Compensated:



Circuit on Simulink:



Step Response on Simulink:



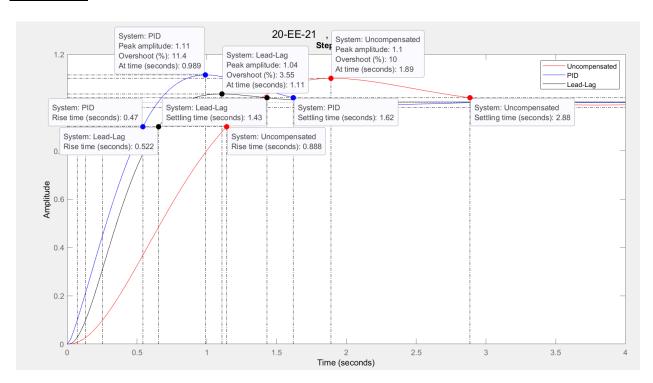
Controller	PM & GM	Rise time	Settling time	Overshoot	ess
Compensator 1	GM = 28.9 dB	0.52 s	1.43 s	3.55 %	0 %
(Lead-Lag)	PM = 66.3°				
Compensator 2	GM = infinite	0.47 s	1.62 s	11.4 %	0 %
(PID)	PM = 60.3 °				

> Results and Comparative Analysis

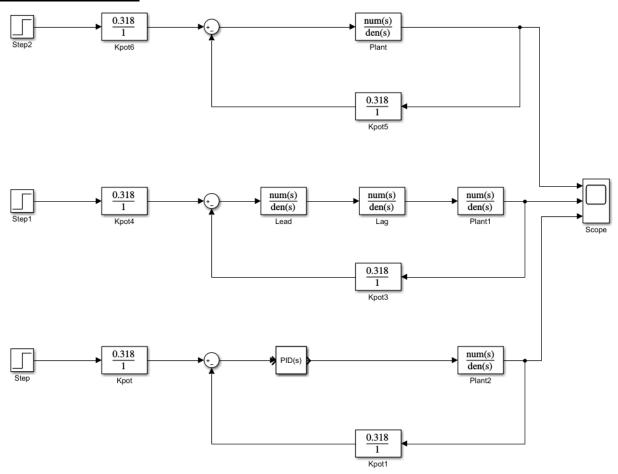
Code:

```
close all
num = [952]
den=[1 73.5 177.5 0]
Gp=tf(num,den)
Gclose=tf([302.73],[1 73.5 177.5 302.73])
Lead=tf(4.33*[1 2.5],[1 6.61])
Lag=tf([1 0.01],[1 0.001])
PID=tf([0.37 2.6],[1])
g0=Gp*PID
g1=feedback(g0,0.318)
                    %close loop with PID figure
g2=g1*0.318
g3=Gp*Lead*Lag
g4=feedback(g3,0.318)
g5=0.318*g4 %close loop with lead-lag figure
step(Gclose,'red') %Uncompensated step response
hold on
step(g2,'blue') % Step response with PID
hold on
step(g5,'black')
                  %Step response with lead-lag
legend('Uncompensated','PID','Lead-Lag')
suptitle('20-EE-21 , 20-EE-149')
```

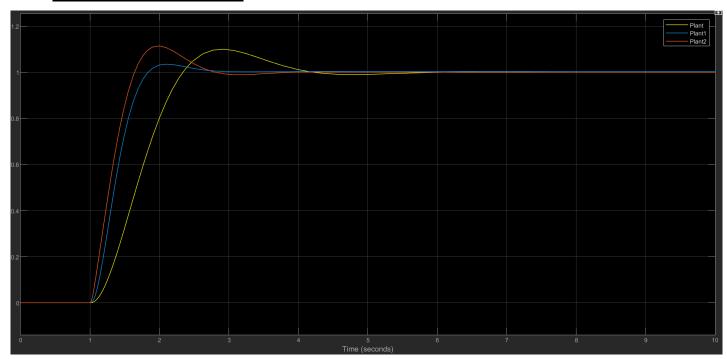
Waveform:



Circuit on Simulink:



Step Response on Simulink:



Controller	PM & GM	Rise time	Settling time	Overshoot	ess
Uncompensated	GM = 32.7 dB	0.888 s	2.88 s	10 %	0 %
	PM = 58.4 °				
Compensator 1	GM = 28.9 dB	0.52 s	1.43 s	3.55 %	0 %
(Lead-Lag)	PM = 66.3 °				
Compensator 2	GM = infinite	0.47 s	1.62 s	11.4 %	0 %
(PID)	PM = 60.3 °				

Hence,

For PID:

- Overshoot is almost 10%.
- Settling time is less than 2 sec.
- Phase margin between 45° and 65° degree (60.3°), and Gain margin of infinite.
- Rise time is less than uncompensated system.
- There is tenfold improvement in the steady state error.

For Lead-Lag:

- Overshoot is less than 10%
- Settling time is less than 2 sec.
- Phase margin between 45° and 65° degree (66.3°) and Gain margin is 28.9dB.
- Rise time is less than uncompensated system.
- There is tenfold improvement in the steady state error.

> Comparative Analysis

Comparison between uncompensated and compensated system can be observed clearly, without compensator phase margin was not lie in our required range but after adding the appropriate compensator in the system both phase margin and gain margin fulfils our requirement, we was talking about Bode plot which was analysis in frequency domain. Now come towards time domain, the difference between uncompensated and compensated system rise time, settling time and overshoot was 0.888s, 2.88s and 10 % respectively. In compensated system rise time, settling time and overshoot is 0.52s, 1.43s and 3.55% respectively and most important fulfils our requirement. The last thing for comparison is steady state error (ess). In uncompensated system the value of steady state error is 0. In compensated system the error value is again 0. Uncompensated system is not stable in other hand compensated system is stable and efficient. Compensator shifts the root locus to the left, which enhances the responsiveness and stability of the system. For the Compensated system we designed two types of compensators one was phase lead compensator which stated above but PID has their own space and own functionality. In this technique we can make controller of different combination like PI, PD and PID etc. PID has different techniques for its tuning.

Conclusion

When we first analyze the system without compensator and observed rise time, which was showing very poor performance

of system for enhancing the system performance improved rise time, settling time, overshoot and improved phase and gain margin using phase lead compensator and PID Controller, then we design required lead lag compensator and then see the compensated step response and after that we design PID controller and replace the lead lag compensator with that PID and again see the step response and observe that the lead lag compensated response and PID compensated response is almost same with small change in parameters but in our required parameters domain. In this project, I developed the phase lead compensator for enhancing the closed loop performance of azimuth position control of antenna system using MATLAB/Simulink/SISO-tool. We compared the responses in terms of time domain specifications for unit step input using lead compensator controller as well as PID controller. I learned a lot from this project, now I can design any compensator with more improvements. Stability of this compensator can be more improve if we consume more time to this project.

> References

- Google
- Lab slides
- Lab manuals
- Theory Lectures

> Appendix

Different Websites