



# OpenCore

Reference Manual (0.6.~~2~~.3)

[2020.10.17]

# 1 Introduction

This document provides information on OpenCore user configuration file format used to set up the correct functioning of the macOS operating system. It is to be read as the official clarification of expected OpenCore behaviour. All deviations, if found in published OpenCore releases, shall be considered to be documentation or implementation bugs which should be reported via the Acidanthera Bugtracker. An errata sheet is available in OpenCorePkg repository.

This document is structured as a specification and is not meant to provide a step-by-step guide to configuring an end-user Board Support Package (BSP). The intended audience of the document is anticipated to be programmers and engineers with a basic understanding of macOS internals and UEFI functionality. For these reasons, this document is available exclusively in English, and all other sources or translations of this document are unofficial and may contain errors.

Third-party articles, utilities, books, and [alikesimilar](#), may be more useful for a wider audience as they could provide guide-like material. However, they are subject to their authors' preferences, tastes, misinterpretations of this document, and unavoidable obsolescence. In cases of using such sources, such as Dortania's OpenCore Install Guide and related material, please refer back to this document on every decision made and re-evaluate potential consequences.

Please note that regardless of the sources used, users are required to fully understand every OpenCore configuration option, and the principles behind them, before posting issues to the Acidanthera Bugtracker.

*Note: Creating this document would not have been possible without the invaluable contributions from other people: Andrey1970, Goldfish64, dakanji, PMheart, and several others, with the full list available in OpenCorePkg history.*

## 1.1 Generic Terms

- **plist** — Subset of ASCII Property List format written in XML, also known as XML plist format version 1. Uniform Type Identifier (UTI): `com.apple.property-list`. Plists consist of **plist objects**, which are combined to form a hierarchical structure. Due to plist format not being well-defined, all the definitions of this document may only be applied after plist is considered valid by running `plutil -lint`. External references: <https://www.apple.com/DTDs/PropertyList-1.0.dtd>, `man plutil`.
- **plist type** — plist collections (**plist array**, **plist dictionary**, **plist key**) and primitives (**plist string**, **plist data**, **plist date**, **plist boolean**, **plist integer**, **plist real**).
- **plist object** — definite realisation of **plist type**, which may be interpreted as value.
- **plist array** — array-like collection, conforms to `array`. Consists of zero or more **plist objects**.
- **plist dictionary** — map-like (associative array) collection, conforms to `dict`. Consists of zero or more **plist keys**.
- **plist key** — contains one **plist object** going by the name of **plist key**, conforms to `key`. Consists of printable 7-bit ASCII characters.
- **plist string** — printable 7-bit ASCII string, conforms to `string`.
- **plist data** — base64-encoded blob, conforms to `data`.
- **plist date** — ISO-8601 date, conforms to `date`, unsupported.
- **plist boolean** — logical state object, which is either true (1) or false (0), conforms to `true` and `false`.
- **plist integer** — possibly signed integer number in base 10, conforms to `integer`. Fits in 64-bit unsigned integer in two's complement representation, unless a smaller signed or unsigned integral type is explicitly mentioned in specific **plist object** description.
- **plist real** — floating point number, conforms to `real`, unsupported.
- **plist metadata** — value cast to data by the implementation. Permits passing **plist string**, in which case the result is represented by a null-terminated sequence of bytes (aka C string), **plist integer**, in which case the result is represented by 32-bit little endian sequence of bytes in two's complement representation, **plist boolean**, in which case the value is one byte: 01 for `true` and 00 for `false`, and **plist data** itself. All other types or larger integers invoke undefined behaviour.

loaded by the firmware by default according to UEFI specification, and `Bootstrap.efi` can be registered as a custom option to let OpenCore coexist with operating systems using `BOOTx64.efi` as their own loaders (e.g. Windows), see `BootProtect` for more details.

- **boot**  
Duet bootstrap loader, which initialises UEFI environment on legacy BIOS ~~firmwares~~firmware and loads `OpenCore.efi` similarly to other bootstrap loaders. Modern Duet bootstrap loader will default to `OpenCore.efi` on the same partition when present.
- **ACPI**  
Directory used for storing supplemental ACPI information for `ACPI` section.
- **Drivers**  
Directory used for storing supplemental UEFI drivers for `UEFI` section.
- **Kexts**  
Directory used for storing supplemental kernel information for `Kernel` section.
- **Resources**  
Directory used for storing media resources, such as audio files for screen reader support. See `UEFI Audio Properties` section for more details. This directory also contains image files for graphical user interface. See `OpenCanopy` section for more details.
- **Tools**  
Directory used for storing supplemental tools.
- **OpenCore.efi**  
Main booter driver responsible for operating system loading.
- **config.plist**  
OC Config.
- **vault.plist**  
Hashes for all files potentially loadable by OC Config.
- **vault.sig**  
Signature for `vault.plist`.
- **SysReport**  
Directory containing system reports generated by `SysReport` option.
- **nvram.plist**  
OpenCore variable import file.
- **opencore-YYYY-MM-DD-HHMMSS.txt**  
OpenCore log file.
- **panic-YYYY-MM-DD-HHMMSS.txt**  
Kernel panic log file.

*Note:* It is not guaranteed that paths longer than `OC_STORAGE_SAFE_PATH_MAX` (128 characters including 0-terminator) will be accessible within OpenCore.

## 3.2 Installation and Upgrade

To install OpenCore reflect the Configuration Structure described in the previous section on a EFI volume of a GPT partition. While corresponding sections of this document do provide some information ~~in regards to external resources~~like regarding external resources such as ACPI tables, UEFI drivers, or kernel extensions (kexts), completeness of the matter is out of the scope of this document. Information about kernel extensions may be found in a separate Kext List document available in OpenCore repository. Vaulting information is provided in Security Properties section of this document.

OC config, just like any property lists can be edited with any stock textual editor (e.g. nano, vim), but specialised software may provide better experience. On macOS the preferred GUI application is Xcode. For a lightweight cross-platform and open-source alternative ProperTree editor can be utilised.

For BIOS booting a third-party UEFI environment provider will have to be used. `OpenDuetPkg` is one of the known UEFI environment providers for legacy systems. To run OpenCore on such a legacy system, `OpenDuetPkg` can be installed with a dedicated tool — `BootInstall` (bundled with OpenCore). Third-party utilities can be used to perform this on systems other than macOS.

For upgrade purposes refer to `Differences.pdf` document, providing the information about the changes affecting the configuration compared to the previous release, and `Changelog.md` document, containing the list of modifications across all published updates.

**Warning:** Tool developers modifying `config.plist` or any other OpenCore files must ensure that their tool checks for `opencore-version` NVRAM variable (see Debug Properties section below) and warn the user if the version listed is unsupported or prerelease. OpenCore configuration may change across the releases and the tool shall ensure that it carefully follows this document. Failure to do so may result in this tool to be considered as malware and blocked with all possible means.

### 3.4 Coding conventions

~~Just like As with~~ any other project, we have conventions that we follow during ~~the~~ development. All third-party contributors are ~~highly recommended to read and follow~~ advised to adhere to the conventions listed below before submitting ~~their patches. In general it is also recommended to firstly discuss the issue in patches. To minimise abortive work and the potential rejection of submissions, third-party contributors should initially raise issues to the~~ Acidanthera Bugtracker ~~before sending the patch to ensure no double work and to avoid the patch being rejected for~~ feedback before submitting patches.

**Organisation.** The codebase is contained in the `OpenCorePkg` repository, which is the primary EDK II package.

- Whenever changes are required in multiple repositories, separate pull requests should be sent to each.
- Committing the changes should happen firstly to dependent repositories, secondly to primary repositories to avoid automatic build errors.
- Each unique commit should compile with `XCORE5` and preferably with other toolchains. In the majority of the cases it can be checked by accessing the CI interface. Ensuring that static analysis finds no warnings is preferred.
- External pull requests and tagged commits must be validated. That said, commits in master may build but may not necessarily work.
- Internal branches should be named as follows: `author-name-date`, e.g. `vit9696-ballooning-20191026`.
- Commit messages should be prefixed with the primary module (e.g. library or code module) the changes were made in. For example, `OcGuardLib: Add OC_ALIGNED macro`. For non-library changes `Docs` or `Build` prefixes are used.

**Design.** The codebase is written in a subset of freestanding C11 (C17) supported by most modern toolchains used by EDK II. Applying common software development practices or requesting clarification is recommended if any particular case is not discussed below.

- Never rely on undefined behaviour and try to avoid implementation defined behaviour unless explicitly covered below (feel free to create an issue when a relevant case is not present).
- Use `OcGuardLib` to ensure safe integral arithmetics avoiding overflows. Unsigned wraparound should be relied on with care and reduced to the necessary amount.
- Check pointers for correct alignment with `OcGuardLib` and do not rely on the architecture being able to dereference unaligned pointers.
- Use flexible array members instead of zero-length or one-length arrays where necessary.
- Use static assertions (`STATIC_ASSERT`) for type and value assumptions, and runtime assertions (`ASSERT`) for precondition and invariant sanity checking. Do not use runtime assertions to check for errors as they should never alter control flow and potentially be excluded.
- Assume `UINT32/INT32` to be `int`-sized and use `%u`, `%d`, and `%x` to print them.
- Assume `UINTN/INTN` to be of unspecified size, and cast them to `UINT64/INT64` for printing with `%Lu`, `%Ld` and so on as normal.
- Do not rely on integer promotions for numeric literals. Use explicit casts when the type is implementation-dependent or suffixes when type size is known. Assume `U` for `UINT32` and `ULL` for `UINT64`.
- Do ensure unsigned arithmetics especially in bitwise maths, shifts in particular.
- `sizeof` operator should take variables instead of types where possible to be error prone. Use `ARRAY_SIZE` to obtain array size in elements. Use `L_STR_LEN` and `L_STR_SIZE` macros from `OcStringLib` to obtain string literal sizes to ensure compiler optimisation.
- Do not use `goto` keyword. Prefer early `return`, `break`, or `continue` after failing to pass error checking instead of nesting conditionals.
- Use `EFIAPI`, force UEFI calling convention, only in protocols, external callbacks between modules, and functions with variadic arguments.
- Provide inline documentation to every added function, at least describing its inputs, outputs, precondition, postcondition, and giving a brief description.
- Do not use `RETURN_STATUS`. Assume `EFI_STATUS` to be a matching superset that is to be always used when `BOOLEAN` is not enough.

- Security violations should halt the system or cause a forced reboot.

**Codestyle.** The codebase follows EDK II codestyle with few changes and clarifications.

- Write inline documentation for the functions and variables only once: in headers, where a header prototype is available, and inline for **static** variables and functions.
- Use line length of 120 characters or less, preferably 100 characters.
- Use spaces after casts, e.g. `(VOID *) (UINTN) Variable`.
- Use SPDX license headers as shown in [acidanthera/bugtracker#483](#).

### 3.5 Debugging

The codebase incorporates EDK II debugging and few custom features to improve the experience.

- Use module prefixes, 2-5 letters followed by a colon (:), for debug messages. For `OpenCorePkg` use `OC:`, for libraries and drivers use their own unique prefixes.
- Do not use dots (.) in the end of debug messages and separate `EFI_STATUS`, printed by `%r`, with a hyphen (e.g. `OCRAM: Allocation of %u bytes failed - %r\n`).
- Use `DEBUG_CODE_BEGIN ()` and `DEBUG_CODE_END ()` constructions to guard debug checks that may potentially reduce the performance of release builds and are otherwise unnecessary.
- Use `DEBUG` macro to print debug messages during normal functioning, and `RUNTIME_DEBUG` for debugging after `EXIT_BOOT_SERVICES`.
- Use `DEBUG_VERBOSE` debug level to leave debug messages for future debugging of the code, which are currently not necessary. By default `DEBUG_VERBOSE` messages are ignored even in `DEBUG` builds.
- Use `DEBUG_INFO` debug level for all non critical messages (including errors) and `DEBUG_BULK_INFO` for extensive messages that should not appear in NVRAM log that is heavily limited in size. These messages are ignored in `RELEASE` builds.
- Use `DEBUG_ERROR` to print critical human visible messages that may potentially halt the boot process, and `DEBUG_WARN` for all other human visible errors, `RELEASE` builds included.

When trying to find the problematic change it is useful to rely on `git-bisect` functionality. There also are some unofficial resources that provide per-commit binary builds of OpenCore, ~~like~~ such as [Dortania](#).

**Failsafe:** All zero

**Description:** Match table signature to be equal to this value unless all zero.

In the majority of the cases ACPI patches are not useful and harmful:

- Avoid renaming devices with ACPI patches. This may fail or perform improper renaming of unrelated devices (e.g. EC and EC0), be unnecessary, or even fail to rename devices in select tables. For ACPI consistency it is much safer to rename devices at I/O Registry level, as done by WhateverGreen.
- Try to avoid patching `_OSI` to support a higher level of feature sets whenever possible. Commonly this enables a number of hacks on APTIO ~~firmwares~~firmware, which result in the need to add more patches. Modern ~~firmwares~~firmware ~~generally do~~generally does not need it ~~at all~~, and those that do are fine with much smaller patches. However, laptop vendors usually rely on this method to determine the availability of functions ~~like~~such as modern I2C input support, thermal adjustment and custom feature additions.
- Avoid patching embedded controller event `_Qxx` just for enabling brightness keys. The conventional process to find these keys usually involves massive modification on DSDT and SSDTs and the debug key is not stable on newer systems. Please switch to built-in brightness key discovery of BrightnessKeys instead.
- Try to avoid hacky changes ~~like~~such as renaming `_PRW` or `_DSM` whenever possible.

Several cases, where patching actually does make sense, include:

- Refreshing HPET (or another device) method header to avoid compatibility checks by `_OSI` on legacy hardware. `_STA` method with `if ((OSFL () == Zero)) { If (HPTE) ... Return (Zero)` content may be forced to always return 0xF by replacing `A0 10 93 4F 53 46 4C 00` with `A4 0A 0F A3 A3 A3 A3`.
- To provide custom method implementation with in an SSDT, for instance, to inject shutdown fix on certain computers, the original method can be replaced with a dummy name by patching `_PTS` with `ZPTS` and adding a callback to original method.

Tianocore AcpiAml.h source file may help understanding ACPI opcodes.

*Note:* Patches of different **Find** and **Replace** lengths are unsupported as they may corrupt ACPI tables and make the system unstable due to area relocation. If such changes are needed, the utilisation of “proxy” patching or the padding of NOP to the remaining area might be taken into account.

## 4.6 Quirks Properties

### 1. FadtEnableReset

**Type:** plist boolean

**Failsafe:** false

**Description:** Provide reset register and flag in FADT table to enable reboot and shutdown.

Mainly required on legacy hardware and few laptops. Can also fix power-button shortcuts. Not recommended unless required.

### 2. NormalizeHeaders

**Type:** plist boolean

**Failsafe:** false

**Description:** Cleanup ACPI header fields to workaround macOS ACPI implementation bug causing boot crashes. Reference: Debugging AppleACPIPlatform on 10.13 by Alex James aka theracermaster. The issue is fixed in macOS Mojave (10.14).

### 3. RebaseRegions

**Type:** plist boolean

**Failsafe:** false

**Description:** Attempt to heuristically relocate ACPI memory regions. Not recommended.

ACPI tables are often generated dynamically by underlying firmware implementation. Among the position-independent code, ACPI tables may contain physical addresses of MMIO areas used for device configuration, usually grouped in regions (e.g. `OperationRegion`). Changing firmware settings or hardware configuration, upgrading or patching the firmware inevitably leads to changes in dynamically generated ACPI code, which sometimes lead to the shift of the addresses in aforementioned `OperationRegion` constructions.

For this reason it is very dangerous to apply any kind of modifications to ACPI tables. The most reasonable approach is to make as few as possible changes to ACPI and try to not replace any tables, especially DSDT. When this is not possible, then at least attempt to ensure that custom DSDT is based on the most recent DSDT or remove writes and reads for the affected areas.

When nothing else helps this option could be tried to avoid stalls at **PCI Configuration Begin** phase of macOS booting by attempting to fix the ACPI addresses. It does not do magic, and only works with most common cases. Do not use unless absolutely required.

#### 4. ResetHwSig

**Type:** plist boolean

**Failsafe:** false

**Description:** Reset FACS table HardwareSignature value to 0.

This works around ~~firmwares~~ firmware that fail to maintain hardware signature across the reboots and cause issues with waking from hibernation.

#### 5. ResetLogoStatus

**Type:** plist boolean

**Failsafe:** false

**Description:** Reset BGRT table Displayed status field to false.

This works around ~~firmwares that provide~~ firmware that provide a BGRT table but fail to handle screen updates afterwards.

## 5 Booter

### 5.1 Introduction

This section allows to apply different kinds of UEFI modifications on Apple bootloader (`boot.efi`). The modifications currently provide various patches and environment alterations for different ~~firmwares~~firmware. Some of these features were originally implemented as a part of `AptioMemoryFix.efi`, which is no longer maintained. See [Tips and Tricks](#) section for migration steps.

If this is used for the first time on a customised firmware, there is a list of checks to do first. Prior to starting, the following requirements should be fulfilled:

- Most up-to-date UEFI firmware (check the motherboard vendor website).
- **Fast Boot** and **Hardware Fast Boot** disabled in firmware settings if present.
- **Above 4G Decoding** or similar enabled in firmware settings if present. Note, that on some motherboards (notably ASUS WS-X299-PRO) this option causes adverse effects, and must be disabled. While no other motherboards with the same issue are known, this option should be checked first whenever erratic boot failures are encountered.
- **DisableIoMapper** quirk enabled, or **VT-d** disabled in firmware settings if present, or **ACPI DMAR** table deleted.
- **No ‘slide’** boot argument present in NVRAM or anywhere else. It is not necessary unless the system cannot be booted at all or **No slide values are usable! Use custom slide!** message can be seen in the log.
- **CFG Lock** (MSR 0xE2 write protection) disabled in firmware settings if present. Consider patching it if no option is available (for advanced users only). See [VerifyMsrE2](#) notes for more details.
- **CSM** (Compatibility Support Module) disabled in firmware settings if present. On NVIDIA 6xx/AMD 2xx or older, **GOP ROM** may have to be flashed first. Use `GopUpdate` (see the second post) or `AMD UEFI GOP MAKER` in case of any potential confusion.
- **EHCI/XHCI Hand-off** enabled in firmware settings **only** if boot stalls unless USB devices are disconnected.
- **VT-x**, **Hyper Threading**, **Execute Disable Bit** enabled in firmware settings if present.
- While it may not be required, sometimes **Thunderbolt support**, **Intel SGX**, and **Intel Platform Trust** may have to be disabled in firmware settings present.

When debugging sleep issues **Power Nap** and **automatic power off** may be (temporarily) disabled, which appear to sometimes cause wake to black screen or boot loop issues on older platforms. The particular issues may vary, but in general **ACPI** tables should be looked up first. Here is an example of a bug found in some Z68 motherboards. To turn **Power Nap** and the others off run the following commands in Terminal:

---

```
sudo pmset autopoweroff 0
sudo pmset powernap 0
sudo pmset standby 0
```

---

*Note:* These settings may reset at hardware change and in certain other circumstances. To view their current state use `pmset -g` command in Terminal.

### 5.2 Properties

1. **MmioWhitelist**  
**Type:** `plist array`  
**Description:** Designed to be filled with `plist dict` values, describing addresses critical for particular firmware functioning when `DevirtualiseMmio` quirk is in use. See [MmioWhitelist Properties](#) section below.
2. **Quirks**  
**Type:** `plist dict`  
**Description:** Apply individual booter quirks described in [Quirks Properties](#) section below.

### 5.3 MmioWhitelist Properties

1. **Address**  
**Type:** `plist integer`  
**Failsafe:** 0  
**Description:** Exceptional MMIO address, which memory descriptor should be left virtualised (unchanged) by



DevirtualiseMmio. This means that the firmware will be able to directly communicate with this memory region during operating system functioning, because the region this value is in will be assigned a virtual address.

The addresses written here must be part of the memory map, have `EfiMemoryMappedIO` type and `EFI_MEMORY_RUNTIME` attribute (highest bit) set. To find the list of the candidates the debug log can be used.

2. Comment

**Type:** plist string

**Failsafe:** Empty string

**Description:** Arbitrary ASCII string used to provide human readable reference for the entry. It is implementation defined whether this value is used.

3. Enabled

**Type:** plist boolean

**Failsafe:** false

**Description:** This address will be devirtualised unless set to `true`.

## 5.4 Quirks Properties

1. AvoidRuntimeDefrag

**Type:** plist boolean

**Failsafe:** false

**Description:** Protect from boot.efi runtime memory defragmentation.

This option fixes UEFI runtime services (date, time, NVRAM, power control, etc.) support on ~~many firmwares using firmware that uses~~ SMM backing for select services ~~like such as~~ variable storage. SMM may try to access physical addresses, but they get moved by boot.efi.

*Note:* Most ~~but types of firmware, apart from~~ Apple and VMware ~~firmwares~~, need this quirk.

2. DevirtualiseMmio

**Type:** plist boolean

**Failsafe:** false

**Description:** Remove runtime attribute from select MMIO regions.

This option reduces stolen memory footprint from the memory map by removing runtime bit for known memory regions. This quirk may result in the increase of KASLR slides available, but is not necessarily compatible with the target board without additional measures. In general this frees from 64 to 256 megabytes of memory (present in the debug log), and on some platforms it is the only way to boot macOS, which otherwise fails with allocation error at bootloader stage.

This option is generally useful on all ~~firmwares types of firmware~~, except some very old ones ~~, like such as~~ Sandy Bridge. On ~~select firmwares it may require some types of firmware~~, a list of ~~exceptional addresses that still need to get their addresses that need~~ virtual addresses for proper NVRAM and hibernation ~~functioning. Use functionality may be required. Use the~~ MmioWhitelist section ~~to do for~~ this.

3. DisableSingleUser

**Type:** plist boolean

**Failsafe:** false

**Description:** Disable single user mode.

This is a security option that restricts the activation of single user mode by ignoring `CMD+S` hotkey and `-s` boot argument. The behaviour with this quirk enabled is supposed to match T2-based model behaviour. Refer to this archived article to understand how to use single user mode with this quirk enabled.

4. DisableVariableWrite

**Type:** plist boolean

**Failsafe:** false

**Description:** Protect from macOS NVRAM write access.

This is a security option that restricts NVRAM access in macOS. This quirk requires `OC_FIRMWARE_RUNTIME` protocol implemented in `OpenRuntime.efi`.

*Note:* This quirk can also be used as an ugly workaround to buggy UEFI runtime services implementations that fail to write variables to NVRAM and break the rest of the operating system.

5. **DiscardHibernateMap**

**Type:** plist boolean

**Failsafe:** false

**Description:** Reuse original hibernate memory map.

This option forces XNU kernel to ignore newly supplied memory map and assume that it did not change after waking from hibernation. This behaviour is required to work by Windows, which mandates to preserve runtime memory size and location after S4 wake.

*Note:* This may be used to workaround buggy memory maps on older hardware, and is now considered rare legacy. Examples of such hardware are Ivy Bridge laptops with Insyde firmware, ~~like~~ such as Acer V3-571G. Do not use this unless a complete understanding of the consequences can be ensured.

6. **EnableSafeModeSlide**

**Type:** plist boolean

**Failsafe:** false

**Description:** Patch bootloader to have KASLR enabled in safe mode.

This option is relevant to the users that have issues booting to safe mode (e.g. by holding `shift` or using `-x boot` argument). By default safe mode forces 0 slide as if the system was launched with `slide=0` boot argument. This quirk tries to patch `boot.efi` to lift that limitation and let some other value (from 1 to 255) be used. This quirk requires `ProvideCustomSlide` to be enabled.

*Note:* The necessity of this quirk is determined by safe mode availability. If booting to safe mode fails, this option can be tried to be enabled.

7. **EnableWriteUnprotector**

**Type:** plist boolean

**Failsafe:** false

**Description:** Permit write access to UEFI runtime services code.

This option bypasses `RX` permissions in code pages of UEFI runtime services by removing write protection (WP) bit from `CR0` register during their execution. This quirk requires `OC_FIRMWARE_RUNTIME` protocol implemented in `OpenRuntime.efi`.

*Note:* This quirk may potentially weaken firmware security, please use `RebuildAppleMemoryMap` if the firmware supports memory attributes table (MAT). Refer to `OCABC: MAT support is 1/0` log entry to determine whether MAT is supported.

8. **ForceExitBootServices**

**Type:** plist boolean

**Failsafe:** false

**Description:** Retry `ExitBootServices` with new memory map on failure.

Try to ensure that `ExitBootServices` call succeeds even with outdated `MemoryMap` key argument by obtaining current memory map and retrying `ExitBootServices` call.

*Note:* The necessity of this quirk is determined by early boot crashes of the firmware. Do not use this without a full understanding of the consequences.

9. **ProtectMemoryRegions**

**Type:** plist boolean

**Failsafe:** false

**Description:** Protect memory regions from incorrect access.

Some ~~firmwares~~ types of firmware incorrectly map select memory regions:

- CSM region can be marked as boot services code or data, which leaves it as free memory for XNU kernel.
- MMIO regions can be marked as reserved memory and stay unmapped, but may be required to be accessible at runtime for NVRAM support.

This quirk attempts to fix types of these regions, e.g. `ACPI NVS` for CSM or `MMIO` for MMIO.

*Note:* The necessity of this quirk is determined by artifacts, sleep wake issues, and boot failures. ~~In general only very old firmwares~~ Only very old firmware typically need this quirk.

10. **ProtectSecureBoot**

**Type:** plist boolean

**Failsafe:** false

**Description:** Protect UEFI Secure Boot variables from being written.

Reports security violation during attempts to write to db, dbx, PK, and KEK variables from the operating system.

*Note:* This quirk mainly attempts to avoid issues with NVRAM implementations with problematic defragmentation, such as select Insyde or MacPro5,1.

11. **ProtectUefiServices**

**Type:** plist boolean

**Failsafe:** false

**Description:** Protect UEFI services from being overridden by the firmware.

Some modern ~~firmwares including both hardware and virtual machines, like~~ firmware, including on virtual machines such as VMware, may update pointers to UEFI services during driver loading and related actions. Consequentially this directly breaks other quirks that affect memory management, ~~like such as~~ DevirtualiseMmio, **ProtectMemoryRegions**, or **RebuildAppleMemoryMap**, and may also break other quirks depending on the effects of these.

*Note:* On VMware the need for this quirk may be diagnosed by “Your Mac OS guest might run unreliably with more than one virtual core.” message.

12. **ProvideCustomSlide**

**Type:** plist boolean

**Failsafe:** false

**Description:** Provide custom KASLR slide on low memory.

This option performs memory map analysis of the firmware and checks whether all slides (from 1 to 255) can be used. As **boot.efi** generates this value randomly with **rdrand** or pseudo randomly **rdtsc**, there is a chance of boot failure when it chooses a conflicting slide. In case potential conflicts exist, this option forces macOS to use a pseudo random value among the available ones. This also ensures that **slide=** argument is never passed to the operating system for security reasons.

*Note:* The necessity of this quirk is determined by OCABC: **Only N/256 slide values are usable!** message in the debug log. If the message is present, this option is to be enabled.

13. **ProvideMaxSlide**

**Type:** plist integer

**Failsafe:** 0

**Description:** Provide maximum KASLR slide when higher ones are unavailable.

This option overrides the maximum slide of 255 by a user specified value between 1 and 254 inclusive when **ProvideCustomSlide** is enabled. It is believed that modern ~~firmwares allocate~~ firmware allocates pool memory from top to bottom, effectively resulting in free memory ~~at the time of slide scanning being later used when slide scanning is used later~~ as temporary memory during kernel loading. ~~In case those memory are unavailable~~ When such memory is not available, this option can stop ~~evaluating the evaluation of~~ higher slides.

*Note:* The necessity of this quirk is determined by random boot failure when **ProvideCustomSlide** is enabled and the randomized slide fall into the unavailable range. When **AppleDebug** is enabled, usually the debug log may contain messages ~~like such as~~ AAPL: [EB] 'LD:LKC] } Err(0x9). To find the optimal value, manually append **slide=X** to **boot-args** and log the largest one that will not result in boot failures.

14. **RebuildAppleMemoryMap**

**Type:** plist boolean

**Failsafe:** false

**Description:** Generate Memory Map compatible with macOS.

Apple kernel has several limitations in parsing UEFI memory map:

- Memory map size must not exceed 4096 bytes as Apple kernel maps it as a single 4K page. Since some ~~firmwares~~ types of firmware can have very large memory maps (~~approximately~~, potentially over 100 entries), the Apple kernel will crash ~~at-on~~ boot.
- Memory attributes table is ignored. `EfiRuntimeServicesCode` memory statically gets RX permissions, and all other memory types get RW permissions. Since some firmware drivers may write to global variables at runtime, Apple kernel will crash at calling UEFI runtime services, unless driver `.data` section has `EfiRuntimeServicesData` type.

To workaround these limitations, this quirk applies memory ~~attributes~~ attribute table permissions to the memory map passed to the Apple kernel and optionally attempts to unify contiguous slots of similar types if the resulting memory map exceeds 4 KB.

*Note 1:* Since ~~many firmwares~~ several types of firmware come with incorrect memory protection ~~table~~ tables, this quirk often comes ~~in pair~~ paired with `SyncRuntimePermissions`.

*Note 2:* The necessity of this quirk is determined by early boot failures. This quirk replaces `EnableWriteUnprotector` on ~~firmwares supporting memory attributes table~~ firmware supporting Memory Attribute Tables (MAT). This quirk is ~~generally~~ usually unnecessary when using `OpenDuetPkg`, but may be required to boot macOS 10.6 ~~and earlier for unclear reasons~~, and earlier, for reasons that are not clear.

#### 15. `SetupVirtualMap`

**Type:** plist boolean

**Failsafe:** false

**Description:** Setup virtual memory at `SetVirtualAddresses`.

~~Select firmwares~~ Some types of firmware access memory by virtual addresses after a `SetVirtualAddresses` call, ~~which results~~ resulting in early boot crashes. This quirk workarounds the problem by performing early boot identity mapping of assigned virtual addresses to physical memory.

*Note:* The necessity of this quirk is determined by early boot failures. Currently ~~new firmwares~~, new firmware with memory protection support (like such as `OVMF`) do not support this quirk ~~due to~~. See `acidanthera/bug-tracker#719`.

#### 16. `SignalAppleOS`

**Type:** plist boolean

**Failsafe:** false

**Description:** Report macOS being loaded through OS Info for any OS.

This quirk is useful on Mac ~~firmwares, which behave~~ firmware, which behaves differently in different OS. For example, it is supposed to enable Intel GPU in Windows and Linux in some dual-GPU MacBook models.

#### 17. `SyncRuntimePermissions`

**Type:** plist boolean

**Failsafe:** false

**Description:** Update memory permissions for runtime environment.

Some ~~firmwares~~ either types of firmware fail to properly handle runtime permissions:

- They incorrectly mark `OpenRuntime` as not executable in the memory map.
- They incorrectly mark `OpenRuntime` as not executable in the memory attributes table.
- They lose entries from the memory attributes table after `OpenRuntime` is loaded.
- They mark items in the memory attributes table as read-write-execute.

This quirk tries to update memory map and memory attributes table to correct this.

*Note:* The ~~necessity of~~ need for this quirk is ~~determined~~ indicated by early boot failures ~~either in macOS or in Linux/Windows~~. In general only firmwares released in 2018 or later are. Only firmware released after 2017 is typically affected.

## 6 DeviceProperties

### 6.1 Introduction

Device configuration is provided to macOS with a dedicated buffer, called `EfiDevicePathPropertyDatabase`. This buffer is a serialised map of `DevicePaths` to a map of property names and their values.

Property data can be debugged with `gfxutil`. To obtain current property data use the following command in macOS:

---

```
ioreg -lw0 -p IODeviceTree -n efi -r -x | grep device-properties |
sed 's/.*<///s/>.*///' > /tmp/device-properties.hex &&
gfxutil /tmp/device-properties.hex /tmp/device-properties.plist &&
cat /tmp/device-properties.plist
```

---

Device properties are part of the `IODeviceTree` (`gIODT`) plane of macOS I/O Registry. This plane has several construction stages relevant for the platform initialisation. While the early construction stage is performed by the XNU kernel in the `IODeviceTreeAlloc` method, the majority of the construction is performed by the platform expert, implemented in `AppleACPIPlatformExpert.kext`.

`AppleACPIPlatformExpert` incorporates two stages of `IODeviceTree` construction implemented by calling `AppleACPIPlatformExpert::mergeDeviceProperties`:

1. During ACPI table initialisation through the recursive ACPI namespace scanning by the calls to `AppleACPIPlatformExpert::createDTNubs`.
2. During IOService registration (`IOServices::registerService`) callbacks implemented as a part of `AppleACPIPlatformExpert::platformAdjustService` function and its private worker method `AppleACPIPlatformExpert::platformAdjustPCIDevice` specific to the PCI devices.

The application of the stages depends on the device presence in ACPI tables. The first stage applies very early but exclusively to the devices present in ACPI tables. The second stage applies to all devices much later after the PCI configuration and may repeat the first stage if the device was not present in ACPI.

For all kernel drivers, which may inspect the `IODeviceTree` plane without probing (e.g. Lilu and its plugins ~~like~~ [such as WhateverGreen](#)) it is particularly important to ensure device presence in the ACPI tables. Failing to do so may result **in all kinds of erratic behaviour** caused by ignoring the injected device properties as they were not constructed at the first stage. See `SSDT-IMEI.dsl` and `SSDT-BRGO.dsl` for an example.

### 6.2 Properties

1. Add

**Type:** `plist dict`

**Description:** Sets device properties from a map (`plist dict`) of device paths to a map (`plist dict`) of variable names and their values in `plist metadata` format. Device paths must be provided in canonic string format (e.g. `PciRoot(0x0)/Pci(0x1,0x0)/Pci(0x0,0x0)`). Properties will only be set if not present and not deleted.

*Note:* Currently properties may only be (formerly) added by the original driver, so unless a separate driver was installed, there is no reason to delete the variables.

2. Delete

**Type:** `plist dict`

**Description:** Removes device properties from a map (`plist dict`) of device paths to an array (`plist array`) of variable names in `plist string` format.

### 6.3 Common Properties

Some known properties include:

- `device-id`  
User-specified device identifier used for I/O Kit matching. Has 4 byte data type.
- `vendor-id`  
User-specified vendor identifier used for I/O Kit matching. Has 4 byte data type.

## 7 Kernel

### 7.1 Introduction

This section allows to apply different kinds of kernelspace modifications on Apple Kernel (XNU). The modifications currently provide driver (kext) injection, kernel and driver patching, and driver blocking.

### 7.2 Properties

#### 1. Add

**Type:** plist array

**Failsafe:** Empty

**Description:** Load selected kernel drivers from `OC/Kexts` directory.

Designed to be filled with `plist dict` values, describing each driver. See Add Properties section below. Kernel driver load order follows the item order in the array, thus the dependencies should be written prior to their consumers.

To track the dependency order, inspect the `OSBundleLibraries` key in the `Info.plist` of the kext. Any kext mentioned in the `OSBundleLibraries` of the other kext must precede this kext.

*Note:* Kexts may have inner kexts (Plug-Ins) in their bundle. Each inner kext must be added separately.

#### 2. Block

**Type:** plist array

**Failsafe:** Empty

**Description:** Remove selected kernel drivers from prelinked kernel.

Designed to be filled with `plist dictionary` values, describing each blocked driver. See Block Properties section below.

#### 3. Emulate

**Type:** plist dict

**Description:** Emulate select hardware in kernelspace via parameters described in Emulate Properties section below.

#### 4. Force

**Type:** plist array

**Failsafe:** Empty

**Description:** Load kernel drivers from system volume if they are not cached.

Designed to be filled with `plist dict` values, describing each driver. See Force Properties section below. This section resolves the problem of injecting drivers that depend on other drivers, which are not cached otherwise. The issue normally affects older operating systems, where various dependency kexts, [like such as](#) `IOAudioFamily` or `IONetworkingFamily` may not be present in the kernel cache by default. Kernel driver load order follows the item order in the array, thus the dependencies should be written prior to their consumers. **Force** happens before **Add**.

*Note:* The signature of the “forced” kernel drivers is not checked anyhow, making the use of this feature extremely dangerous and undesired for secure boot. This feature may not work on encrypted partitions in newer operating systems.

#### 5. Patch

**Type:** plist array

**Failsafe:** Empty

**Description:** Perform binary patches in kernel and drivers prior to driver addition and removal.

Designed to be filled with `plist dictionary` values, describing each patch. See Patch Properties section below.

#### 6. Quirks

**Type:** plist dict

**Description:** Apply individual kernel and driver quirks described in Quirks Properties section below.

**Failsafe:** false

**Requirement:** 10.4

**Description:** Disables PKG\_CST\_CONFIG\_CONTROL (0xE2) MSR modification in AppleIntelCPUPowerManagement.kext, commonly causing early kernel panic, when it is locked from writing.

~~Certain firmwares lock~~ Some types of firmware lock the PKG\_CST\_CONFIG\_CONTROL MSR register ~~—The and the~~ bundled VerifyMsrE2 tool can be used to check its state. ~~Some firmware~~ Note that some types of firmware only have this register locked ~~only~~ on some cores.

As modern ~~firmwares provide~~ firmware provide a CFG Lock setting ~~; which allows configuring that allows~~ configuring the PKG\_CST\_CONFIG\_CONTROL MSR register lock, this option should be avoided whenever possible. ~~For several APTIO firmwares not displaying~~ On APTIO firmware that do not provide a CFG Lock setting in the GUI, it is possible to access the option directly:

- (a) Download UEFITool and IFR-Extractor.
- (b) Open the firmware image in UEFITool and find CFG Lock unicode string. If it is not present, the firmware may not have this option and the process should therefore be discontinued.
- (c) Extract the Setup.bin PE32 Image Section (the UEFITool found) through the Extract Body menu option.
- (d) Run IFR-Extractor on the extracted file (e.g. ./ifrextact Setup.bin Setup.txt).
- (e) Find CFG Lock, VarStoreInfo (VarOffset/VarName): in Setup.txt and remember the offset right after it (e.g. 0x123).
- (f) Download and run Modified GRUB Shell compiled by brainsucker or use a newer version by datasone.
- (g) Enter setup\_var 0x123 0x00 command, where 0x123 should be replaced by the actual offset, and reboot.

**Warning:** Variable offsets are unique not only to each motherboard but even to its firmware version. Never ever try to use an offset without checking.

## 2. AppleXcpmCfgLock

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.8 (not required for older)

**Description:** Disables PKG\_CST\_CONFIG\_CONTROL (0xE2) MSR modification in XNU kernel, commonly causing early kernel panic, when it is locked from writing (XCPM power management).

*Note:* This option should be avoided whenever possible. See AppleCpuPmCfgLock description for more details.

## 3. AppleXcpmExtraMsrs

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.8 (not required for older)

**Description:** Disables multiple MSR access critical for select CPUs, which have no native XCPM support.

This is normally used in conjunction with Emulate section on Haswell-E, Broadwell-E, Skylake-SP, and similar CPUs. More details on the XCPM patches are outlined in acidanthera/bugtracker#365.

*Note:* Additional not provided patches will be required for Ivy Bridge or Pentium CPUs. It is recommended to use AppleIntelCpuPowerManagement.kext for the former.

## 4. AppleXcpmForceBoost

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.8 (not required for older)

**Description:** Forces maximum performance in XCPM mode.

This patch writes 0xFF00 to MSR\_IA32\_PERF\_CONTROL (0x199), effectively setting maximum multiplier for all the time.

*Note:* While this may increase the performance, this patch is strongly discouraged on all systems but those explicitly dedicated to scientific or media calculations. In general only certain Xeon models benefit from the patch.

## 5. CustomSMBIOSGuid

**Type:** plist boolean

**Failsafe:** false



**Requirement:** 10.4

**Description:** Performs GUID patching for UpdateSMBIOSMode Custom mode. Usually relevant for Dell laptops.

6. DisableIoMapper

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.8 (not required for older)

**Description:** Disables IOMapper support in XNU (VT-d), which may conflict with the firmware implementation.

*Note:* This option is a preferred alternative to deleting DMAR ACPI table and disabling VT-d in firmware preferences, which does not break VT-d support in other systems in case they need it.

7. DisableLinkeditJettison

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 11.0

**Description:** Disables \_\_LINKEDIT jettison code.

This option lets Lilu.kext and possibly some others function in macOS Big Sur with best performance without keepsyms=1 boot argument.

8. DisableRtcChecksum

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.4

**Description:** Disables primary checksum (0x58-0x59) writing in AppleRTC.

*Note 1:* This option will not protect other areas from being overwritten, see RTCMemoryFixup kernel extension if this is desired.

*Note 2:* This option will not protect areas from being overwritten at firmware stage (e.g. macOS bootloader), see AppleRtcRam protocol description if this is desired.

9. ExtendBTFeatureFlags

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.8

**Description:** Set FeatureFlags to 0x0F for full functionality of Bluetooth, including Continuity.

*Note:* This option is a substitution for BT4LEContinuityFixup.kext, which does not function properly due to late patching progress.

10. ExternalDiskIcons

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.4

**Description:** Apply icon type patches to AppleAHCIPort.kext to force internal disk icons for all AHCI disks.

*Note:* This option should be avoided whenever possible. Modern ~~firmwares~~firmware usually have compatible AHCI controllers.

11. IncreasePciBarSize

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.10

**Description:** Increases 32-bit PCI bar size in IOPCIFamily from 1 to 4 GBs.

*Note:* This option should be avoided whenever possible. In general the necessity of this option means misconfigured or broken firmware.

12. LpicKernelPanic

**Type:** plist boolean

**Failsafe:** false



**Requirement:** 10.6 (64-bit)

**Description:** Disables kernel panic on LAPIC interrupts.

13. LegacyCommpage

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.4 - 10.6

**Description:** Replaces the default 64-bit commpage bcopy implementation with one that does not require SSSE3, useful for legacy platforms. This prevents a `commpage no match for last` panic due to no available 64-bit bcopy functions that do not require SSSE3.

14. PanicNoKextDump

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.13 (not required for older)

**Description:** Prevent kernel from printing kext dump in the panic log preventing from observing panic details. Affects 10.13 and above.

15. PowerTimeoutKernelPanic

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.15 (not required for older)

**Description:** Disables kernel panic on setPowerState timeout.

An additional security measure was added to macOS Catalina (10.15) causing kernel panic on power change timeout for Apple drivers. Sometimes it may cause issues on misconfigured hardware, notably digital audio, which sometimes fails to wake up. For debug kernels `setpowerstate_panic=0` boot argument should be used, which is otherwise equivalent to this quirk.

16. ThirdPartyDrives

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.6 (not required for older)

**Description:** Apply vendor patches to IOAHCIBlockStorage.kext to enable native features for third-party drives, such as TRIM on SSDs or hibernation support on 10.15 and newer.

*Note:* This option may be avoided on user preference. NVMe SSDs are compatible without the change. For AHCI SSDs on modern macOS version there is a dedicated built-in utility called `trimforce`. Starting from 10.15 this utility creates `EnableTRIM` variable in `APPLE_BOOT_VARIABLE_GUID` namespace with 01 00 00 00 value.

17. XhciPortLimit

**Type:** plist boolean

**Failsafe:** false

**Requirement:** 10.11 (not required for older)

**Description:** Patch various kexts (AppleUSBXHCI.kext, AppleUSBXHCIPCI.kext, IOUSBHostFamily.kext) to remove USB port count limit of 15 ports.

*Note:* This option should be avoided whenever possible. USB port limit is imposed by the amount of used bits in locationID format and there is no possible way to workaround this without heavy OS modification. The only valid solution is to limit the amount of used ports to 15 (discarding some). More details can be found on AppleLife.ru.

## 7.9 Scheme Properties

These properties are particularly relevant for older macOS operating systems. For more details on how to install and troubleshoot such macOS installation refer to Legacy Apple OS.

1. FuzzyMatch

**Type:** plist boolean

**Failsafe:** false

**Description:** Use `kernelcache` with different checksums when available.

On macOS 10.6 and earlier `kernelcache` filename has a checksum, which essentially is `adler32` from SMBIOS product name and EfiBoot device path. On ~~certain firmwares~~ some types of firmware, the EfiBoot device path

differs between UEFI and macOS due to ACPI or hardware specifics, rendering `kernelcache` checksum as always different.

This setting allows matching the latest `kernelcache` with a suitable architecture when the `kernelcache` without suffix is unavailable, improving macOS 10.6 boot performance on several platforms.

## 2. KernelArch

**Type:** plist string

**Failsafe:** Auto

**Description:** Prefer specified kernel architecture (`Auto`, `i386`, `i386-user32`, `x86_64`) when available.

On macOS 10.7 and earlier XNU kernel can boot with architectures different from the usual `x86_64`. This setting will use the specified architecture to boot macOS when it is supported by the macOS and the configuration:

- **Auto** — Choose the preferred architecture automatically.
- **i386** — Use `i386` (32-bit) kernel when available.
- **i386-user32** — Use `i386` (32-bit) kernel when available and force the use of 32-bit userspace on 64-bit capable processors if supported by the operating system. On macOS 64-bit capable processors are assumed to support `SSSE3`. This is not the case for older 64-bit capable Pentium processors, which cause some applications to crash on macOS 10.6. This behaviour corresponds to `-legacy` kernel boot argument. This option is unavailable for 10.4 and 10.5 when running on 64-bit firmware due to an uninitialised 64-bit segment in the XNU kernel, which causes `AppleEFIRuntime` to incorrectly execute 64-bit code as 16-bit code.
- **x86\_64** — Use `x86_64` (64-bit) kernel when available.

Below is the algorithm determining the kernel architecture.

- (a) `arch` argument in image arguments (e.g. when launched via UEFI Shell) or in `boot-args` variable overrides any compatibility checks and forces the specified architecture, completing this algorithm.
- (b) OpenCore build architecture restricts capabilities to `i386` and `i386-user32` mode for the 32-bit firmware variant.
- (c) Determined EfiBoot version restricts architecture choice:
  - 10.4-10.5 — `i386` or `i386-user32` (only on 32-bit firmware)
  - 10.6 — `i386`, `i386-user32`, or `x86_64`
  - 10.7 — `i386` or `x86_64`
  - 10.8 or newer — `x86_64`
- (d) If `KernelArch` is set to `Auto` and `SSSE3` is not supported by the CPU, capabilities are restricted to `i386-user32` if supported by EfiBoot.
- (e) Board identifier (from SMBIOS) based on EfiBoot version disables `x86_64` support on an unsupported model if any `i386` variant is supported. `Auto` is not consulted here as the list is not overridable in EfiBoot.
- (f) `KernelArch` restricts the support to the explicitly specified architecture (when not set to `Auto`) if the architecture remains present in the capabilities.
- (g) The best supported architecture is chosen in this order: `x86_64`, `i386`, `i386-user32`.

Unlike macOS 10.7, where select boards identifiers are treated as the `i386` only machines, and macOS 10.5 or earlier, where `x86_64` is not supported by the macOS kernel, macOS 10.6 is very special. The architecture choice on macOS 10.6 depends on many factors including not only the board identifier, but also macOS product type (client vs server), macOS point release, and RAM amount. The detection of them all is complicated and not practical, because several point releases had genuine bugs and failed to properly perform the server detection in the first place. For this reason OpenCore on macOS 10.6 will fallback to `x86_64` architecture whenever it is supported by the board at all, ~~just like as~~ on macOS 10.7. As a reference here is the 64-bit Mac model compatibility corresponding to actual EfiBoot behaviour on macOS 10.6.8 and 10.7.5.

Model	10.6 (minimal)	10.6 (client)	10.6 (server)	10.7 (any)
Macmini	4,x (Mid 2010)	5,x (Mid 2011)	4,x (Mid 2010)	3,x (Early 2009)
MacBook	Unsupported	Unsupported	Unsupported	5,x (2009/09)
MacBookAir	Unsupported	Unsupported	Unsupported	2,x (Late 2008)
MacBookPro	4,x (Early 2008)	8,x (Early 2011)	8,x (Early 2011)	3,x (Mid 2007)
iMac	8,x (Early 2008)	12,x (Mid 2011)	12,x (Mid 2011)	7,x (Mid 2007)
MacPro	3,x (Early 2008)	5,x (Mid 2010)	3,x (Early 2008)	3,x (Early 2008)
Xserve	2,x (Early 2008)	2,x (Early 2008)	2,x (Early 2008)	2,x (Early 2008)

For Tools OpenCore will try to load a custom icon and fallback to the default icon:

- `ResetNVRAM` — `Resources\Image\ResetNVRAM.icns` — `ResetNVRAM.icns` from icons directory.
- `Tools\<TOOL_RELATIVE_PATH>.icns` — icon near the tool file with appended `.icns` extension.

For custom boot Entries OpenCore will try to load a custom icon and fallback to the volume icon or the default icon:

- `<ENTRY_PATH>.icns` — icon near the entry file with appended `.icns` extension.

For all other entries OpenCore will try to load a volume icon and fallback to the default icon:

- `.VolumeIcon.icns` file at `Preboot` root for APFS.
- `.VolumeIcon.icns` file at volume root for other filesystems.

Volume icons can be set in Finder. Note, that enabling this may result in external and internal icons to be indistinguishable.

- `0x0002` — `OC_ATTR_USE_DISK_LABEL_FILE`, provides custom rendered titles for boot entries:
  - `.disk_label` (`.disk_label_2x`) file near bootloader for all filesystems.
  - `<TOOL_NAME>.1b1` (`<TOOL_NAME>.12x`) file near tool for Tools.

Prerendered labels can be generated via `disklabel` utility or `bless` command. When disabled or missing text labels (`.contentDetails` or `.disk_label.contentDetails`) are to be rendered instead.

- `0x0004` — `OC_ATTR_USE_GENERIC_LABEL_IMAGE`, provides predefined label images for boot entries without custom entries. May give less detail for the actual boot entry.
- `0x0008` — `OC_ATTR_USE_ALTERNATE_ICONS`, changes used icon set to an alternate one if it is supported. For example, this could make a use of old-style icons with a custom background colour.

#### 5. PickerAudioAssist

**Type:** plist boolean

**Failsafe:** false

**Description:** Enable screen reader by default in boot picker.

For macOS bootloader screen reader preference is set in `preferences.efi` archive in `isV0Enabled.int32` file and is controlled by the operating system. For OpenCore screen reader support this option is an independent equivalent. Toggling screen reader support in both OpenCore boot picker and macOS bootloader FileVault 2 login window can also be done with `Command + F5` key combination.

*Note:* screen reader requires working audio support, see [UEFI Audio Properties](#) section for more details.

#### 6. PollAppleHotKeys

**Type:** plist boolean

**Failsafe:** false

**Description:** Enable modifier hotkey handling in boot picker.

In addition to `action hotkeys`, which are partially described in `PickerMode` section and are normally handled by Apple BDS, there exist modifier keys, which are handled by operating system bootloader, namely `boot.efi`. These keys allow to change operating system behaviour by providing different boot modes.

On some [firmwares-types of firmware](#), it may be problematic to use modifier keys due to driver incompatibilities. To workaround this problem this option allows registering select hotkeys in a more permissive manner from within boot picker. Such extensions include the support of tapping on keys in addition to holding and pressing `Shift` along with other keys instead of just `Shift` alone, which is not detectable on many PS/2 keyboards. This list of known `modifier hotkeys` includes:

- `CMD+C+MINUS` — disable board compatibility checking.
- `CMD+K` — boot release kernel, similar to `kcsuffix=release`.
- `CMD+S` — single user mode.
- `CMD+S+MINUS` — disable KASLR slide, requires disabled SIP.
- `CMD+V` — verbose mode.
- `Shift` — safe mode.

#### 7. ShowPicker

**Type:** plist boolean

**Failsafe:** false

**Description:** Show simple boot picker to allow boot entry selection.

#### 8. TakeoffDelay

**Type:** plist integer, 32 bit

**Failsafe:** 0

**Description:** Delay in microseconds performed before handling picker startup and **action hotkeys**.

Introducing a delay may give extra time to hold the right **action hotkey** sequence to e.g. boot to recovery mode. On some platforms setting this option to at least 5000–10000 microseconds may be necessary to access **action hotkeys** at all due to the nature of the keyboard driver.

#### 9. Timeout

**Type:** plist integer, 32 bit

**Failsafe:** 0

**Description:** Timeout in seconds in boot picker before automatic booting of the default boot entry. Use 0 to disable timer.

#### 10. PickerMode

**Type:** plist string

**Failsafe:** Builtin

**Description:** Choose boot picker used for boot management.

Picker describes underlying boot management with an optional user interface responsible for handling boot options. The following values are supported:

- **Builtin** — boot management is handled by OpenCore, a simple text only user interface is used.
- **External** — an external boot management protocol is used if available. Otherwise **Builtin** mode is used.
- **Apple** — Apple boot management is used if available. Otherwise **Builtin** mode is used.

Upon success **External** mode will entirely disable all boot management in OpenCore except policy enforcement. In **Apple** mode it may additionally bypass policy enforcement. See OpenCanopy plugin for an example of a custom user interface.

OpenCore built-in boot picker contains a set of actions chosen during the boot process. The list of supported actions is similar to Apple BDS and in general can be accessed by holding **action hotkeys** during boot process. Currently the following actions are considered:

- **Default** — this is the default option, and it lets OpenCore built-in boot picker to loads the default boot option as specified in Startup Disk preference pane.
- **ShowPicker** — this option forces picker to show. Normally it can be achieved by holding OPT key during boot. Setting **ShowPicker** to **true** will make **ShowPicker** the default option.
- **ResetNvram** — this option performs select UEFI variable erase and is normally achieved by holding CMD+OPT+P+R key combination during boot. Another way to erase UEFI variables is to choose **Reset NVRAM** in the picker. This option requires **AllowNvramReset** to be set to **true**.
- **BootApple** — this options performs booting to the first found Apple operating system unless the default chosen operating system is already made by Apple. Hold X key to choose this option.
- **BootAppleRecovery** — this option performs booting to Apple operating system recovery. Either the one related to the default chosen operating system, or first found in case default chosen operating system is not made by Apple or has no recovery. Hold CMD+R key combination to choose this option.

*Note 1:* Activated **KeySupport**, **OpenUsbKbDxe**, or similar driver is required for key handling to work. On ~~many firmwares~~ several types of firmware, it is not possible to get all the ~~keys function~~ key functions.

*Note 2:* In addition to OPT OpenCore supports **Escape** key to display picker when **ShowPicker** is disabled. This key exists for the Apple picker mode and for ~~firmwares~~ firmware with PS/2 keyboards that fail to report held OPT ~~key and require~~ keys and requiring continual presses of the Escape key to ~~enter access~~ the boot menu.

*Note 3:* On Macs with problematic GOP, it may be difficult to access the Apple BootPicker. The BootKicker utility can be blessed to workaround this problem even without loading OpenCore. On some Macs however, the BootKicker ~~will not~~ utility cannot be run from OpenCore.

## 8.4 Debug Properties

#### 1. AppleDebug

**Type:** plist boolean

**Failsafe:** false

**Description:** Enable `boot.efi` debug log saving to OpenCore log.

*Note:* This option only applies to 10.15.4 and newer.

## 2. ApplePanic

**Type:** plist boolean

**Failsafe:** false

**Description:** Save macOS kernel panic to OpenCore root partition.

The file is saved as `panic-YYYY-MM-DD-HHMMSS.txt`. It is strongly recommended to have `keepsym=1` boot argument to see debug symbols in the panic log. In case it was not present `kpdescribe.sh` utility (bundled with OpenCore) may be used to partially recover the stacktrace.

Development and debug kernels produce more helpful kernel panics. Consider downloading and installing `KernelDebugKit` from [developer.apple.com](https://developer.apple.com) when debugging a problem. To activate a development kernel the boot argument `kcsuffix=development` should be added. Use `uname -a` command to ensure that the current loaded kernel is a development (or a debug) kernel.

In case OpenCore kernel panic saving mechanism was not used, kernel panics may still be found in `/Library/Logs/DiagnosticReports` directory. Starting with macOS Catalina kernel panics are stored in JSON format, so they need to be preprocessed before passing to `kpdescribe.sh`:

---

```
cat Kernel.panic | grep macOSProcessedStackshotData |  
python -c 'import json,sys;print(json.load(sys.stdin)["macOSPanicString"]'
```

---

## 3. DisableWatchDog

**Type:** plist boolean

**Failsafe:** false

**Description:** ~~Select firmwares~~ Some types of firmware may not succeed in ~~quickly~~ booting the operating system quickly, especially in debug mode, which results in ~~watch-dog the watchdog~~ timer aborting the process. This option turns off ~~watch-dog the watchdog~~ timer.

## 4. DisplayDelay

**Type:** plist integer

**Failsafe:** 0

**Description:** Delay in microseconds performed after every printed line visible onscreen (i.e. console).

## 5. DisplayLevel

**Type:** plist integer, 64 bit

**Failsafe:** 0

**Description:** EDK II debug level bitmask (sum) showed onscreen. Unless **Target** enables console (onscreen) printing, onscreen debug output will not be visible. The following levels are supported (discover more in `DebugLib.h`):

- 0x00000002 (bit 1) — `DEBUG_WARN` in `DEBUG`, `NOOPT`, `RELEASE`.
- 0x00000040 (bit 6) — `DEBUG_INFO` in `DEBUG`, `NOOPT`.
- 0x00400000 (bit 22) — `DEBUG_VERBOSE` in custom builds.
- 0x80000000 (bit 31) — `DEBUG_ERROR` in `DEBUG`, `NOOPT`, `RELEASE`.

## 6. SerialInit

**Type:** plist boolean

**Failsafe:** false

**Description:** Perform serial port initialisation.

This option will perform serial port initialisation within OpenCore prior to enabling (any) debug logging. Serial port configuration is defined via PCDs at compile time in `gEfiMdeModulePkgTokenSpaceGuid` GUID. Default values as found in `MdeModulePkg.dec` are as follows:

- `PcdSerialBaudRate` — Baud rate: 115200.
- `PcdSerialLineControl` — Line control: no parity, 8 data bits, 1 stop bit.

See more details in [Debugging](#) section.

## 7. SysReport

**Type:** plist boolean

**Failsafe:** false

**Description:** Produce system report on ESP folder.

This option will create a **SysReport** directory on ESP partition unless it is already present. The directory will contain ACPI and SMBIOS dumps.

*Note:* For security reasons **SysReport** option is **not** available in **RELEASE** builds. Use a **DEBUG** build if this option is needed.

## 8. Target

**Type:** plist integer

**Failsafe:** 0

**Description:** A bitmask (sum) of enabled logging targets. By default all the logging output is hidden, so this option is required to be set when debugging is necessary.

The following logging targets are supported:

- 0x01 (bit 0) — Enable logging, otherwise all log is discarded.
- 0x02 (bit 1) — Enable basic console (onscreen) logging.
- 0x04 (bit 2) — Enable logging to Data Hub.
- 0x08 (bit 3) — Enable serial port logging.
- 0x10 (bit 4) — Enable UEFI variable logging.
- 0x20 (bit 5) — Enable non-volatile UEFI variable logging.
- 0x40 (bit 6) — Enable logging to file.

Console logging prints less than all the other variants. Depending on the build type (**RELEASE**, **DEBUG**, or **NOOPT**) different amount of logging may be read (from least to most).

Data Hub log will not log kernel and kext patches. To obtain Data Hub log use the following command in macOS:

---

```
ioreg -lw0 -p IODeviceTree | grep boot-log | sort | sed 's/.*<\(.*\)>.*\/1/' | xxd -r -p
```

---

UEFI variable log does not include some messages and has no performance data. For safety reasons log size is limited to 32 kilobytes. Some ~~firmwares~~ [types of firmware](#) may truncate it much earlier or drop completely if they have no memory. Using non-volatile flag will write the log to NVRAM flash after every printed line. To obtain UEFI variable log use the following command in macOS:

---

```
nvrasm 4D1FDA02-38C7-4A6A-9CC6-4BCCA8B30102:boot-log |  
awk '{gsub(/%0d%0a%00/, ""); gsub(/%0d%0a/, "\n")}1'
```

---

**Warning:** Some ~~firmwares are reported to have broken~~ [types of firmware appear to have flawed](#) NVRAM garbage collection. This means that they may not be able to always free space after variable deletion. Do not use non-volatile NVRAM logging without extra need on such devices.

While OpenCore boot log already contains basic version information with build type and date, this data may also be found in NVRAM in **opencore-version** variable even with boot log disabled.

File logging will create a file named **opencore-YYYY-MM-DD-HHMMSS.txt** at EFI volume root with log contents (the upper case letter sequence is replaced with date and time from the firmware). Please be warned that some file system drivers present in ~~firmwares~~ [firmware](#) are not reliable, and may corrupt data when writing files through UEFI. Log ~~is attempted to be written~~ [writing is attempted](#) in the safest manner, ~~and thus~~ [and thus](#), is very slow. Ensure that **DisableWatchDog** is set to **true** when a slow drive is used. Try to avoid frequent use of this option when dealing with flash drives as large I/O amounts may speedup memory wear and render ~~this the~~ [this the](#) flash drive unusable ~~in shorter time~~ [quicker](#).

When interpreting the log, note that the lines are prefixed with a tag describing the relevant location (module) of the log line allowing better attribution of the line to the functionality. The list of currently used tags is provided below.

**Drivers and tools:**

- BMF — OpenCanopy, bitmap font



- **None** — do nothing.
- **Bootstrap** — create or update top-priority \EFI\OC\Bootstrap\Bootstrap.efi boot option (Boot9696) in UEFI variable storage at bootloader startup. For this option to work RequestBootVarRouting is required to be enabled.

This option provides integration with third-party operating system installation and upgrade at the times they overwrite \EFI\BOOT\BOOTx64.efi file. By creating a custom option in **Bootstrap** mode this file path becomes no longer used for bootstrapping OpenCore.

*Note 1:* Some ~~firmwares may have broken~~ types of firmware may have faulty NVRAM, no boot option support, or ~~various other incompatibilities of any kind~~ other incompatibilities. While unlikely, the use of this option may even cause boot ~~failure~~ failures. This option should be used without any warranty exclusively on the boards known to be compatible. [Check acidanthera/bugtracker#1222 for some known issues with Haswell and other boards.](#)

*Note 2:* Be ~~warned~~ aware that while NVRAM reset executed from OpenCore should not erase the boot option created in **Bootstrap**, executing NVRAM reset prior to loading OpenCore will remove it.

## 6. DmgLoading

**Type:** plist string

**Failsafe:** Signed

**Description:** Define Disk Image (DMG) loading policy used for macOS Recovery.

Valid values:

- **Disabled** — loading DMG images will fail. **Disabled** policy will still let macOS Recovery to load in most cases as there usually are boot.efi files compatible with Apple Secure Boot. Manually downloaded DMG images stored in com.apple.recovery.boot directories will not load, however.
- **Signed** — only Apple-signed DMG images will load. Due to Apple Secure Boot design **Signed** policy will let any Apple-signed macOS Recovery to load regardless of Apple Secure Boot state, which may not always be desired.
- **Any** — any DMG images will mount as normal filesystems. **Any** policy is strongly not recommended and will cause a boot failure when Apple Secure Boot is activated.

## 7. EnablePassword

**Type:** plist boolean

**Failsafe:** false

**Description:** Enable password protection to allow sensitive operations.

Password protection ensures that sensitive operations ~~like such as~~ booting a non-default operating system (e.g. macOS recovery or a tool), resetting NVRAM storage, trying to boot into a non-default mode (e.g. verbose mode or safe mode) are not allowed without explicit user authentication by a custom password. Currently password and salt are hashed with 5000000 iterations of SHA-512.

*Note:* This functionality is currently in development and is not ready for daily usage.

## 8. ExposeSensitiveData

**Type:** plist integer

**Failsafe:** 0x6

**Description:** Sensitive data exposure bitmask (sum) to operating system.

- 0x01 — Expose printable booter path as an UEFI variable.
- 0x02 — Expose OpenCore version as an UEFI variable.
- 0x04 — Expose OpenCore version in boot picker menu title.
- 0x08 — Expose OEM information as a set of UEFI variables.

Exposed booter path points to OpenCore.efi or its booter depending on the load order. To obtain booter path use the following command in macOS:

---

```
nvr 4D1FDA02-38C7-4A6A-9CC6-4BCCA8B30102:boot-path
```

---

To use booter path for mounting booter volume use the following command in macOS:

---

```
u=$(nvr 4D1FDA02-38C7-4A6A-9CC6-4BCCA8B30102:boot-path | sed 's/.*GPT,\([^,]*\) ,.*\/1\/'); \  
if [ "$u" != "" ]; then sudo diskutil mount $u ; fi
```

---

---

```
cd /Volumes/EFI/EFI/OC
/path/to/create_vault.sh .
/path/to/RsaTool -sign vault.plist vault.sig vault.pub
off=$((strings -a -t d OpenCore.efi | grep "=BEGIN OC VAULT=" | cut -f1 -d' ') + 16))
dd of=OpenCore.efi if=vault.pub bs=1 seek=$off count=528 conv=notrunc
rm vault.pub
```

---

*Note 1:* While it may appear obvious, an external method is required to verify `OpenCore.efi` and `B00Tx64.efi` for secure boot path. For this, it is recommended to enable UEFI SecureBoot using a custom certificate and to sign `OpenCore.efi` and `B00Tx64.efi` with a custom key. More details on customising secure boot on modern ~~firmwares~~ [firmware](#) can be found in Taming UEFI SecureBoot paper (in Russian).

*Note 2:* `vault.plist` and `vault.sig` are used regardless of this option when `vault.plist` is present or public key is embedded into `OpenCore.efi`. Setting this option will only ensure configuration sanity, and abort the boot process otherwise.

### 13. ScanPolicy

**Type:** plist integer, 32 bit

**Failsafe:** 0x10F0103

**Description:** Define operating system detection policy.

This value allows to prevent scanning (and booting) from untrusted source based on a bitmask (sum) of select flags. As it is not possible to reliably detect every file system or device type, this feature cannot be fully relied upon in open environments, and the additional measures are to be applied.

Third party drivers may introduce additional security (and performance) measures following the provided scan policy. Scan policy is exposed in `scan-policy` variable of 4D1FDA02-38C7-4A6A-9CC6-4BCCA8B30102 GUID for UEFI Boot Services only.

- 0x00000001 (bit 0) — `OC_SCAN_FILE_SYSTEM_LOCK`, restricts scanning to only known file systems defined as a part of this policy. File system drivers may not be aware of this policy, and to avoid mounting of undesired file systems it is best not to load its driver. This bit does not affect dmg mounting, which may have any file system. Known file systems are prefixed with `OC_SCAN_ALLOW_FS_`.
- 0x00000002 (bit 1) — `OC_SCAN_DEVICE_LOCK`, restricts scanning to only known device types defined as a part of this policy. This is not always possible to detect protocol tunneling, so be aware that on some systems it may be possible for e.g. USB HDDs to be recognised as SATA. Cases like this must be reported. Known device types are prefixed with `OC_SCAN_ALLOW_DEVICE_`.
- 0x00000100 (bit 8) — `OC_SCAN_ALLOW_FS_APFS`, allows scanning of APFS file system.
- 0x00000200 (bit 9) — `OC_SCAN_ALLOW_FS_HFS`, allows scanning of HFS file system.
- 0x00000400 (bit 10) — `OC_SCAN_ALLOW_FS_ESP`, allows scanning of EFI System Partition file system.
- 0x00000800 (bit 11) — `OC_SCAN_ALLOW_FS_NTFS`, allows scanning of NTFS (Msft Basic Data) file system.
- 0x00001000 (bit 12) — `OC_SCAN_ALLOW_FS_EXT`, allows scanning of EXT (Linux Root) file system.
- 0x00010000 (bit 16) — `OC_SCAN_ALLOW_DEVICE_SATA`, allow scanning SATA devices.
- 0x00020000 (bit 17) — `OC_SCAN_ALLOW_DEVICE_SAS`, allow scanning SAS and Mac NVMe devices.
- 0x00040000 (bit 18) — `OC_SCAN_ALLOW_DEVICE_SCSI`, allow scanning SCSI devices.
- 0x00080000 (bit 19) — `OC_SCAN_ALLOW_DEVICE_NVME`, allow scanning NVMe devices.
- 0x00100000 (bit 20) — `OC_SCAN_ALLOW_DEVICE_ATAPI`, allow scanning CD/DVD devices and old SATA.
- 0x00200000 (bit 21) — `OC_SCAN_ALLOW_DEVICE_USB`, allow scanning USB devices.
- 0x00400000 (bit 22) — `OC_SCAN_ALLOW_DEVICE_FIREWIRE`, allow scanning FireWire devices.
- 0x00800000 (bit 23) — `OC_SCAN_ALLOW_DEVICE_SDCARD`, allow scanning card reader devices.
- 0x01000000 (bit 24) — `OC_SCAN_ALLOW_DEVICE_PCI`, allow scanning devices directly connected to PCI bus (e.g. VIRTIO).

*Note:* Given the above description, 0xF0103 value is expected to allow scanning of SATA, SAS, SCSI, and NVMe devices with APFS file system, and prevent scanning of any devices with HFS or FAT32 file systems in addition to not scanning APFS file systems on USB, CD, and FireWire drives. The combination reads as:

- `OC_SCAN_FILE_SYSTEM_LOCK`
- `OC_SCAN_DEVICE_LOCK`
- `OC_SCAN_ALLOW_FS_APFS`



- OC\_SCAN\_ALLOW\_DEVICE\_SATA
- OC\_SCAN\_ALLOW\_DEVICE\_SASEX
- OC\_SCAN\_ALLOW\_DEVICE\_SCSI
- OC\_SCAN\_ALLOW\_DEVICE\_NVME

#### 14. SecureBootModel

**Type:** plist string

**Failsafe:** Default

**Description:** Apple Secure Boot hardware model.

Sets Apple Secure Boot hardware model and policy. Specifying this value defines which operating systems will be bootable. Operating systems shipped before the specified model was released will not boot. Valid values:

- **Default** — Recent available model, currently set to j137.
- **Disabled** — No model, Secure Boot will be disabled.
- j137 — iMacPro1,1 (December 2017) minimum macOS 10.13.2 (17C2111)
- j680 — MacBookPro15,1 (July 2018) minimum macOS 10.13.6 (17G2112)
- j132 — MacBookPro15,2 (July 2018) minimum macOS 10.13.6 (17G2112)
- j174 — Macmini8,1 (October 2018) minimum macOS 10.14 (18A2063)
- j140k — MacBookAir8,1 (October 2018) minimum macOS 10.14.1 (18B2084)
- j780 — MacBookPro15,3 (May 2019) minimum macOS 10.14.5 (18F132)
- j213 — MacBookPro15,4 (July 2019) minimum macOS 10.14.5 (18F2058)
- j140a — MacBookAir8,2 (July 2019) minimum macOS 10.14.5 (18F2058)
- j152f — MacBookPro16,1 (November 2019) minimum macOS 10.15.1 (19B2093)
- j160 — MacPro7,1 (December 2019) minimum macOS 10.15.1 (19B88)
- j230k — MacBookAir9,1 (March 2020) minimum macOS 10.15.3 (19D2064)
- j214k — MacBookPro16,2 (May 2020) minimum macOS 10.15.4 (19E2269)
- j223 — MacBookPro16,3 (May 2020) minimum macOS 10.15.4 (19E2265)
- j215 — MacBookPro16,4 (June 2020) minimum macOS 10.15.5 (19F96)
- j185 — iMac20,1 (August 2020) minimum macOS 10.15.6 (19G2005)
- j185f — iMac20,2 (August 2020) minimum macOS 10.15.6 (19G2005)

PlatformInfo and SecureBootModel are independent, allowing to enabling Apple Secure Boot with any SMBIOS. Setting SecureBootModel to any valid value but Disabled is equivalent to Medium Security of Apple Secure Boot. The ApECID value must also be specified to achieve Full Security.

Enabling Apple Secure Boot is more demanding to incorrect configurations, buggy macOS installations, and unsupported setups. Things to consider:

- As with T2 Macs, unsigned kernel drivers and several signed kernel drivers, including NVIDIA Web Drivers, cannot be installed.
- The list of cached drivers may be different, resulting in the need to change the list of Added or Forced kernel drivers. For example, IO80211Family cannot be injected in this case.
- System volume alterations on operating systems with sealing, ~~like such as~~ macOS 11, may result in the operating system being unbootable. Do not try to disable system volume encryption unless Apple Secure Boot is disabled.
- If the platform requires certain settings, but they were not enabled, because the obvious issues did not trigger before, boot failure might occur. Be extra careful with IgnoreInvalidFlexRatio or HashServices.
- Operating systems released before Apple Secure Boot landed (e.g. macOS 10.12 or earlier) will still boot until UEFI Secure Boot is enabled. This is so, because from Apple Secure Boot point they are treated as incompatible and are assumed to be handled by the firmware ~~just like as~~ Microsoft Windows is.
- On older CPUs (e.g. before Sandy Bridge) enabling Apple Secure Boot might cause slightly slower loading by up to 1 second.
- Since Default value will increase with time to support the latest major release operating system, it is not recommended to use ApECID and Default value together.

Sometimes the already installed operating system may have outdated Apple Secure Boot manifests on the Preboot partition causing boot failure. If there is “OCB: Apple Secure Boot prohibits this boot entry, enforcing!” message, it is likely the case. When this happens, either reinstall the operating system or copy the manifests (files with .im4m extension, such as boot.efi.j137.im4m) from /usr/standalone/i386 to /Volumes/Preboot/<UUID>/System/Library/CoreServices. Here <UUID> is the system volume identifier. On

**Warning:** This feature is very dangerous as it passes unprotected data to firmware variable services. Use it only when no hardware NVRAM implementation is provided by the firmware or it is incompatible.

#### 4. LegacyOverwrite

**Type:** plist boolean

**Failsafe:** false

**Description:** Permits overwriting firmware variables from `nvr.plist`.

*Note:* Only variables accessible from the operating system will be overwritten.

#### 5. LegacySchema

**Type:** plist dict

**Description:** Allows setting select NVRAM variables from a map (plist dict) of GUIDs to an array (plist array) of variable names in `plist string` format.

\* value can be used to accept all variables for select GUID.

**WARNING:** Choose variables very carefully, as `nvr.plist` is not vaulted. For instance, do not put `boot-args` or `csr-active-config`, as this can bypass SIP.

#### 6. WriteFlash

**Type:** plist boolean

**Failsafe:** false

**Description:** Enables writing to flash memory for all added variables.

*Note:* ~~This value~~ It is recommended to ~~be~~ have this value enabled on most ~~firmwares, but~~ types of firmware but it is left configurable for ~~firmwares~~ firmware that may have issues with NVRAM variable storage garbage collection or ~~alikesimilar~~.

To read NVRAM variable value from macOS, `nvr` could be used by concatenating GUID and name variables separated by a `:` symbol. For example, `nvr 7C436110-AB2A-4BBB-A880-FE41995C9F82:boot-args`.

A continuously updated variable list can be found in a corresponding document: NVRAM Variables.

## 9.3 Mandatory Variables

**Warning:** These variables may be added by PlatformNVRAM or Generic subsections of PlatformInfo section. Using PlatformInfo is the recommend way of setting these variables.

The following variables are mandatory for macOS functioning:

- `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:FirmwareFeatures`  
32-bit `FirmwareFeatures`. Present on all Macs to avoid extra parsing of SMBIOS tables
- `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:FirmwareFeaturesMask`  
32-bit `FirmwareFeaturesMask`. Present on all Macs to avoid extra parsing of SMBIOS tables.
- `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:MLB`  
`BoardSerialNumber`. Present on newer Macs (2013+ at least) to avoid extra parsing of SMBIOS tables, especially in `boot.efi`.
- `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:ROM`  
Primary network adapter MAC address or replacement value. Present on newer Macs (2013+ at least) to avoid accessing special memory region, especially in `boot.efi`.

## 9.4 Recommended Variables

The following variables are recommended for faster startup or other improvements:

- `7C436110-AB2A-4BBB-A880-FE41995C9F82:csr-active-config`  
32-bit System Integrity Protection bitmask. Declared in XNU source code in `csr.h`.
- `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:ExtendedFirmwareFeatures`  
Combined `FirmwareFeatures` and `ExtendedFirmwareFeatures`. Present on newer Macs to avoid extra parsing of SMBIOS tables
- `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:ExtendedFirmwareFeaturesMask`  
Combined `FirmwareFeaturesMask` and `ExtendedFirmwareFeaturesMask`. Present on newer Macs to avoid extra parsing of SMBIOS tables.

## 10 PlatformInfo

Platform information is comprised of several identification fields generated or filled manually to be compatible with macOS services. The base part of the configuration may be obtained from `AppleModels`, which itself generates a set of interfaces based on a database in YAML format. These fields are written to three select destinations:

- SMBIOS
- Data Hub
- NVRAM

Most of the fields specify the overrides in SMBIOS, and their field names conform to EDK2 `SmBios.h` header file. However, several important fields reside in Data Hub and NVRAM. Some of the values can be found in more than one field and/or destination, so there are two ways to control their update process: manual, where all the values are specified (the default), and semi-automatic, where (`Automatic`) only select values are specified, and later used for system configuration.

To inspect SMBIOS contents `dmidecode` utility can be used. Version with macOS specific enhancements can be downloaded from [Acidanthera/dmidecode](#).

### 10.1 Properties

#### 1. Automatic

**Type:** plist boolean

**Failsafe:** false

**Description:** Generate PlatformInfo based on `Generic` section instead of using values from `DataHub`, `NVRAM`, and `SMBIOS` sections.

Enabling this option is useful when `Generic` section is flexible enough:

- When enabled `SMBIOS`, `DataHub`, and `PlatformNVRAM` data is unused.
- When disabled `Generic` section is unused.

**Warning:** It is strongly discouraged set this option to `false` when intending to update platform information. The only reason to do that is when doing minor correction of the SMBIOS present and [alikesimilar](#). In all other cases not using `Automatic` may lead to hard to debug errors.

#### 2. CustomMemory

**Type:** plist boolean

**Failsafe:** false

**Description:** Use custom memory configuration defined in the `Memory` section. This completely replaces any existing memory configuration in SMBIOS, and is only active when `UpdateSMBIOS` is set to `true`.

#### 3. UpdateDataHub

**Type:** plist boolean

**Failsafe:** false

**Description:** Update Data Hub fields. These fields are read from `Generic` or `DataHub` sections depending on `Automatic` value.

#### 4. UpdateNVRAM

**Type:** plist boolean

**Failsafe:** false

**Description:** Update NVRAM fields related to platform information.

These fields are read from `Generic` or `PlatformNVRAM` sections depending on `Automatic` value. All the other fields are to be specified with `NVRAM` section.

If `UpdateNVRAM` is set to `false` the aforementioned variables can be updated with `NVRAM` section. If `UpdateNVRAM` is set to `true` the behaviour is undefined when any of the fields are present in `NVRAM` section.

#### 5. UpdateSMBIOS

**Type:** plist boolean

**Failsafe:** false

**Description:** Update SMBIOS fields. These fields are read from `Generic` or `SMBIOS` sections depending on `Automatic` value.

## 6. UpdateSMBIOSMode

**Type:** plist string

**Failsafe:** Create

**Description:** Update SMBIOS fields approach:

- **TryOverwrite** — **Overwrite** if new size is <= than the page-aligned original and there are no issues with legacy region unlock. **Create** otherwise. Has issues ~~with some firmwares~~ on some types of firmware.
- **Create** — Replace the tables with newly allocated EfiReservedMemoryType at AllocateMaxAddress without any fallbacks.
- **Overwrite** — Overwrite existing gEfiSmbiosTableGuid and gEfiSmbiosTable3Guid data if it fits new size. Abort with unspecified state otherwise.
- **Custom** — Write SMBIOS tables (gEfiSmbios(3)TableGuid) to gOcCustomSmbios(3)TableGuid to workaround ~~firmwares~~ firmware overwriting SMBIOS contents at ExitBootServices. Otherwise equivalent to **Create**. Requires patching AppleSmbios.kext and AppleACPIPlatform.kext to read from another GUID: "EB9D2D31" - "EB9D2D35" (in ASCII), done automatically by CustomSMBIOSGuid quirk.

*Note:* A side effect of using **Custom** approach is making SMBIOS updates exclusive to macOS, avoiding a collision with existing Windows activation and custom OEM software but potentially breaking Apple-specific tools.

## 7. Generic

**Type:** plist dictionary

**Description:** Update all fields. This section is read only when **Automatic** is active.

## 8. DataHub

**Type:** plist dictionary

**Optional:** When **Automatic** is true

**Description:** Update Data Hub fields. This section is read only when **Automatic** is not active.

## 9. Memory

**Type:** plist dictionary

**Optional:** When **CustomMemory** is false

**Description:** Define custom memory configuration.

## 10. PlatformNVRAM

**Type:** plist dictionary

**Optional:** When **Automatic** is true

**Description:** Update platform NVRAM fields. This section is read only when **Automatic** is not active.

## 11. SMBIOS

**Type:** plist dictionary

**Optional:** When **Automatic** is true

**Description:** Update SMBIOS fields. This section is read only when **Automatic** is not active.

# 10.2 Generic Properties

## 1. SpoofVendor

**Type:** plist boolean

**Failsafe:** false

**Description:** Sets SMBIOS vendor fields to Acidanthera.

It is dangerous to use Apple in SMBIOS vendor fields for reasons given in **SystemManufacturer** description. However, certain ~~firmwares~~ firmware may not provide valid values otherwise, which could break some software.

## 2. AdviseWindows

**Type:** plist boolean

**Failsafe:** false

**Description:** Forces Windows support in **FirmwareFeatures**.

Added bits to **FirmwareFeatures**:

- **FW\_FEATURE\_SUPPORTS\_CSM\_LEGACY\_MODE** (0x1) - Without this bit it is not possible to reboot to Windows installed on a drive with EFI partition being not the first partition on the disk.

**Description:** Sets `SystemSerialNumber` in `gEfiMiscSubClassGuid`. Value found on Macs is equal to SMBIOS `SystemSerialNumber` in Unicode.

4. `SystemUUID`

**Type:** plist string, GUID

**Failsafe:** Not installed

**Description:** Sets `system-id` in `gEfiMiscSubClassGuid`. Value found on Macs is equal to SMBIOS `SystemUUID`.

5. `BoardProduct`

**Type:** plist string

**Failsafe:** Not installed

**Description:** Sets `board-id` in `gEfiMiscSubClassGuid`. Value found on Macs is equal to SMBIOS `BoardProduct` in ASCII.

6. `BoardRevision`

**Type:** plist data, 1 byte

**Failsafe:** 0

**Description:** Sets `board-rev` in `gEfiMiscSubClassGuid`. Value found on Macs seems to correspond to internal board revision (e.g. 01).

7. `StartupPowerEvents`

**Type:** plist integer, 64-bit

**Failsafe:** 0

**Description:** Sets `StartupPowerEvents` in `gEfiMiscSubClassGuid`. Value found on Macs is power management state bitmask, normally 0. Known bits read by `X86PlatformPlugin.kext`:

- 0x00000001 — Shutdown cause was a `PWROK` event (Same as `GEN_PMCN_2` bit 0)
- 0x00000002 — Shutdown cause was a `SYS_PWROK` event (Same as `GEN_PMCN_2` bit 1)
- 0x00000004 — Shutdown cause was a `THRMTRIP#` event (Same as `GEN_PMCN_2` bit 3)
- 0x00000008 — Rebooted due to a `SYS_RESET#` event (Same as `GEN_PMCN_2` bit 4)
- 0x00000010 — Power Failure (Same as `GEN_PMCN_3` bit 1 `PWR_FLR`)
- 0x00000020 — Loss of RTC Well Power (Same as `GEN_PMCN_3` bit 2 `RTC_PWR_STS`)
- 0x00000040 — General Reset Status (Same as `GEN_PMCN_3` bit 9 `GEN_RST_STS`)
- 0xffffffff80 — SUS Well Power Loss (Same as `GEN_PMCN_3` bit 14)
- 0x00010000 — Wake cause was a ME Wake event (Same as `PRSTS` bit 0, `ME_WAKE_STS`)
- 0x00020000 — Cold Reboot was ME Induced event (Same as `PRSTS` bit 1 `ME_HRST_COLD_STS`)
- 0x00040000 — Warm Reboot was ME Induced event (Same as `PRSTS` bit 2 `ME_HRST_WARM_STS`)
- 0x00080000 — Shutdown was ME Induced event (Same as `PRSTS` bit 3 `ME_HOST_PWRDN`)
- 0x00100000 — Global reset ME Watchdog Timer event (Same as `PRSTS` bit 6)
- 0x00200000 — Global reset PowerManagement Watchdog Timer event (Same as `PRSTS` bit 15)

8. `InitialTSC`

**Type:** plist integer, 64-bit

**Failsafe:** 0

**Description:** Sets `InitialTSC` in `gEfiProcessorSubClassGuid`. Sets initial TSC value, normally 0.

9. `FSBFrequency`

**Type:** plist integer, 64-bit

**Failsafe:** 0 (Automatic)

**Description:** Sets `FSBFrequency` in `gEfiProcessorSubClassGuid`.

Sets CPU FSB frequency. This value equals to CPU nominal frequency divided by CPU maximum bus ratio and is specified in Hz. Refer to `MSR_NEHALEM_PLATFORM_INFO` (CEh) MSR value to determine maximum bus ratio on modern Intel CPUs.

*Note:* This value is not used on Skylake and newer but is still provided to follow suit.

10. `ARTFrequency`

**Type:** plist integer, 64-bit

**Failsafe:** 0 (Automatic)

**Description:** Sets `ARTFrequency` in `gEfiProcessorSubClassGuid`.

This value contains CPU ART frequency, also known as crystal clock frequency. Its existence is exclusive to [the](#)

Skylake generation and newer. The value is specified in Hz, and is normally 24 MHz for client Intel segment, 25 MHz for server Intel segment, and 19.2 MHz for Intel Atom CPUs. macOS till 10.15 inclusive assumes 24 MHz by default.

*Note:* On Intel Skylake X ART frequency may be a little less (approx. 0.25%) than 24 or 25 MHz due to special EMI-reduction circuit as described in Acidanthera Bugtracker.

11. **DevicePathsSupported**  
**Type:** plist integer, 32-bit  
**Failsafe:** Not installed  
**Description:** Sets DevicePathsSupported in gEfiMiscSubClassGuid. Must be set to 1 for AppleACPIPlatform.kext to append SATA device paths to Boot#### and efi-boot-device-data variables. Set to 1 on all modern Macs.
12. **SmcRevision**  
**Type:** plist data, 6 bytes  
**Failsafe:** Not installed  
**Description:** Sets REV in gEfiMiscSubClassGuid. Custom property read by VirtualSMC or FakeSMC to generate SMC REV key.
13. **SmcBranch**  
**Type:** plist data, 8 bytes  
**Failsafe:** Not installed  
**Description:** Sets RBr in gEfiMiscSubClassGuid. Custom property read by VirtualSMC or FakeSMC to generate SMC RBr key.
14. **SmcPlatform**  
**Type:** plist data, 8 bytes  
**Failsafe:** Not installed  
**Description:** Sets RPlt in gEfiMiscSubClassGuid. Custom property read by VirtualSMC or FakeSMC to generate SMC RPlt key.

## 10.4 Memory Properties

1. DataWidth  
Type: plist integer, 16-bit  
Failsafe: 0xFFFF (unknown)  
SMBIOS: Memory Device (Type 17) — Data Width  
Description: Specifies the data width, in bits, of the memory. A DataWidth of 0 and a TotalWidth of 8 indicates that the device is being used solely to provide 8 error-correction bits.
2. Devices  
Type: plist array  
Failsafe: Empty  
Description: Specifies the custom memory devices to be added.  
  
Designed to be filled with plist dictionary values, describing each memory device. See Memory Devices Properties section below. This should include all memory slots, even if unpopulated.
3. ErrorCorrection  
Type: plist integer, 8-bit  
Failsafe: 0x03  
SMBIOS: Physical Memory Array (Type 16) — Memory Error Correction  
Description: Specifies the primary hardware error correction or detection method supported by the memory.
  - 0x01 — Other
  - 0x02 — Unknown
  - 0x03 — None
  - 0x04 — Parity
  - 0x05 — Single-bit ECC
  - 0x06 — Multi-bit ECC
  - 0x07 — CRC

4. FormFactor  
Type: plist integer, 8-bit  
Failsafe: 0x02  
SMBIOS: Memory Device (Type 17) — Form Factor  
Description: Specifies the form factor of the memory. On Macs this should usually be DIMM or SODIMM. Commonly used form factors are listed below.  
When CustomMemory is false, this value is automatically set based on Mac product name.
  - 0x01 — Other
  - 0x02 — Unknown
  - 0x09 — DIMM
  - 0x0D — SODIMM
  - 0x0F — FB-DIMM
5. MaxCapacity  
Type: plist integer, 64-bit  
Failsafe: 0  
SMBIOS: Physical Memory Array (Type 16) — Maximum Capacity  
Description: Specifies the maximum amount of memory, in bytes, supported by the system.
6. TotalWidth  
Type: plist integer, 16-bit  
Failsafe: 0xFFFF (unknown)  
SMBIOS: Memory Device (Type 17) — Total Width  
Description: Specifies the total width, in bits, of the memory, including any check or error-correction bits. If there are no error-correction bits, this value should be equal to DataWidth.
7. Type  
Type: plist integer, 8-bit  
Failsafe: 0x02  
SMBIOS: Memory Device (Type 17) — Memory Type  
Description: Specifies the memory type. Commonly used types are listed below.
  - 0x01 — Other
  - 0x02 — Unknown
  - 0x0F — SDRAM
  - 0x12 — DDR
  - 0x13 — DDR2
  - 0x14 — DDR2 FB-DIMM
  - 0x18 — DDR3
  - 0x1A — DDR4
  - 0x1B — LPDDR
  - 0x1C — LPDDR2
  - 0x1D — LPDDR3
  - 0x1E — LPDDR4
8. TypeDetail  
Type: plist integer, 16-bit  
Failsafe: 0x4  
SMBIOS: Memory Device (Type 17) — Type Detail  
Description: Specifies additional memory type information.
  - Bit 0 — Reserved, set to 0
  - Bit 1 — Other
  - Bit 2 — Unknown
  - Bit 7 — Synchronous
  - Bit 13 — Registered (buffered)
  - Bit 14 — Unbuffered (unregistered)



#### 10.4.1 Memory Device Properties

1. AssetTag  
Type: `plist string`  
Failsafe: `Unknown`  
SMBIOS: Memory Device (Type 17) — Asset Tag  
Description: Specifies the asset tag of this memory device.
2. BankLocator  
Type: `plist string`  
Failsafe: `Unknown`  
SMBIOS: Memory Device (Type 17) — Bank Locator  
Description: Specifies the physically labeled bank where the memory device is located.
3. DeviceLocator  
Type: `plist string`  
Failsafe: `Unknown`  
SMBIOS: Memory Device (Type 17) — Device Locator  
Description: Specifies the physically-labeled socket or board position where the memory device is located.
4. Manufacturer  
Type: `plist string`  
Failsafe: `Unknown`  
SMBIOS: Memory Device (Type 17) — Manufacturer  
Description: Specifies the manufacturer of this memory device.
5. PartNumber  
Type: `plist string`  
Failsafe: `Unknown`  
SMBIOS: Memory Device (Type 17) — Part Number  
Description: Specifies the part number of this memory device.
6. SerialNumber  
Type: `plist string`  
Failsafe: `Unknown`  
SMBIOS: Memory Device (Type 17) — Serial Number  
Description: Specifies the serial number of this memory device.
7. Size  
Type: `plist integer, 32-bit`  
Failsafe: `0`  
SMBIOS: Memory Device (Type 17) — Size  
Description: Specifies the size of the memory device, in megabytes. `0` indicates this slot is not populated.
8. Speed  
Type: `plist integer, 16-bit`  
Failsafe: `0`  
SMBIOS: Memory Device (Type 17) — Speed  
Description: Specifies the maximum capable speed of the device, in megatransfers per second (MT/s). `0` indicates an unknown speed.

#### 10.5 PlatformNVRAM Properties

1. BID  
Type: `plist string`  
Failsafe: `Not installed`  
Description: Specifies the value of NVRAM variable `4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:HW_BID`.
2. ROM  
Type: `plist data, 6 bytes`  
Failsafe: `Not installed`



**Description:** Specifies the values of NVRAM variables 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:HW\_ROM and 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:ROM.

3. MLB

**Type:** plist string

**Failsafe:** Not installed

**Description:** Specifies the values of NVRAM variables 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:HW\_MLB and 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:MLB.

4. FirmwareFeatures

**Type:** plist data, 8 bytes

**Failsafe:** Not installed

**Description:** This variable comes in pair with FirmwareFeaturesMask. Specifies the values of NVRAM variables:

- 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:FirmwareFeatures
- 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:ExtendedFirmwareFeatures

5. FirmwareFeaturesMask

**Type:** plist data, 8 bytes

**Failsafe:** Not installed

**Description:** This variable comes in pair with FirmwareFeatures. Specifies the values of NVRAM variables:

- 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:FirmwareFeaturesMask
- 4D1EDE05-38C7-4A6A-9CC6-4BCCA8B38C14:ExtendedFirmwareFeaturesMask

## 10.6 SMBIOS Properties

1. BIOSVendor

**Type:** plist string

**Failsafe:** OEM specified

**SMBIOS:** BIOS Information (Type 0) — Vendor

**Description:** BIOS Vendor. All rules of SystemManufacturer do apply.

2. BIOSVersion

**Type:** plist string

**Failsafe:** OEM specified

**SMBIOS:** BIOS Information (Type 0) — BIOS Version

**Description:** Firmware version. This value gets updated and takes part in update delivery configuration and macOS version compatibility. This value could look like MM71.88Z.0234.B00.1809171422 in older ~~firmwares~~, firmware and is described in BiosId.h. In newer ~~firmwares~~ firmware, it should look like 236.0.0.0.0 or 220.230.16.0.0 (iBridge: 16.16.2542.0.0,0). iBridge version is read from BridgeOSVersion variable, and is only present on macs with T2.

Apple ROM Version

```
BIOS ID:      MBP151.88Z.F000.B00.1811142212
Model:        MBP151
EFI Version:   220.230.16.0.0
Built by:      root@quinoa
Date:          Wed Nov 14 22:12:53 2018
Revision:      220.230.16 (B&I)
ROM Version:   F000_B00
Build Type:    Official Build, RELEASE
Compiler:      Apple LLVM version 10.0.0 (clang-1000.2.42)
UUID:          E5D1475B-29FF-32BA-8552-682622BA42E1
UUID:          151B0907-10F9-3271-87CD-4BF5DBECACF5
```

3. BIOSReleaseDate

**Type:** plist string

**Failsafe:** OEM specified

**SMBIOS:** BIOS Information (Type 0) — BIOS Release Date

**Description:** Firmware release date. Similar to BIOSVersion. May look like 12/08/2017.

**SMBIOS:** System Enclosure or Chassis (Type 3) — Asset Tag Number

**Description:** Chassis type name. Varies, could be empty or MacBook-Aluminum.

23. PlatformFeature

**Type:** plist integer, 32-bit

**Failsafe:** 0xFFFFFFFF

**SMBIOS:** APPLE\_SMBIOS\_TABLE\_TYPE133 - PlatformFeature

**Description:** Platform features bitmask. Refer to AppleFeatures.h for more details. Use 0xFFFFFFFF value to not provide this table.

24. SmcVersion

**Type:** plist data, 16 bytes

**Failsafe:** All zero

**SMBIOS:** APPLE\_SMBIOS\_TABLE\_TYPE134 - Version

**Description:** ASCII string containing SMC version in upper case. Missing on T2 based Macs. Ignored when zero.

25. FirmwareFeatures

**Type:** plist data, 8 bytes

**Failsafe:** 0

**SMBIOS:** APPLE\_SMBIOS\_TABLE\_TYPE128 - FirmwareFeatures and ExtendedFirmwareFeatures

**Description:** 64-bit firmware features bitmask. Refer to AppleFeatures.h for more details. Lower 32 bits match FirmwareFeatures. Upper 64 bits match ExtendedFirmwareFeatures.

26. FirmwareFeaturesMask

**Type:** plist data, 8 bytes

**Failsafe:** 0

**SMBIOS:** APPLE\_SMBIOS\_TABLE\_TYPE128 - FirmwareFeaturesMask and ExtendedFirmwareFeaturesMask

**Description:** Supported bits of extended firmware features bitmask. Refer to AppleFeatures.h for more details. Lower 32 bits match FirmwareFeaturesMask. Upper 64 bits match ExtendedFirmwareFeaturesMask.

27. ProcessorType

**Type:** plist integer, 16-bit

**Failsafe:** 0 (Automatic)

**SMBIOS:** APPLE\_SMBIOS\_TABLE\_TYPE131 - ProcessorType

**Description:** Combined of Processor Major and Minor types.

Automatic value generation tries to provide most accurate value for the currently installed CPU. When this fails please make sure to create an issue and provide `sysctl machdep.cpu` and `dmidecode` output. For a full list of available values and their limitations (the value will only apply if the CPU core count matches) refer to Apple SMBIOS definitions header here.

28. ~~MemoryFormFactorType: plist integer, 8-bit~~~~Failsafe: OEM specified~~~~SMBIOS: Memory Device (Type 17)~~  
~~— Form Factor~~~~Description: Memory form factor. On Macs it should be DIMM or SODIMM.~~

## 11 UEFI

### 11.1 Introduction

UEFI (Unified Extensible Firmware Interface) is a specification that defines a software interface between an operating system and platform firmware. This section allows to load additional UEFI modules and/or apply tweaks for the onboard firmware. To inspect firmware contents, apply modifications and perform upgrades UEFITool and supplementary utilities can be used.

### 11.2 Drivers

Depending on the firmware a different set of drivers may be required. Loading an incompatible driver may lead the system to unbootable state or even cause permanent firmware damage. Some of the known drivers are listed below:

```
build -a X64 -b RELEASE -t XCODE5 -p MdeModulePkg/MdeModulePkg.dsc
```

---

## 11.3 Tools and Applications

Standalone tools may help to debug firmware and hardware. Some of the known tools are listed below. While some tools can be launched from within OpenCore, see more details in the Tools subsection of the configuration, most should be run separately either directly or from **Shell**.

To boot into OpenShell or any other tool directly save **OpenShell.efi** under the name of **EFI\BOOT\BOOTX64.EFI** on a FAT32 partition. In general it is unimportant whether the partition scheme is GPT or MBR.

While the previous approach works both on Macs and other computers, an alternative Mac-only approach to bless the tool on an HFS+ or APFS volume:

---

```
sudo bless --verbose --file /Volumes/VOLNAME/DIR/OpenShell.efi \
--folder /Volumes/VOLNAME/DIR/ --setBoot
```

---

Listing 3: Blessing tool

*Note 1:* **/System/Library/CoreServices/BridgeVersion.bin** should be copied to **/Volumes/VOLNAME/DIR**.

*Note 2:* To be able to use **bless** disabling System Integrity Protection is necessary.

*Note 3:* To be able to boot Secure Boot might be disabled if present.

Some of the known tools are listed below (builtin tools are marked with \*):

<b>BootKicker*</b>	Enter Apple BootPicker menu (exclusive for Macs with compatible GPUs).
<b>ChipTune*</b>	Test BeepGen protocol and generate audio signals of different style and length.
<b>CleanNvram*</b>	Reset NVRAM alternative bundled as a standalone tool.
<b>GopStop*</b>	Test GraphicsOutput protocol with a simple scenario.
<b>HdaCodecDump*</b>	Parse and dump High Definition Audio codec information (requires <b>AudioDxe</b> ).
<b>KeyTester*</b>	Test keyboard input in <b>SimpleText</b> mode.
<b>MemTest86</b>	Memory testing utility.
<b>OpenControl*</b>	Unlock and lock back NVRAM protection for other tools to be able to get full NVRAM access when launching from OpenCore.
<b>OpenShell*</b>	OpenCore-configured UEFI <b>Shell</b> for compatibility with a broad range of <a href="#">firmwares</a> <a href="#">firmware</a> .
<b>PavpProvision</b>	Perform EPID provisioning (requires certificate data configuration).
<b>ResetSystem*</b>	Utility to perform system reset. Takes reset type as an argument: <b>ColdReset</b> , <b>Firmware</b> , <b>Shutdown</b> , <b>WarmReset</b> . Defaults to <b>ColdReset</b> .
<b>RtcRw*</b>	Utility to read and write RTC (CMOS) memory.
<b>VerifyMsrE2*</b>	Check CFG Lock (MSR 0xE2 write protection) consistency across all cores.

## 11.4 OpenCanopy

OpenCanopy is a graphical OpenCore user interface that runs in **External PickerMode** and relies on **OpenCorePkg** **OcBootManagementLib** similar to the builtin text interface.

OpenCanopy requires graphical resources located in **Resources** directory to run. Sample resources (fonts and images) can be found in **OcBinaryData** repository. Customised icons can be found over the internet (e.g. [here](#) or [there](#)).

OpenCanopy provides full support for **PickerAttributes** and offers a configurable builtin icon set. The default chosen icon set depends on the **DefaultBackgroundColor** variable value. For **Light Gray 01d** icon set will be used, for other colours — the one without a prefix.

Predefined icons are put to **\EFI\OC\Resources\Image** directory. Full list of supported icons (in **.icns** format) is provided below. Missing optional icons will use the closest available icon. External entries will use **Ext**-prefixed icon if available (e.g. **OldExtHardDrive.icns**).

- **Cursor** — Mouse cursor (mandatory).
- **Selected** — Selected item (mandatory).
- **Selector** — Selecting item (mandatory).
- **HardDrive** — Generic OS (mandatory).
- **Apple** — Apple OS.

- **AppleRecv** — Apple Recovery OS.
- **AppleTM** — Apple Time Machine.
- **Windows** — Windows.
- **Other** — Custom entry (see **Entries**).
- **ResetNVRAM** — Reset NVRAM system action or tool.
- **Shell** — Entry with UEFI Shell name (e.g. **OpenShell**).
- **Tool** — Any other tool.

Predefined labels are put to `\EFI\OC\Resources\Label` directory. Each label has `.1b1` or `.12x` suffix to represent the scaling level. Full list of labels is provided below. All labels are mandatory.

- **EFIBoot** — Generic OS.
- **Apple** — Apple OS.
- **AppleRecv** — Apple Recovery OS.
- **AppleTM** — Apple Time Machine.
- **Windows** — Windows.
- **Other** — Custom entry (see **Entries**).
- **ResetNVRAM** — Reset NVRAM system action or tool.
- **Shell** — Entry with UEFI Shell name (e.g. **OpenShell**).
- **Tool** — Any other tool.

Label and icon generation can be performed with bundled utilities: **disklabel** and **icnspack**. Please refer to sample data for the details about the dimensions. Font is Helvetica 12 pt times scale factor.

Font format corresponds to AngelCode binary BMF. While there are many utilities to generate font files, currently it is recommended to use **dpFontBaker** to generate bitmap font (using **CoreText** produces best results) and **fconverter** to export it to binary format.

## 11.5 OpenRuntime

**OpenRuntime** is an OpenCore plugin implementing **OC\_FIRMWARE\_RUNTIME** protocol. This protocol implements multiple features required for OpenCore that are otherwise not possible to implement in OpenCore itself as they are needed to work in runtime, i.e. during operating system functioning. Feature highlights:

- NVRAM namespaces, allowing to isolate operating systems from accessing select variables (e.g. **RequestBootVarRouting** or **ProtectSecureBoot**).
- Read-only and write-only NVRAM variables, enhancing the security of OpenCore, Lilu, and Lilu plugins, ~~like~~ [such as](#) **VirtualSMC**, which implements **AuthRestart** support.
- NVRAM isolation, allowing to protect all variables from being written from an untrusted operating system (e.g. **DisableVariableWrite**).
- UEFI Runtime Services memory protection management to workaround read-only mapping (e.g. **EnableWriteUnprotector**).

## 11.6 Properties

### 1. APFS

**Type:** plist dict

**Failsafe:** None

**Description:** Provide APFS support as configured in APFS Properties section below.

### 2. Audio

**Type:** plist dict

**Failsafe:** None

**Description:** Configure audio backend support described in Audio Properties section below.

Audio support provides a way for upstream protocols to interact with the selected hardware and audio resources. All audio resources should reside in `\EFI\OC\Resources\Audio` directory. Currently the only supported audio file format is WAVE PCM. While it is driver-dependent which audio stream format is supported, most common audio cards support 16-bit signed stereo audio at 44100 or 48000 Hz.

Audio file path is determined by audio type, audio localisation, and audio path. Each filename looks as follows: [audio type]\_[audio localisation]\_[audio path].wav. For unlocalised files filename does not include the language code and looks as follows: [audio type]\_[audio path].wav.

- Audio type can be `OCEFIAudio` for OpenCore audio files or `AXEFIAudio` for macOS bootloader audio files.
- Audio localisation is a two letter language code (e.g. `en`) with an exception for Chinese, Spanish, and Portuguese. Refer to `APPLE_VOICE_OVER_LANGUAGE_CODE` definition for the list of all supported localisations.
- Audio path is the base filename corresponding to a file identifier. For macOS bootloader audio paths refer to `APPLE_VOICE_OVER_AUDIO_FILE` definition. For OpenCore audio paths refer to `OC_VOICE_OVER_AUDIO_FILE` definition. The only exception is OpenCore boot chime file, which is `OCEFIAudio_VoiceOver_Boot.wav`.

Audio localisation is determined separately for macOS bootloader and OpenCore. For macOS bootloader it is set in `preferences.efi` archive in `systemLanguage.utf8` file and is controlled by the operating system. For OpenCore the value of `prev-lang:kbd` variable is used. When native audio localisation of a particular file is missing, English language (`en`) localisation is used. Sample audio files can be found in `OcBinaryData` repository.

### 3. ConnectDrivers

**Type:** plist boolean

**Failsafe:** false

**Description:** Perform UEFI controller connection after driver loading.

This option is useful for loading drivers following UEFI driver model as they may not start by themselves. Examples of such drivers are filesystem or audio drivers. While effective, this option may not be necessary for drivers performing automatic connection, and may slightly slowdown the boot.

*Note:* Some [firmwares, types of firmware, particularly those](#) made by Apple [in particular](#), only connect the boot drive to speed up the boot process. Enable this option to be able to see all the boot options when [having running](#) multiple drives.

### 4. Drivers

**Type:** plist array

**Failsafe:** None

**Description:** Load selected drivers from `OC/Drivers` directory.

Designed to be filled with string filenames meant to be loaded as UEFI drivers.

### 5. Input

**Type:** plist dict

**Failsafe:** None

**Description:** Apply individual settings designed for input (keyboard and mouse) in Input Properties section below.

### 6. Output

**Type:** plist dict

**Failsafe:** None

**Description:** Apply individual settings designed for output (text and graphics) in Output Properties section below.

### 7. ProtocolOverrides

**Type:** plist dict

**Failsafe:** None

**Description:** Force builtin versions of select protocols described in ProtocolOverrides Properties section below.

*Note:* all protocol instances are installed prior to driver loading.

### 8. Quirks

**Type:** plist dict

**Failsafe:** None

**Description:** Apply individual firmware quirks described in Quirks Properties section below.

### 9. ReservedMemory

**Type:** plist array

**Description:** Designed to be filled with `plist dict` values, describing memory areas exquisite to particular firmware and hardware functioning, which should not be used by the operating system. An example of such memory

region could be second 256 MB corrupted by Intel HD 3000 or an area with faulty RAM. See ReservedMemory Properties section below.

## 11.7 APFS Properties

### 1. EnableJumpstart

**Type:** plist boolean

**Failsafe:** false

**Description:** Load embedded APFS drivers from APFS containers.

APFS EFI driver is bundled in all bootable APFS containers. This option performs loading of signed APFS drivers with respect to ScanPolicy. See more details in “EFI Jumpstart” section of Apple File System Reference.

### 2. GlobalConnect

**Type:** plist boolean

**Failsafe:** false

**Description:** Perform full device connection during APFS loading.

Instead of partition handle connection normally used for APFS driver loading every handle is connected recursively. This may take more time than usual but can be the only way to access APFS partitions on some ~~firmwares like those found~~ types of firmware such as those on older HP laptops.

### 3. HideVerbose

**Type:** plist boolean

**Failsafe:** false

**Description:** Hide verbose output from APFS driver.

APFS verbose output can be useful for debugging.

### 4. JumpstartHotPlug

**Type:** plist boolean

**Failsafe:** false

**Description:** Load APFS drivers for newly connected devices.

Performs APFS driver loading not only at OpenCore startup but also during boot picker. This permits APFS USB hot plug. Disable if not required.

### 5. MinDate

**Type:** plist integer

**Failsafe:** 0

**Description:** Minimal allowed APFS driver date.

APFS driver date connects APFS driver with the calendar release date. Older versions of APFS drivers may contain unpatched vulnerabilities, which can be used to inflict harm to the computer. This option permits restricting APFS drivers to only recent releases.

- 0 — require the default supported release date of APFS in OpenCore. The default release date will increase with time and thus this setting is recommended. Currently set to 2018/06/21.
- -1 — permit any release date to load (strongly discouraged).
- Other — use custom minimal APFS release date, e.g. 20200401 for 2020/04/01. APFS release dates can be found in OpenCore boot log and OcApfsLib.

### 6. MinVersion

**Type:** plist integer

**Failsafe:** 0

**Description:** Minimal allowed APFS driver version.

APFS driver version connects APFS driver with the macOS release. APFS drivers from older macOS releases will become unsupported and thus may contain unpatched vulnerabilities, which can be used to inflict harm to the computer. This option permits restricting APFS drivers to only modern macOS versions.

- 0 — require the default supported version of APFS in OpenCore. The default version will increase with time and thus this setting is recommended. Currently set to the latest point release from High Sierra from App Store (748077008000000).

#### 6. PlayChime

**Type:** plist boolean

**Failsafe:** false

**Description:** Play chime sound at startup.

Enabling this setting plays boot chime through builtin audio support. Volume level is determined by `MinimumVolume` and `VolumeAmplifier` settings and `SystemAudioVolume` NVRAM variable.

*Note:* this setting is separate from `StartupMute` NVRAM variable to avoid conflicts when the firmware is able to play boot chime.

#### 7. VolumeAmplifier

**Type:** plist integer

**Failsafe:** 0

**Description:** Multiplication coefficient for system volume to raw volume linear translation from 0 to 1000.

Volume level range read from `SystemAudioVolume` varies depending on the codec. To transform read value in [0, 127] range into raw volume range [0, 100] the read value is scaled to `VolumeAmplifier` percents:

$$RawVolume = MIN(\frac{SystemAudioVolume * VolumeAmplifier}{100}, 100)$$

*Note:* the transformation used in macOS is not linear, but it is very close and this nuance is thus ignored.

## 11.9 Input Properties

#### 1. KeyFiltering

**Type:** plist boolean

**Failsafe:** false

**Description:** Enable keyboard input sanity checking.

Apparently some boards ~~like~~ such as the GA Z77P-D3 may return uninitialised data in `EFI_INPUT_KEY` with all input protocols. This option discards keys that are neither ASCII, nor are defined in the UEFI specification (see tables 107 and 108 in version 2.8).

#### 2. KeyForgetThreshold

**Type:** plist integer

**Failsafe:** 0

**Description:** Remove key unless it was submitted during this timeout in milliseconds.

`AppleKeyMapAggregator` protocol is supposed to contain a fixed length buffer of currently pressed keys. However, the majority of the drivers only report key presses as interrupts and pressing and holding the key on the keyboard results in subsequent submissions of this key with some defined time interval. As a result we use a timeout to remove once pressed keys from the buffer once the timeout expires and no new submission of this key happened.

This option allows to set this timeout based on the platform. The recommended value that works on the majority of the platforms is 5 milliseconds. For reference, holding one key on VMware will repeat it roughly every 2 milliseconds and the same value for APTIO V is 3–4 milliseconds. Thus it is possible to set a slightly lower value on faster platforms and slightly higher value on slower platforms for more responsive input.

*Note:* Some platforms may require different values, higher or lower. For example, when detecting key misses in OpenCanopy try increasing this value (e.g. to 10), and when detecting key stall, try decreasing this value. Since every platform is different it may be reasonable to check every value from 1 to 25.

#### 3. KeyMergeThreshold

**Type:** plist integer

**Failsafe:** 0

**Description:** Assume simultaneous combination for keys submitted within this timeout in milliseconds.

Similarly to `KeyForgetThreshold`, this option works around the sequential nature of key submission. To be able to recognise simultaneously pressed keys in the situation when all keys arrive sequentially, we are required to set a timeout within which we assume the keys were pressed together.

Holding multiple keys results in reports every 2 and 1 milliseconds for VMware and APTIO V respectively. Pressing keys one after the other results in delays of at least 6 and 10 milliseconds for the same platforms. The



Currently two renderers are supported: `Builtin` and `System`. `System` renderer uses firmware services for text rendering. `Builtin` bypassing firmware services and performs text rendering on its own. Different renderers support a different set of options. It is recommended to use `Builtin` renderer, as it supports HiDPI mode and uses full screen resolution.

UEFI ~~firmwares generally support~~ firmware generally supports `ConsoleControl` with two rendering modes: `Graphics` and `Text`. Some ~~firmwares~~ types of firmware do not support `ConsoleControl` and rendering modes. OpenCore and macOS expect text to only be shown in `Graphics` mode and graphics to be drawn in any mode. Since this is not required by UEFI specification, exact behaviour varies.

Valid values are combinations of text renderer and rendering mode:

- `BuiltinGraphics` — Switch to `Graphics` mode and use `Builtin` renderer with custom `ConsoleControl`.
- `BuiltinText` — Switch to `Text` mode and use `Builtin` renderer with custom `ConsoleControl`.
- `SystemGraphics` — Switch to `Graphics` mode and use `System` renderer with custom `ConsoleControl`.
- `SystemText` — Switch to `Text` mode and use `System` renderer with custom `ConsoleControl`.
- `SystemGeneric` — Use `System` renderer with system `ConsoleControl` assuming it behaves correctly.

The use of `BuiltinGraphics` is generally straightforward. For most platforms it is necessary to enable `ProvideConsoleGop`, set `Resolution` to `Max`. `BuiltinText` variant is an alternative `BuiltinGraphics` for some very old and buggy laptop ~~firmwares~~ firmware, which can only draw in `Text` mode.

The use of `System` protocols is more complicated. In general the preferred setting is `SystemGraphics` or `SystemText`. Enabling `ProvideConsoleGop`, setting `Resolution` to `Max`, enabling `ReplaceTabWithSpace` is useful on almost all platforms. `SanitiseClearScreen`, `IgnoreTextInGraphics`, and `ClearScreenOnModeSwitch` are more specific, and their use depends on the firmware.

*Note:* Some Macs, namely `MacPro5,1`, may have broken console output with newer GPUs, and thus only `BuiltinGraphics` may work for them.

## 2. `ConsoleMode`

**Type:** plist string

**Failsafe:** Empty string

**Description:** Sets console output mode as specified with the `WxH` (e.g. `80x24`) formatted string.

Set to empty string not to change console mode. Set to `Max` to try to use largest available console mode. Currently `Builtin` text renderer supports only one console mode, so this option is ignored.

*Note:* This field is best ~~to be~~ left empty on most ~~firmwares~~ types of firmware.

## 3. `Resolution`

**Type:** plist string

**Failsafe:** Empty string

**Description:** Sets console output screen resolution.

- Set to `WxH@Bpp` (e.g. `1920x1080@32`) or `WxH` (e.g. `1920x1080`) formatted string to request custom resolution from GOP if available.
- Set to empty string not to change screen resolution.
- Set to `Max` to try to use largest available screen resolution.

On HiDPI screens `APPLE_VENDOR_VARIABLE_GUID UIScale` NVRAM variable may need to be set to `02` to enable HiDPI scaling in `Builtin` text renderer, FileVault 2 UEFI password interface, and boot screen logo. Refer to Recommended Variables section for more details.

*Note:* This will fail when console handle has no GOP protocol. When the firmware does not provide it, it can be added with `ProvideConsoleGop` set to `true`.

## 4. `ClearScreenOnModeSwitch`

**Type:** plist boolean

**Failsafe:** false

**Description:** Some ~~firmwares clear only part of~~ types of firmware only clear part of the screen when switching from graphics to text mode, leaving a fragment of previously drawn ~~image~~ images visible. This option fills the entire graphics screen with black colour before switching to text mode.

*Note:* This option only applies to `System` renderer.

5. DirectGopRendering

**Type:** plist boolean

**Failsafe:** false

**Description:** Use builtin graphics output protocol renderer for console.

On some ~~firmwares~~ types of firmware, such as on the MacPro5,1, this may provide better performance or ~~even~~ fix rendering issues, ~~like on MacPro5,1~~. However, ~~it is recommended not to use this option~~ this option is not recommended unless there is an obvious benefit as it may ~~even result in~~ result in issues such as slower scrolling.

6. IgnoreTextInGraphics

**Type:** plist boolean

**Failsafe:** false

**Description:** ~~Select firmwares~~ Some types of firmware output text onscreen in both graphics and text mode. This is ~~normally unexpected, because~~ typically unexpected as random text may appear over graphical images and cause UI corruption. Setting this option to **true** will discard all text output when console control is in ~~mode~~ different a different mode from Text.

*Note:* This option only applies to the System renderer.

7. ReplaceTabWithSpace

**Type:** plist boolean

**Failsafe:** false

**Description:** Some ~~firmwares~~ types of firmware do not print tab characters or ~~even~~ everything that follows them, causing difficulties ~~or inability to use in using~~ the UEFI Shell's builtin text editor to edit property lists and other documents. This option makes the console output spaces instead of tabs.

*Note:* This option only applies to System renderer.

8. ProvideConsoleGop

**Type:** plist boolean

**Failsafe:** false

**Description:** Ensure GOP (Graphics Output Protocol) on console handle.

macOS bootloader requires GOP or UGA (for 10.4 EfiBoot) to be present on console handle, yet the exact location of the graphics protocol is not covered by the UEFI specification. This option will ensure GOP and UGA, if present, are available on the console handle.

*Note:* This option will also replace broken GOP protocol on console handle, which may be the case on MacPro5,1 with newer GPUs.

9. ReconnectOnResChange

**Type:** plist boolean

**Failsafe:** false

**Description:** Reconnect console controllers after changing screen resolution.

On some ~~firmwares when screen resolution is changed via GOP, it is required to reconnect the controllers, which~~ types of firmware, the controllers that produce the console protocols (simple text out) must be reconnected when the screen resolution is changed via GOP. Otherwise they will not produce text based on the new resolution.

*Note:* On several boards this logic may result in black screen when launching OpenCore from Shell and thus it is optional. In versions prior to 0.5.2 this option was mandatory and not configurable. Please do not use this unless required.

10. SanitiseClearScreen

**Type:** plist boolean

**Failsafe:** false

**Description:** Some ~~firmwares reset screen resolution~~ types of firmware reset screen resolutions to a failsafe value (~~like such as~~ 1024x768) on the attempts to clear screen contents when large display (e.g. 2K or 4K) is used. This option attempts to apply a workaround.

*Note:* This option only applies to System renderer. On all known affected systems ConsoleMode had to be set to empty string for this to work.

#### 11. UgaPassThrough

**Type:** plist boolean

**Failsafe:** false

**Description:** Provide UGA protocol instances on top of GOP protocol.

Some ~~firmwares~~ types of firmware do not implement the legacy UGA protocol ~~, but it~~ but this may be required for screen output by older EFI applications like such as EfiBoot from 10.4.

### 11.11 ProtocolOverrides Properties

#### 1. AppleAudio

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple audio protocols with builtin versions.

Apple audio protocols allow macOS bootloader and OpenCore to play sounds and signals for screen reading or audible error reporting. Supported protocols are beep generation and VoiceOver. VoiceOver protocol is specific to Gibraltar machines (T2) and is not supported before macOS High Sierra (10.13). Instead older macOS versions use AppleHDA protocol, which is currently not implemented.

Only one set of audio protocols can be available at a time, so in order to get audio playback in OpenCore user interface on Mac system implementing some of these protocols this setting should be enabled.

*Note:* Backend audio driver needs to be configured in UEFI Audio section for these protocols to be able to stream audio.

#### 2. AppleBootPolicy

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple Boot Policy protocol with a builtin version. This may be used to ensure APFS compatibility on VMs or legacy Macs.

*Note:* Some Macs, namely MacPro5,1, do have APFS compatibility, but their Apple Boot Policy protocol contains recovery detection issues, thus using this option is advised on them as well.

#### 3. AppleDebugLog

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple Debug Log protocol with a builtin version.

#### 4. AppleEvent

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple Event protocol with a builtin version. This may be used to ensure File Vault 2 compatibility on VMs or legacy Macs.

#### 5. AppleFramebufferInfo

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple Framebuffer Info protocol with a builtin version. This may be used to override framebuffer information on VMs or legacy Macs to improve compatibility with legacy EfiBoot like such as the one in macOS 10.4.

#### 6. AppleImageConversion

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple Image Conversion protocol with a builtin version.

#### 7. AppleImg4Verification

**Type:** plist boolean

**Failsafe:** false

**Description:** Reinstalls Apple IMG4 Verification protocol with a builtin version. This protocol is used to verify im4m manifest files used by Apple Secure Boot.

**Description:** Forcibly reinstalls OS Info protocol with builtin versions. This protocol is generally used to receive notifications from macOS bootloader, by the firmware or by other applications.

#### 18. UnicodeCollation

**Type:** plist boolean

**Failsafe:** false

**Description:** Forcibly reinstalls unicode collation services with builtin version. Should be set to **true** to ensure UEFI Shell compatibility on platforms providing broken unicode collation. In general legacy Insyde and APTIO platforms on Ivy Bridge and earlier are affected.

## 11.12 Quirks Properties

#### 1. DeduplicateBootOrder

**Type:** plist boolean

**Failsafe:** false

**Description:** Remove duplicate entries in **BootOrder** variable in **EFI\_GLOBAL\_VARIABLE\_GUID**.

This quirk requires **RequestBootVarRouting** to be enabled and therefore **OC\_FIRMWARE\_RUNTIME** protocol implemented in **OpenRuntime.efi**.

By redirecting **Boot** prefixed variables to a separate GUID namespace with the help of **RequestBootVarRouting** quirk we achieve multiple goals:

- Operating systems are jailed and only controlled by OpenCore boot environment to enhance security.
- Operating systems do not mess with OpenCore boot priority, and guarantee fluent updates and hibernation wakes for cases that require reboots with OpenCore in the middle.
- Potentially incompatible boot entries, such as macOS entries, are not deleted or anyhow corrupted.

However, some ~~firmwares~~ types of firmware do their own boot option scanning ~~upon on~~ startup by checking for file presence on the available disks. ~~Quite often this scanning~~ This scanning often includes non-standard locations, such as Windows Bootloader paths. ~~Normally it is~~ This is typically not an issue, ~~but some firmwares, ASUS firmwares on APTIO V in particular~~ but some firmware, such as ASUS firmware on the APTIO V, have bugs. ~~For them~~ On such, scanning is implemented improperly, and firmware preferences may get accidentally corrupted due to **BootOrder** entry duplication (each option will be added twice) making it impossible to boot without resetting NVRAM.

To trigger the bug, some valid boot options (e.g. OpenCore) are required. Then install Windows with **RequestBootVarRouting** enabled. As the Windows bootloader option will not be created by the Windows installer, the firmware will attempt to create this itself, leading to a corruption of its boot option list.

This quirk removes all duplicates in **BootOrder** variable attempting to resolve the consequences of the bugs upon OpenCore loading. It is recommended to use this key along with **BootProtect** option.

#### 2. ExitBootServicesDelay

**Type:** plist integer

**Failsafe:** 0

**Description:** Adds delay in microseconds after **EXIT\_BOOT\_SERVICES** event.

This is a very ~~ugly quirk to circumvent~~ "Still waiting for root device" message on select APTIO IV firmwares, namely rough workaround to circumvent the Still waiting for root device message on some APTIO IV firmware (ASUS Z87-Pro, ~~) particularly~~ when using FileVault 2 in particular. It seems 2. It appears that for some reason, they execute code in parallel to **EXIT\_BOOT\_SERVICES**, which results in the SATA controller being inaccessible from macOS. A better approach should be found in some future. Expect ~~3-5~~ 3 to 5 seconds to be ~~enough in case the~~ adequate when this quirk is needed.

#### 3. IgnoreInvalidFlexRatio

**Type:** plist boolean

**Failsafe:** false

**Description:** ~~Select firmwares, namely APTIO IV~~ Some types of firmware (such as APTIO IV) may contain invalid values in the **MSR\_FLEX\_RATIO** (0x194) MSR register. These values may cause macOS boot ~~failure~~ failures on Intel platforms.

*Note:* While the option is not ~~supposed to induce harm on unaffected firmwares, its usage is not expected to harm unaffected firmware, its use is only~~ recommended when it is ~~not specifically~~ required.

#### 4. ReleaseUsbOwnership

**Type:** plist boolean

**Failsafe:** false

**Description:** Attempt to detach USB controller ownership from the firmware driver. While most ~~firmwares manage to properly do that~~ types of firmware manage to do that properly, or at least have an option for ~~, select firmwares this, some~~ do not. As a result, the operating system may freeze upon boot. Not recommended unless required.

#### 5. RequestBootVarRouting

**Type:** plist boolean

**Failsafe:** false

**Description:** Request redirect of all Boot prefixed variables from EFI\_GLOBAL\_VARIABLE\_GUID to OC\_VENDOR\_VARIABLE\_GUID.

This quirk requires OC\_FIRMWARE\_RUNTIME protocol implemented in `OpenRuntime.efi`. The quirk lets default boot entry preservation at times when ~~firmwares delete the firmware deletes~~ incompatible boot entries. ~~Simply said~~ In summary, this quirk is required to reliably use the Startup Disk preference pane in firmware that is not compatible with macOS boot entries by design.

#### 6. TscSyncTimeout

**Type:** plist integer

**Failsafe:** 0

**Description:** Attempts to perform TSC synchronisation with a specified timeout.

The primary purpose of this quirk is to enable early bootstrap TSC synchronisation on some server and laptop models when running a debug XNU kernel. For the debug kernel the TSC needs to be kept in sync across the cores before any kext could kick in rendering all other solutions problematic. The timeout is specified in microseconds and depends on the amount of cores present on the platform, the recommended starting value is 500000.

This is an experimental quirk, which should only be used for the aforementioned problem. In all other cases the quirk may render the operating system unstable and is not recommended. The recommended solution in the other cases is to install a kernel driver ~~like such as~~ VoodooTSCSync, TSCAdjustReset, or CpuTscSync (a more specialised variant of VoodooTSCSync for newer laptops).

*Note:* The reason this quirk cannot replace the kernel driver is because it cannot operate in ACPI S3 mode (sleep wake) and because the UEFI ~~firmwares provide~~ firmware provides very limited multicore support preventing the precise update of the MSR registers.

#### 7. UnblockFsConnect

**Type:** plist boolean

**Failsafe:** false

**Description:** Some ~~firmwares types of firmware~~ block partition handles by opening them in ~~By Driver mode, which results in File System protocols~~ By Driver mode, resulting in being unable to install File System protocols.

*Note:* The quirk is mostly relevant for select HP laptops with no drives listed.

## 11.13 ReservedMemory Properties

#### 1. Address

**Type:** plist integer

**Failsafe:** 0

**Description:** Start address of the reserved memory region, which should be allocated as reserved effectively marking the memory of this type inaccessible to the operating system.

The addresses written here must be part of the memory map, have `EfiConventionalMemory` type, and page-aligned (4 KBs).

*Note:* Some ~~firmwares types of firmware~~ may not allocate memory areas used by S3 (sleep) and S4 (hibernation) code unless CSM is enabled causing wake failures. After comparing the memory maps with CSM disabled and

## 12 Troubleshooting

### 12.1 Legacy Apple OS

Older operating systems may be more complicated to install, but sometimes can be necessary to use for all kinds of reasons. While a compatible board identifier and CPUID are the obvious requirements for proper functioning of an older operating system, there are many other less obvious things to consider. This section tries to cover a common set of issues relevant to installing older macOS operating systems.

While newer operating systems can be downloaded over the internet, older operating systems did not have installation media for every minor release, so to get a compatible distribution one may have to download a device-specific image and mod it if necessary. To get the list of the bundled device-specific builds for legacy operating systems one can visit this archived Apple Support article. Since it is not always accurate, the latest versions are listed below.

#### 12.1.1 macOS 10.8 and 10.9

- Disk images on these systems use Apple Partitioning Scheme and will require the proprietary `PartitionDxe` driver to run DMG recovery and installation. It is possible to set `DmgLoading` to `Disabled` to run the recovery without DMG loading avoiding the need for `PartitionDxe`.
- Cached kernel images often do not contain family drivers for networking (`IONetworkingFamily`) or audio (`IOAudioFamily`) requiring the use of `Force` loading in order to inject networking or audio drivers.

#### 12.1.2 macOS 10.7

- All previous issues apply.
- SSSE3 support (not to be confused with SSE3 support) is a hard requirement for macOS 10.7 kernel.
- Many kexts, including Lilu when 32-bit kernel is used and a lot of Lilu plugins, are unsupported on macOS 10.7 and older as they require newer kernel APIs, which are not part of the macOS 10.7 SDK.
- Prior to macOS 10.8 KASLR sliding is not supported, which will result in memory allocation failures on [firmwares](#) [firmware](#) that utilise lower memory for their own purposes. Refer to [acidanthera/bugtracker#1125](#) for tracking.

#### 12.1.3 macOS 10.6

- All previous issues apply.
- SSSE3 support is a requirement for macOS 10.6 kernel with 64-bit userspace enabled. This limitation can mostly be lifted by enabling the `LegacyCommpage` quirk.
- Last released installer images for macOS 10.6 are macOS 10.6.7 builds 10J3250 (for MacBookPro8,x) and 10J4139 (for iMac12,x), without Xcode). These images are limited to their target model identifiers and have no `-no_compat_check` boot argument support. Modified images (with ACDT suffix) without model restrictions can be found [here](#), assuming macOS 10.6 is legally owned. Read `DIGEST.txt` for more details. Note that these are the earliest tested versions of macOS 10.6 with OpenCore.

Model checking may also be erased by editing `OSInstall.mpkg` with e.g. Flat Package Editor by making `Distribution` script to always return `true` in `hwbeModelCheck` function. Since updating the only file in the image and not corrupting other files can be difficult and may cause slow booting due to kernel cache date changes, it is recommended to script image rebuilding as shown below:

---

```
#!/bin/bash
# Original.dmg is original image, OSInstall.mpkg is patched package
mkdir R0
hdiutil mount Original.dmg -noverify -noautoopen -noautoopenrw -noautofsck -mountpoint R0
cp R0/.DS_Store DS_STORE
hdiutil detach R0 -force
rm -rf R0
hdiutil convert Original.dmg -format UDRW -o ReadWrite.dmg
mkdir RW
xattr -c OSInstall.mpkg
```



```
hdiutil mount ReadWrite.dmg -noverify -noautoopen -noautoopenrw -noautofsck -mountpoint RW
cp OSInstall.mpkg RW/System/Installation/Packages/OSInstall.mpkg
killall Finder fsevents
rm -rf RW/.fsevents
cp DS_STORE RW/.DS_Store
hdiutil detach RW -force
rm -rf DS_STORE RW
hdiutil convert ReadWrite.dmg -format UDZO -o ReadOnly.dmg
```

---

#### 12.1.4 macOS 10.5

- All previous issues apply.
- This macOS version does not support x86\_64 kernel and requires i386 kernel extensions and patches.
- This macOS version uses the first (V1) version of `prelinkedkernel`, which has kext symbol tables corrupted by the kext tools. This nuance renders `prelinkedkernel` kext injection impossible in OpenCore. `Mkext` kext injection will still work without noticeable performance drain and will be chosen automatically when `KernelCache` is set to `Auto`.
- Last released installer image for macOS 10.5 is macOS 10.5.7 build 9J3050 (for MacBookPro5,3). Unlike the others, this image is not limited to the target model identifiers and can be used as is. The original 9J3050 image can be found [here](#), assuming macOS 10.5 is legally owned. Read `DIGEST.txt` for more details. Note that this is the earliest tested version of macOS 10.5 with OpenCore.

#### 12.1.5 macOS 10.4

- All previous issues apply.
- This macOS version has a hard requirement to access all the optional packages on the second DVD disk installation media, requiring either two disks or USB media installation.
- Last released installer images for macOS 10.4 are macOS 10.4.10 builds 8R4061a (for MacBookPro3,1) and 8R4088 (for iMac7,1). These images are limited to their target model identifiers ~~just like the~~ [as on](#) newer macOS versions. Modified 8R4088 images (with ACDT suffix) without model restrictions can be found [here](#), assuming macOS 10.4 is legally owned. Read `DIGEST.txt` for more details. Note that these are the earliest tested versions of macOS 10.4 with OpenCore.

## 12.2 UEFI Secure Boot

OpenCore is designed to provide a secure boot chain between firmware and operating system. On most x86 platforms trusted loading is implemented via UEFI Secure Boot model. Not only OpenCore fully supports this model, but it also extends its capabilities to ensure sealed configuration via vaulting and provide trusted loading to the operating systems using custom verification, such as Apple Secure Boot. Proper secure boot chain requires several steps and careful configuration of select settings as explained below:

1. Enable Apple Secure Boot by setting `SecureBootModel` to run macOS. Note, that not every macOS is compatible with Apple Secure Boot and there are several other restrictions as explained in Apple Secure Boot section.
2. Disable DMG loading by setting `DmgLoading` to `Disabled` if users have concerns of loading old vulnerable DMG recoveries. This is **not** required, but recommended. For the actual tradeoffs see the details in DMG loading section.
3. Make sure that APFS JumpStart functionality restricts the loading of old vulnerable drivers by setting `MinDate` and `MinVersion` to 0. More details are provided in APFS JumpStart section. An alternative is to install `apfs.efi` driver manually.
4. Make sure that `Force` driver loading is not needed and all the operating systems are still bootable.
5. Make sure that `ScanPolicy` restricts loading from undesired devices. It is a good idea to prohibit all removable drivers or unknown filesystems.



6. Sign all the installed drivers and tools with the private key. Do not sign tools that provide administrative access to the computer, ~~like such as~~ UEFI Shell.
7. Vault the configuration as explained Vaulting section.
8. Sign all OpenCore binaries (`BOOTX64.efi`, `BOOTIa32.efi`, `Bootstrap.efi`, `OpenCore.efi`) used on this system with the same private key.
9. Sign all third-party operating system (not made by Microsoft or Apple) bootloaders if needed. For Linux there is an option to install Microsoft-signed Shim bootloader as explained on e.g. Debian Wiki.
10. Enable UEFI Secure Boot in firmware preferences and install the certificate with a private key. Details on how to generate a certificate can be found in various articles, ~~like such as~~ this one, and are out of the scope of this document. If Windows is needed one will also need to add the Microsoft Windows Production CA 2011. To launch option ROMs or to use signed Linux drivers, Microsoft UEFI Driver Signing CA will also be needed.
11. Password-protect changing firmware settings to ensure that UEFI Secure Boot cannot be disabled without the user's knowledge.

## 12.3 Windows support

### Can I install Windows?

While no official Windows support is provided, 64-bit UEFI Windows installations (Windows 8 and above) prepared with Boot Camp are supposed to work. Third-party UEFI installations as well as systems partially supporting UEFI boot, ~~like such as~~ Windows 7, might work with some extra precautions. Things to consider:

- MBR (Master Boot Record) installations are legacy and will not be supported.
- All the modifications applied (to ACPI, NVRAM, SMBIOS, etc.) are supposed to be operating system agnostic, i.e. apply equally regardless of the OS booted. This enables Boot Camp software experience on Windows.
- macOS requires the first partition to be EFI System Partition, and does not support the default Windows layout. While OpenCore does have a workaround for this, it is highly recommend not to rely on it and install properly.
- Windows may need to be reactivated. To avoid it consider setting SystemUUID to the original firmware UUID. Be ~~warned, on old firmwares aware that~~ it may be invalid ~~on old firmware~~, i.e., not random. If there still are issues, consider using HWID or KMS38 license or making the use `Custom UpdateSMBIOSMode`. Other nuances of Windows activation are out of the scope of this document and can be found online.

### What additional software do I need?

To enable operating system switching and install relevant drivers in the majority of cases Windows support software from Boot Camp is required. For simplicity of the download process or when configuring an already installed Windows version a third-party utility, Brigadier, can be used successfully. Note, that 7-Zip may be downloaded and installed prior to using Brigadier.

Remember to always use the latest version of Windows support software from Boot Camp, as versions prior to 6.1 do not support APFS, and thus will not function correctly. To download newest software pass most recent Mac model to Brigadier, for example `./brigadier.exe -m iMac19,1`. To install Boot Camp on an unsupported Mac model afterwards run PowerShell as Administrator and enter `msiexec /i BootCamp.msi`. If there is a previous version of Boot Camp installed it should be removed first by running `msiexec /x BootCamp.msi` command. `BootCamp.msi` file is located in `BootCamp/Drivers/Apple` directory and can be reached through Windows Explorer.

While Windows support software from Boot Camp solves most of compatibility problems, the rest may still have to be addressed manually:

- To invert mouse wheel scroll direction `FlipFlopWheel` must be set to 1 as explained on SuperUser.
- `RealTimeIsUniversal` must be set to 1 to avoid time desync between Windows and macOS as explained on SuperUser (this is usually not needed).
- To access Apple filesystems ~~like HFS and APFS~~ ~~such as HFS+ and APFS~~, separate software may need to be installed. Some of the known utilities are: Apple HFS+ driver (hack for Windows 10), HFSExplorer, MacDrive, Paragon APFS, Paragon HFS+, TransMac, etc. Remember to never ever attempt to modify Apple file systems from Windows as this often leads to irrecoverable data loss.

*Note:* On several motherboards (and possibly USB UART dongles) PIN naming may be incorrect. It is very common to have GND swapped with RX, thus, motherboard “TX” must be connected to USB UART GND, and motherboard “GND” to USB UART RX.

Remember to enable COM port in firmware settings, and never use USB cables longer than 1 meter to avoid output corruption. To additionally enable XNU kernel serial output `debug=0x8` boot argument is needed.

## 12.5 Tips and Tricks

### 1. How to debug boot failure?

Normally it is enough to obtain the actual error message. For this ensure that:

- A DEBUG or NOOPT version of OpenCore is used.
- Logging is enabled (1) and shown onscreen (2): Misc → Debug → Target = 3.
- Logged messages from at least DEBUG\_ERROR (0x80000000), DEBUG\_WARN (0x00000002), and DEBUG\_INFO (0x00000040) levels are visible onscreen: Misc → Debug → DisplayLevel = 0x80000042.
- Critical error messages, ~~like such as~~ DEBUG\_ERROR, stop booting: Misc → Security → HaltLevel = 0x80000000.
- Watch Dog is disabled to prevent automatic reboot: Misc → Debug → DisableWatchDog = true.
- Boot Picker (entry selector) is enabled: Misc → Boot → ShowPicker = true.

If there is no obvious error, check the available hacks in Quirks sections one by one. For early boot troubleshooting, for instance, when OpenCore menu does not appear, using UEFI Shell (bundled with OpenCore) may help to see early debug messages.

### 2. How to debug macOS boot failure?

- Refer to boot-args values ~~like such as~~ debug=0x100, keepsyms=1, -v, and similar.
- Do not forget about AppleDebug and ApplePanic properties.
- Take care of Booter, Kernel, and UEFI quirks.
- Consider using serial port to inspect early kernel boot failures. For this debug=0x108, serial=5, and msgbuf=1048576 boot arguments are needed. Refer to the patches in Sample.plist when dying before serial init.
- Always read the logs carefully.

### 3. How to customise boot entries?

OpenCore follows standard Apple Bless model and extracts the entry name from .contentDetails and .disk\_label.contentDetails files in the booter directory if present. These files contain an ASCII string with an entry title, which may then be customised by the user.

### 4. How to choose the default boot entry?

OpenCore uses the primary UEFI boot option to select the default entry. This choice can be altered from UEFI Setup, with the macOS Startup Disk preference, or the Windows Boot Camp Control Panel. Since choosing OpenCore’s BOOTx64.EFI as a primary boot option limits this functionality in addition to several ~~firmwares types~~ of firmware deleting incompatible boot options, potentially including those created by macOS, users are strongly encouraged to use the RequestBootVarRouting quirk, which will preserve the selection made in the operating system within the OpenCore variable space. Note, that RequestBootVarRouting requires a separate driver for functioning.

### 5. What is the simplest way to install macOS?

Copy online recovery image (\*.dmg and \*.chunklist files) to com.apple.recovery.boot directory on a FAT32 partition with OpenCore. Load OpenCore Boot Picker and choose the entry, it will have a (dmg) suffix. Custom name may be created by providing .contentDetails file.

To download recovery online macrecovery.py can be used.

For offline installation refer to How to create a bootable installer for macOS article. Apart from App Store and softwareupdate utility there also are third-party utilities to download an offline image.

### 6. Why do online recovery images (\*.dmg) fail to load?

This may be caused by missing HFS+ driver, as all presently known recovery volumes have HFS+ filesystem.

## 7. Can I use this on Apple hardware or virtual machines?

Sure, most relatively modern Mac models including MacPro5,1 and virtual machines are fully supported. Even though there are little to none specific details relevant to Mac hardware, some ongoing instructions can be found on MacRumors.com.

## 8. Why do Find&Replace patches must equal in length?

For machine code (x86 code) it is not possible to do differently sized replacements due to relative addressing. For ACPI code this is risky, and is technically equivalent to ACPI table replacement, thus not implemented. More detailed explanation can be found on AppleLife.ru or in the ACPI section of this document.

## 9. How can I decide which Booter quirks to use?

These quirks originate from AptioMemoryFix driver but provide a wider set of changes specific to modern systems. Note, that OpenRuntime driver is required for most configurations. To get a configuration similar to AptioMemoryFix the following set of quirks should be enabled:

- ProvideConsoleGop (UEFI quirk)
- AvoidRuntimeDefrag
- DiscardHibernateMap
- EnableSafeModeSlide
- EnableWriteUnprotector
- ForceExitBootServices
- ProtectMemoryRegions
- ProvideCustomSlide
- RebuildAppleMemoryMap
- SetupVirtualMap

However, as of today, such set is strongly discouraged as some of these quirks are not necessary to be enabled or need additional quirks. For example, DevirtualiseMmio and ProtectUefiServices are often required, while DiscardHibernateMap and ForceExitBootServices are rarely necessary.

Unfortunately for some quirks ~~like such as~~ RebuildAppleMemoryMap, EnableWriteUnprotector, ProtectMemoryRegions, SetupVirtualMap, and SyncRuntimePermissions there is no definite approach even on similar systems, so trying all their combinations may be required for optimal setup. Refer to individual quirk descriptions in this document for more details.