

Abstract Machines

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Big O

We all know that MERGESORT has $\mathcal{O}(n \log n)$ time complexity, and that BUBBLESORT has $\mathcal{O}(n^2)$ time complexity, but what does that actually mean?

Big O Notation

Given functions $f,g:\mathbb{R}\to\mathbb{R}$, $f\in\mathcal{O}(g)$ if and only if there exists a value $x_0\in\mathbb{R}$ and a coefficient m such that:

$$\forall x > x_0. \ f(x) \leq m \cdot g(x)$$

What is the codomain of f?

When analysing algorithms, we don't usually time how long they take to run on a real machine.

Big O

Q: How would you derive the complexity of this mergesort?

```
 \begin{array}{lll} \mathtt{mergesort}([]) = [] & f(0) = c_1 \\ \mathtt{mergesort}(xs) = & f(n) = \\ & \mathtt{let}(ys,zs) = \mathtt{partition}\ xs; & c_2 * n + \\ & ys' = \mathtt{mergesort}\ ys; & f(n/2) + \\ & zs' = \mathtt{mergesort}\ zs & f(n/2) + \\ & \mathtt{in}\ \mathtt{merge}\ ys'\ zs' & c_3 * n \end{array}
```

A: Define a cost function f, then find its closed form.

Q: Is there a formal connection between mergesort and f, or did we just pull f out of thin air?

A: Well, um.

Cost Models

A *cost model* is a mathematical model that measures the cost of executing a program.

There are *denotational* cost models, that assign a cost directly to syntax:

 $\llbracket \cdot \rrbracket : \operatorname{Program} \to \operatorname{Cost}$

In this course, we will focus on *operational cost models*.

Operational Cost Models

First, we define a program-evaluating *abstract machine*. We determine the time cost by counting the number of steps it takes.

Abstract Machines

Abstract Machines

An abstract machine consists of:

- **1** A set of states Σ ,
- **2** A set of initial states $I \subseteq \Sigma$,
- **3** A set of final states $F \subseteq \Sigma$, and
- **4** A transition relation $\mapsto \subseteq \Sigma \times \Sigma$.

We've seen this before in structured operational (or small-step) semantics.

The M Machine

Is just our usual small-step rules:

$$\frac{e_1 \mapsto_M e_1'}{(\operatorname{Plus}\ e_1\ e_2) \mapsto_M (\operatorname{Plus}\ e_1'\ e_2)} \cdot \cdot \cdot$$

$$\frac{e_1 \mapsto_M e_1'}{(\operatorname{If}\ e_1\ e_2\ e_3) \mapsto_M (\operatorname{If}\ e_1'\ e_2\ e_3)}$$

$$\overline{(\operatorname{If}\ (\operatorname{Lit}\ \operatorname{True})\ e_2\ e_3) \mapsto_M e_2} \quad \overline{(\operatorname{If}\ (\operatorname{Lit}\ \operatorname{False})\ e_2\ e_3) \mapsto_M e_3}$$

$$\frac{e_1 \mapsto_M e_1'}{(\operatorname{Apply}\ e_1\ e_2) \mapsto_M (\operatorname{Apply}\ e_1'\ e_2)}$$

$$\frac{e_2 \mapsto_M e_2'}{(\operatorname{Apply}\ (\operatorname{Recfun}\ (f.x.\ e))\ e_2) \mapsto_M (\operatorname{Apply}\ (\operatorname{Recfun}\ (f.x.\ e))\ e_2')}$$

$$v \in F$$

$$\overline{(\operatorname{Apply}\ (\operatorname{Recfun}\ (f.x.\ e))\ v) \mapsto_M e[x := v, f := (\operatorname{Recfun}\ (f.x.\ e))]}$$

The M Machine is unsuitable as a basis for a cost model. Why?

Performance

One step in our machine should always only be $\mathcal{O}(1)$ in our language implementation. Otherwise, counting steps will not get an accurate description of the time cost.

This makes for two potential problems:

- **Substitution** occurs in function application, which is potentially $\mathcal{O}(n)$ time.
- Control Flow is not explicit which subexpression to reduce is found by recursively descending the abstract syntax tree each time.

. . .

The C Machine

We want to define a machine where all the rules are axioms, so there can be no recursive descent into subexpressions. How is recursion typically implemented?

Stacks!
$$\frac{f \text{ Frame } s \text{ Stack}}{\circ \text{ Stack}} \frac{f \text{ Frame } s \text{ Stack}}{f \triangleright s \text{ Stack}}$$

Key Idea: States will consist of a current expression to evaluate and a stack of computational contexts that situate it in the overall computation. An example stack would be:

(Plus 3
$$\square$$
) \triangleright (Times \square (Num 2)) \triangleright \circ

This represents the computational context:

(Times (Plus
$$3 \square$$
) (Num 2))

The C Machine

Our states will consist of two modes:

- **1** Evaluate the current expression within stack s, written $s \succ e$.
- **2 Return** a value v (either a function, integer, or boolean) back into the context in s, written $s \prec v$.

Initial states start evaluation with an empty stack, i.e. $\circ \succ e$. Final states return a value to the empty stack, i.e. $\circ \prec v$.

Stack frames are expressions with holes or values in them:

e ₂ Expr	v_1 Value
(Plus \square e_2) Frame	(Plus $v_1 \square$) Frame

. . .

Evaluating

There are three axioms about Plus now:

When evaluating a Plus expression, first evaluate the LHS:

$$\overline{s \succ (\text{Plus } e_1 \ e_2)} \quad \mapsto_{\mathcal{C}} \quad (\text{Plus } \square \ e_2) \triangleright s \succ e_1$$

Once the LHS is evaluated, switch to the RHS:

$$(\text{Plus } \square \ e_2) \triangleright s \prec v_1 \quad \mapsto_C \quad (\text{Plus } v_1 \ \square) \triangleright s \succ e_2$$

Once the RHS is evaluated, return the sum:

$$\overline{(\text{Plus } v_1 \ \Box) \triangleright s \prec v_2 \quad \mapsto_{\mathcal{C}} \quad s \prec v_1 + v_2}$$

We also have a single rule about Num that just returns the value:

$$\overline{s \succ (\operatorname{Num} n) \mapsto_C s \prec n}$$

Example

```
\circ \succ (Plus (Plus (Num 2) (Num 3)) (Num 4))
\mapsto_{\mathcal{C}} (Plus \square (Num 4)) \triangleright \circ \succ (Plus (Num 2) (Num 3))
\mapsto_{\mathcal{C}} (Plus \square (Num 3)) \triangleright (Plus \square (Num 4)) \triangleright \circ \succ (Num 2)
\mapsto_{\mathcal{C}} (Plus \square (Num 3)) \triangleright (Plus \square (Num 4)) \triangleright \circ \prec 2
\mapsto_{\mathcal{C}} (Plus 2 \square) \triangleright (Plus \square (Num 4)) \triangleright \circ \succ (Num 3)
\mapsto_{\mathcal{C}} (Plus 2 \square) \triangleright (Plus \square (Num 4)) \triangleright \circ \prec 3
\mapsto_{\mathcal{C}} (Plus \square (Num 4)) \triangleright \circ \prec 5
\mapsto_{\mathcal{C}} (Plus 5 \square) \triangleright \circ \succ (Num 4)
\mapsto_{\mathcal{C}} (Plus 5 \square) \triangleright \circ \prec 4
\mapsto c \circ \prec 9
```

Other Rules

We have similar rules for the other operators and for booleans. For If:

$$s \succ (\text{If } e_1 \ e_2 \ e_3) \quad \mapsto_{\mathcal{C}} \quad (\text{If } \square \ e_2 \ e_3) \triangleright s \succ e_1$$

Functions

Recfun (here abbreviated to Fun) evaluates to a function value:

$$\overline{s \succ (\operatorname{Fun}(f.x.\ e)) \ \mapsto_{C} \ s \prec \langle\!\langle f.x.\ e \rangle\!\rangle}$$

Function application is then handled similarly to Plus.

$$s \succ (\texttt{Apply } e_1 \ e_2) \quad \mapsto_{C} \quad (\texttt{Apply } \Box \ e_2) \triangleright s \succ e_1$$

$$(\operatorname{Apply} \ \Box \ e_2) \triangleright s \prec \langle \langle f.x. \ e \rangle \rangle \quad \mapsto_C \quad (\operatorname{Apply} \ \langle \langle f.x. \ e \rangle \rangle \ \Box) \triangleright s \succ e_2$$

$$(\operatorname{Apply} \langle \langle f.x. \ e \rangle \rangle \ \Box) \triangleright s \prec v \quad \mapsto_{C} \quad s \prec e[x := v, f := (\operatorname{Fun} \ (f.x.e))]$$

We are still using substitution for now.

What have we done?

- All the rules are axioms we can now implement the evaluator with a simple while loop (or a *tail recursive* function).
- We have a lower-level specification helps with code generation (e.g. in an assembly language)
- Substitution is still a machine operation we need to find a way to eliminate that.

Correctness

While the M-Machine is reasonably straightforward definition of the language's semantics, the C-Machine is much more detailed.

We wish to prove a theorem that tells us that the C-Machine behaves analogously to the M-Machine.

Refinement

A low-level (concrete) semantics of a program is a refinement of a high-level (abstract) semantics if every possible execution in the low-level semantics has a corresponding execution in the high-level semantics. In our case:

$$\forall e, v. \frac{\circ \succ e \quad \stackrel{\star}{\mapsto}_{C} \quad \circ \prec v}{e \quad \stackrel{\star}{\mapsto}_{M} \quad v}$$

Functional correctness properties are preserved by refinement, but security properties are not.

How to Prove Refinement

We can't get away with simply proving that each C machine step has a corresponding step in the M-Machine, because the C-Machine makes multiple steps that are no-ops in the M-Machine:

$$\circ \succ (+ (+ (N 2) (N 3)) (N 4)) \qquad (+ (+ (N 2) (N 3)) (N 4))$$

$$\mapsto_{C} (+ \square (N 4)) \triangleright \circ \succ (+ (N 2) (N 3))$$

$$\mapsto_{C} (+ \square (N 3)) \triangleright (+ \square (N 4)) \triangleright \circ \succ (N 2)$$

$$\mapsto_{C} (+ \square (N 3)) \triangleright (+ \square (N 4)) \triangleright \circ \sim 2$$

$$\mapsto_{C} (+ 2 \square) \triangleright (+ \square (N 4)) \triangleright \circ \succ (N 3)$$

$$\mapsto_{C} (+ 2 \square) \triangleright (+ \square (N 4)) \triangleright \circ \sim 3$$

$$\mapsto_{C} (+ \square (N 4)) \triangleright \circ \sim 5 \qquad \mapsto_{M} (+ (N 5) (N 4))$$

$$\mapsto_{C} (+ 5 \square) \triangleright \circ \succ (N 4)$$

$$\mapsto_{C} (+ 5 \square) \triangleright \circ \sim 4$$

$$\mapsto_{C} \circ \sim 9 \qquad \mapsto_{M} (N 9)$$

How to Prove Refinement

- Define an abstraction function $\mathcal{A}:\Sigma_C\to\Sigma_M$ that relates C-Machine states to M-Machine states, describing how they "correspond".
- **2** Prove, for all initial states $\sigma \in I_C$, that the corresponding state $A(\sigma) \in I_M$.
- **3** Prove for each step in the C-Machine $\sigma_1 \mapsto_C \sigma_2$, either:
 - the step is a no-op in the M-Machine and $\mathcal{A}(\sigma_1)=\mathcal{A}(\sigma_2)$, or
 - the step is replicated by the M-Machine $\mathcal{A}(\sigma_1) \mapsto_M \mathcal{A}(\sigma_2)$.
- **1** Prove, for all final states $\sigma \in F_C$, that $\mathcal{A}(\sigma) \in F_M$.

In general this abstraction function is called a *simulation relation* and this type of proof is called a *simulation* proof.

The Abstraction Function

Our abstraction function \mathcal{A} will need to relate states such that each transition that corresponds to a no-op in the M-Machine will move between \mathcal{A} -equivalent states:

```
\circ \succ (+ (+ (N 2) (N 3)) (N 4)) -
                                                                                                                       (+ (+ (N 2) (N 3)) (N 4))
\mapsto_{\mathcal{C}} (+ \square (N 4)) \triangleright \circ \succ (+ (N 2) (N 3)) \longrightarrow
\mapsto_{\mathcal{C}} (+ \square (N 3)) \triangleright (+ \square (N 4)) \triangleright \circ \succ (N 2)
\mapsto_{\mathcal{C}} (+ \square (N 3)) \triangleright (+ \square (N 4)) \triangleright \circ \prec 2 \sim
\mapsto_{\mathcal{C}} (+ 2 \square) \triangleright (+ \square (N 4)) \triangleright \circ \succ (N 3) \rightarrow
\mapsto_{\mathcal{C}} (+2\square)\triangleright(+\square(N4))\triangleright\circ\prec 3-
\mapsto_{\mathcal{C}} (+ \square (N 4)) \triangleright \circ \prec 5
                                                                                                                  \rightarrow (+ (N 5) (N 4))
\mapsto_{\mathcal{C}} (+5 \square) \triangleright \circ \succ (N 4)
\mapsto_{\mathcal{C}} (+5 \square) \triangleright \circ \prec 4
\mapsto c \circ \prec 9
```

Abstraction Function

Given a C-Machine state with a stack and a current expression (or value), we reconstruct the overall expression to get the corresponding M-Machine state.

$$\mathcal{A}(\circ \succ e) = e$$
 $\mathcal{A}(\circ \prec v) = (\text{Num } v)$
 $\mathcal{A}((\text{Plus } \square \ e_2) \triangleright s \succ e_1) = \mathcal{A}(s \succ (\text{Plus } e_1 \ e_2))$
etc.

By definition, all the initial/final states of the C-Machine are mapped to initial/final states of the M-Machine. So all that is left is the requirement for each transition.

Showing Refinement for Plus

```
s \succ (\text{Plus } e_1 \ e_2) \mapsto_{C} (\text{Plus } \square \ e_2) \triangleright s \succ e_1
```

This is a no-op in the M-Machine:

$$\begin{array}{rcl} \mathcal{A}(\textit{RHS}) & = & \mathcal{A}((\textit{Plus} \ \Box \ e_2) \, \triangleright \, s \, \succ \, e_1) \\ & = & \mathcal{A}(s \, \succ \, (\textit{Plus} \ e_1 \ e_2)) \\ & = & \mathcal{A}(\textit{LHS}) \end{array}$$

Showing Refinement for Plus

```
(\text{Plus} \square e_2) \triangleright s \prec v_1 \quad \mapsto_{\mathcal{C}} \quad (\text{Plus} \ v_1 \ \square) \triangleright s \succ e_2
```

Another no-op in the M-Machine:

```
\mathcal{A}(LHS) = \mathcal{A}((\text{Plus} \square e_2) \triangleright s \prec v_1)
= \mathcal{A}(s \succ (\text{Plus} (\text{Num } v_1) e_2))
= \mathcal{A}((\text{Plus } v_1 \square) \triangleright s \succ e_2)
= \mathcal{A}(RHS)
```

Showing Refinement for Plus

$$(Plus \ v_1 \ \Box) \triangleright s \prec v_2 \quad \mapsto_{\mathcal{C}} \quad s \prec v_1 + v_2$$

This corresponds to a M-Machine transition:

$$\mathcal{A}(LHS) = \mathcal{A}((\text{Plus } v_1 \square) \triangleright s \prec v_2)$$

$$= \mathcal{A}(s \succ (\text{Plus } (\text{Num } v_1) (\text{Num } v_2)))$$

$$\mapsto_{M} \mathcal{A}(s \succ (\text{Num } (v_1 + v_2)))$$

$$= \mathcal{A}(s \prec v_1 + v_2)$$

$$= \mathcal{A}(RHS)$$
(*)

Technically the reduction step (*) requires induction on the stack.